

View from Top side (Scale 1:1)

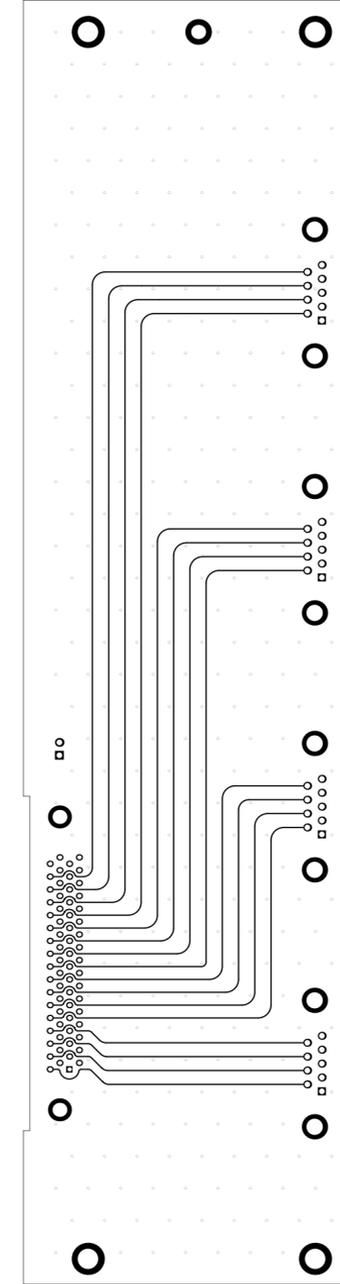
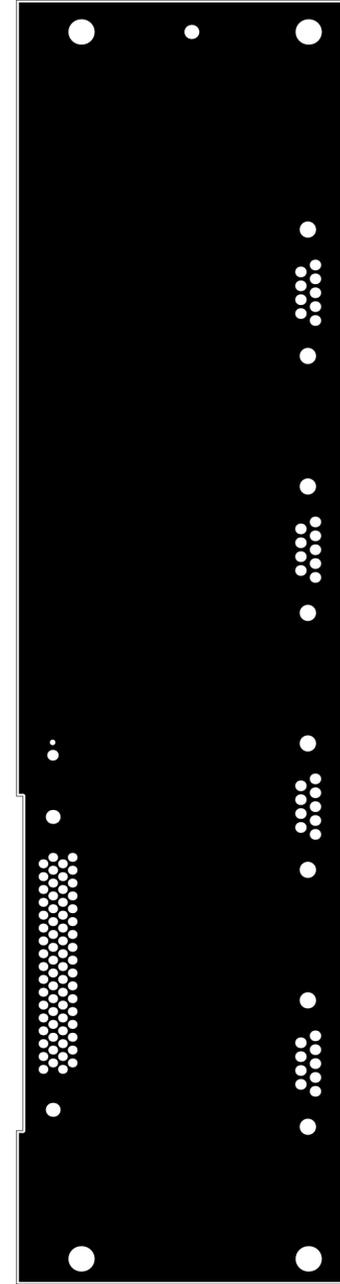
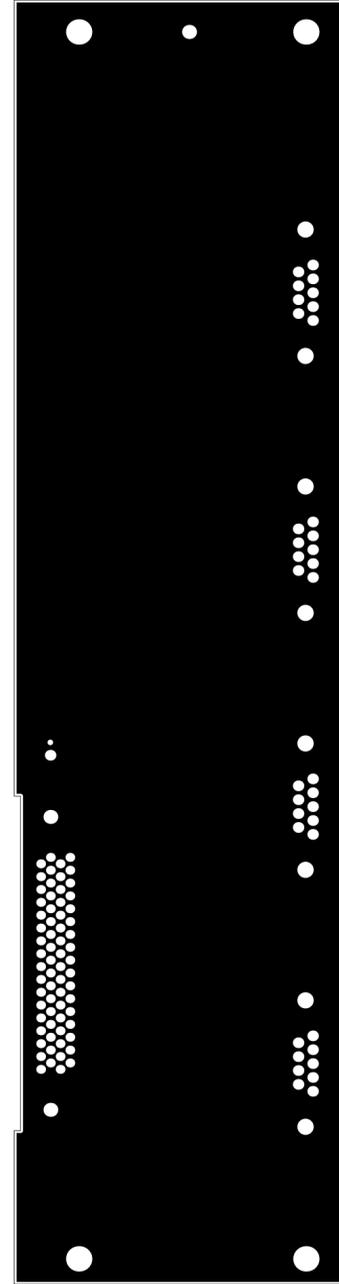
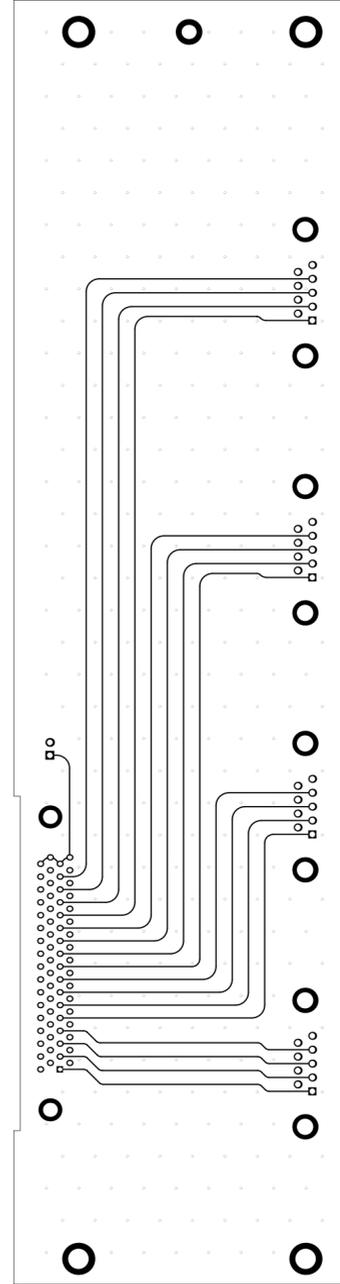
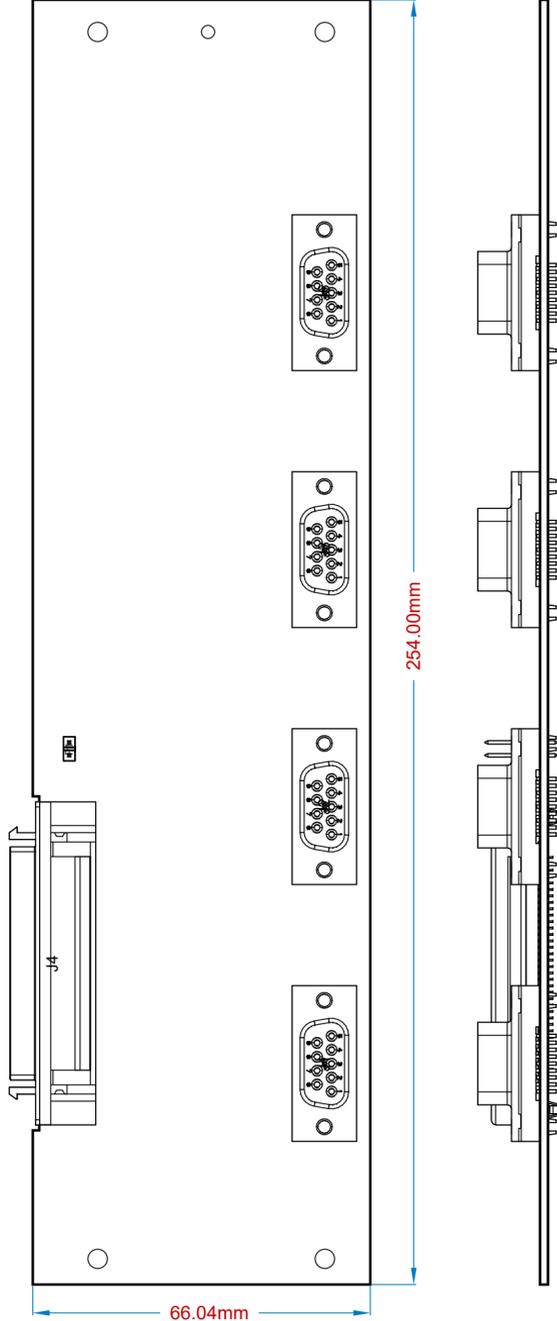
View from Right side (Scale 1:1)

Top Layer (Scale 1:1)

GND (Scale 1:1)

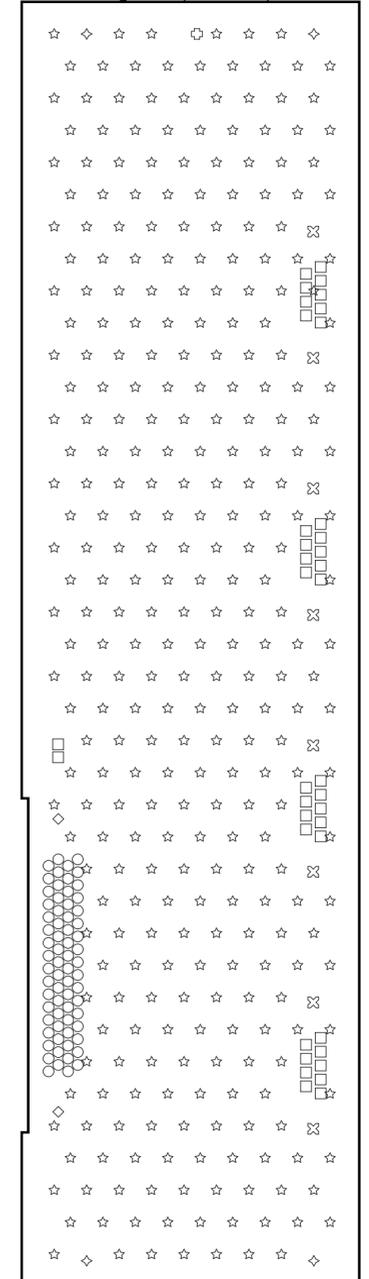
GND (Scale 1:1)

Bottom Layer (Scale 1:1)



Symbol	Count	Hole Size	Plated	Hole Tolerance
☆	323	15.00mil	Plated	None
○	68	30.00mil	Plated	None
□	38	43.31mil	Plated	None
◇	2	109.06mil	Plated	None
⊕	1	110.00mil	Plated	None
⊗	8	125.98mil	Plated	None
◇	4	157.48mil	Plated	None
444 Total				

Drill Drawing View (Scale 1:1)



Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Paste			Paste Mask	GTP
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.40mil	Solder Resist	Solder Mask	GTS
Copper	Top Layer	1.40mil		Signal	GTL
Prepreg		7.50mil	FR-4	Dielectric	
Copper	GND	1.40mil		Internal Plane	GP1
Prepreg		40.60mil	FR-4	Dielectric	
Copper	GND	1.40mil		Internal Plane	GP2
Prepreg		7.50mil	FR-4	Dielectric	
Copper	Bottom Layer	1.40mil		Signal	GBL
Surface Material	Bottom Solder	0.40mil	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
	Bottom Paste			Paste Mask	GBP

Total thickness: 62.00mil

Bill Of Materials

Line #	Designator	Comment	Quantity
1	J1, J2, J3, J5	D9F	4
2	J4	787082-7	1
3	P1	Header 2	1

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		UNLESS OTHERWISE SPECIFIED:		NAME	DATE		
		DIMENSIONS ARE IN INCHES		DRAWN	Dean Schaezel	10/16/2019	TITLE
		TOLERANCES:		CHECKED			A+ DAC AI Interface Board
		FRACTIONAL ±		ENG APPR.			
		ANGULAR: MACH ± BEND ±		MFG APPR.			
		TWO PLACE DECIMAL ±		Q.A.			SIZE DWG. NO.
		THREE PLACE DECIMAL ±		COMMENTS:			D070101v2
		INTERPRET GEOMETRIC TOLERANCING PER:					SCALE: 1:1 WEIGHT: SHEET 1 OF 1
		MATERIAL					
NEXT ASSY		USED ON		FINISH			
APPLICATION				DO NOT SCALE DRAWING			