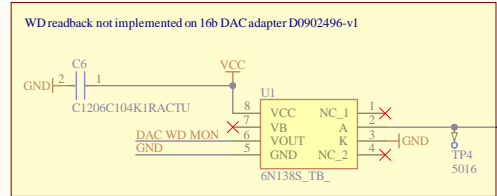
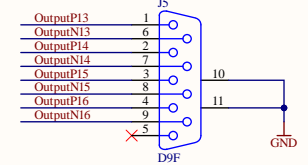
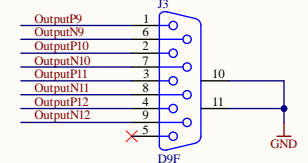
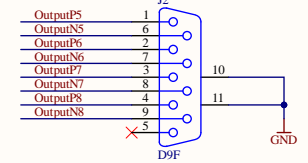
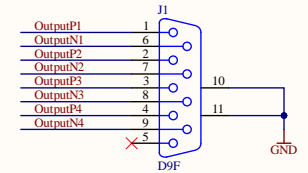
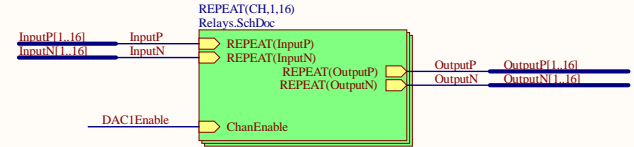
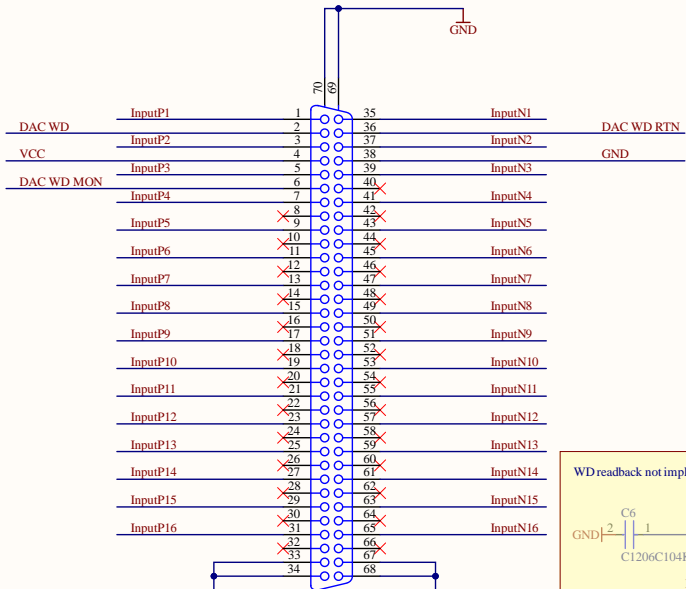


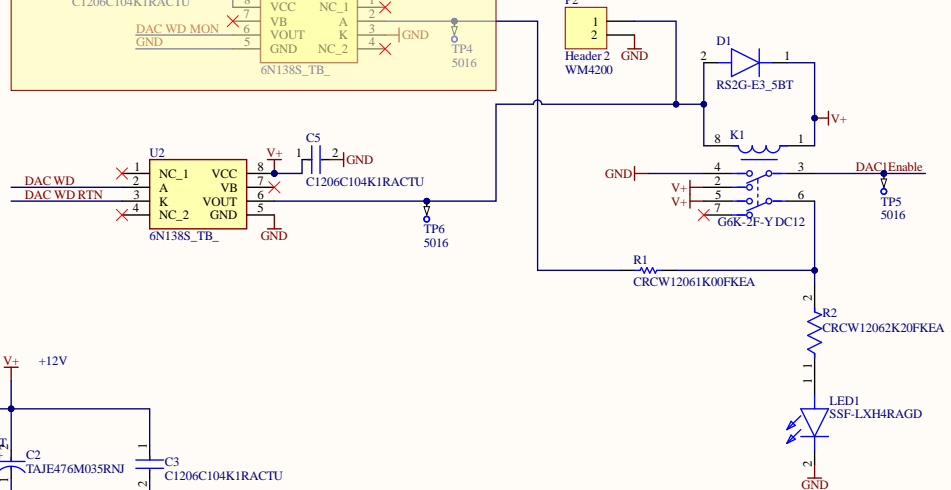
Last Edited: 5/10/2023

Title		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
16bit DAC AI Rear PCB					
Size: B	DCCNumber: D070101	Revision: v4	Engineer: Michael Laxen	Date: 5/9/2023	
File: Relays.SchDoc				Time: 7:22:59 PM	
				Sheet 1 of 2	

Inputs from DAC Adapter



Local On-Board WD Override



Nominal connection is grounded

v2 Updated the BOM for A+
v3 Corrected labeling
v4 added relays to implement WD function as done with 18/20b DACs

Title		Last Edited: 5/10/2023	
16bit DAC AI Rear PCB			
LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology			
Size: B	DCCNumber: D070101	Revision: v4	Date: 5/9/2023
File: D070101-v4 16bit_DAC_AI_Rear_PCB.SchDoc		Engineer: Michael Laxen	Time: 7:22:59 PM
			Sheet 1 of 2