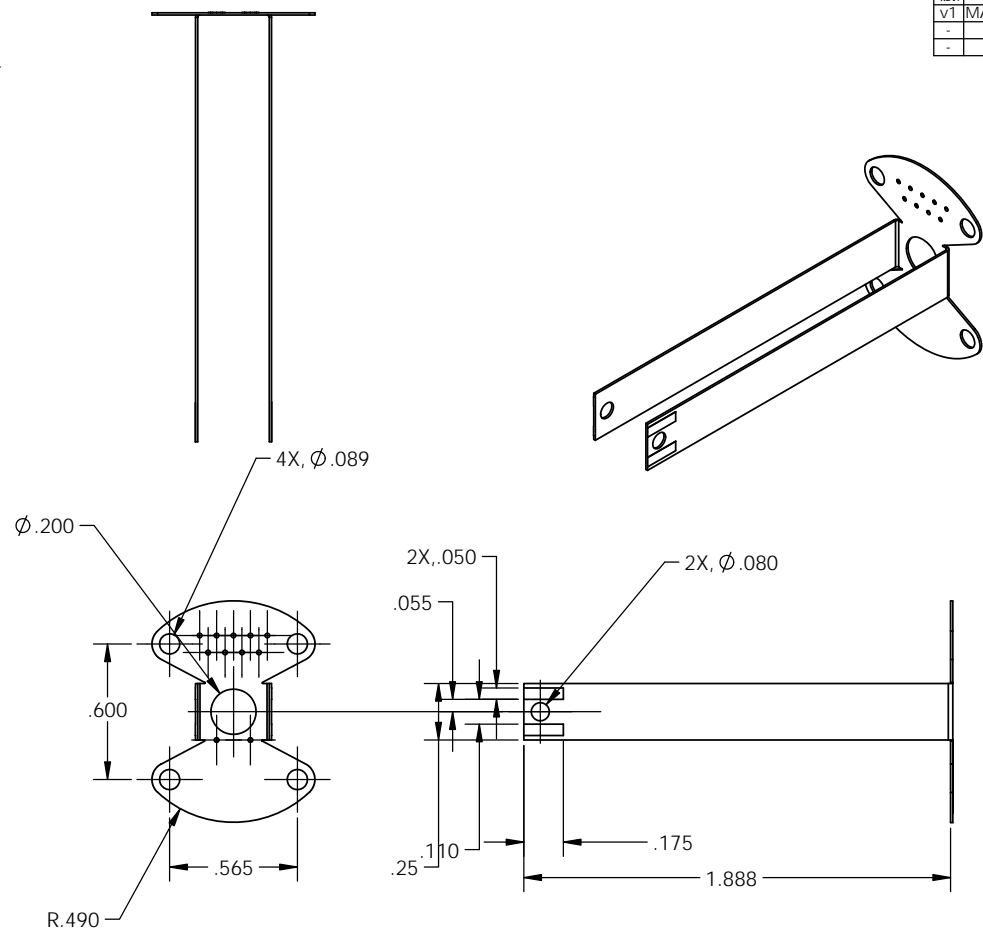


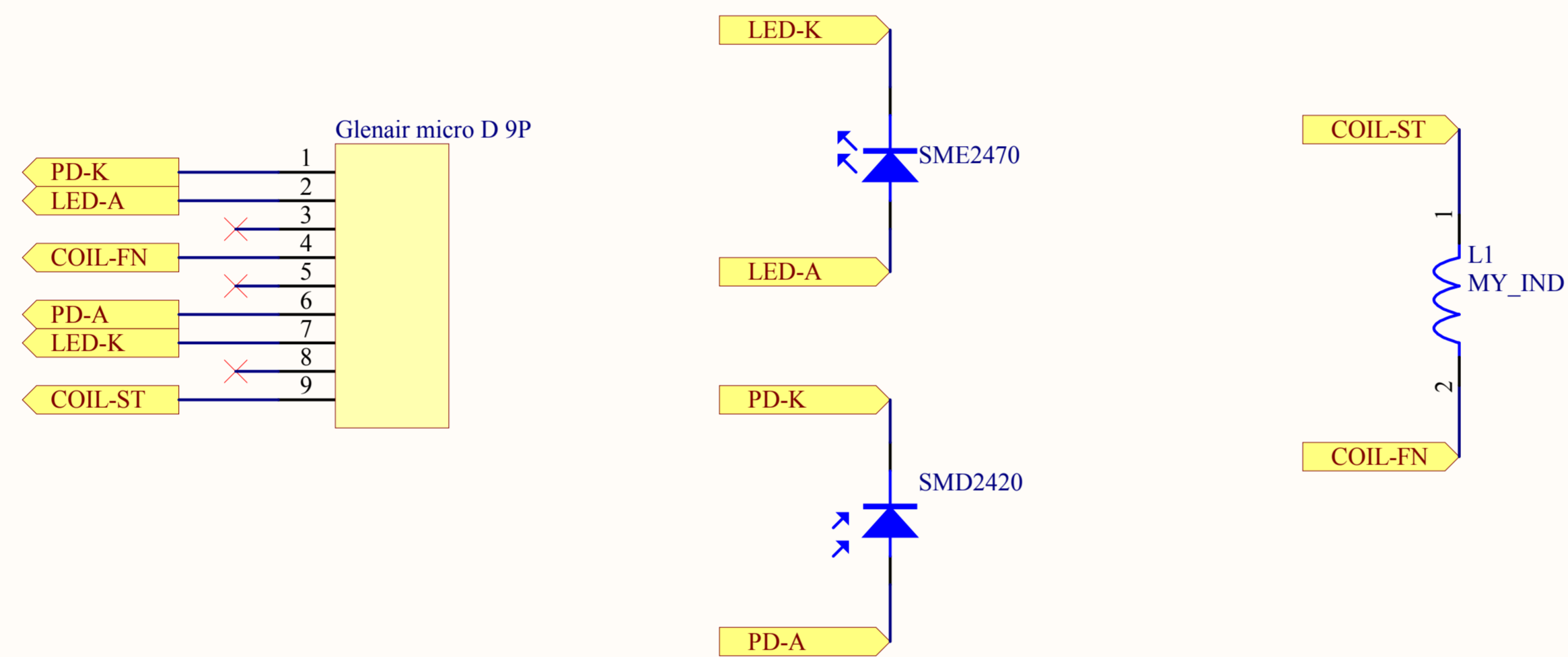
REV.	DATE	DCN #	DRAWING TREE #
v1	MARCH 2010	E1300492	-
-	-	-	-
-	-	-	-

NOTES CONTINUED:
 1. FLEXIBLE CIRCUIT FABRICATION
 SPECIFICATION T1000115. STANDARD
 LIGO ELECTRICAL INTERFACES T060123.



D0901252 FlexibleCircuit, PART PDM REV: v1, DRAWING PDM REV:

NOTES AND TOLERANCES: (UNLESS OTHERWISE SPECIFIED)				LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY		PART NAME		FLEXIBLE CIRCUIT		
DIMENSIONS ARE IN TOLERANCES: XX: ± SEE GERBER XXX: ± FILES ANGULAR: ±				SYSTEM aLIGO		SUB-SYSTEM SUS		DESIGNER R. ABBOTT	DATE MARCH 2010	REV. v1
MATERIAL POLYIMIDE/COPPER		FINISH N/A μinch		NEXT ASSY D0901066		SCALE: 2:1		DWG. NO. D0901252		
PROJECTION:								SHEET 1 OF 3		



Last Edited:

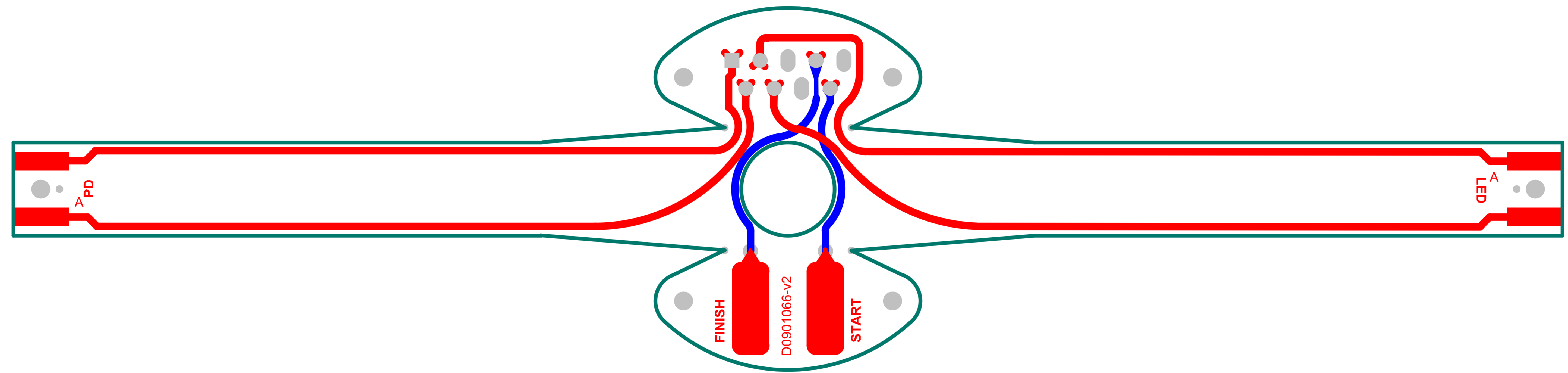
Title: **FLEXIBLE CIRCUIT**

LIGO Laboratory
California Institute of Technology
Massachusetts Institute of Technology



Size: B	DCC Number: D0901252	Revision:	Engineer: R.Abbott/C.Osthelder	Date: 3/2/2010
File: C:\Documents and Settings\costheld\My Documents\chub_ligo_files\ChubAltium\project_files\osem_pcb\osem_pcb.SchDoc				Time: 3:47:05 PM

Sheet **2 OF 3**



FLEXIBLE CIRCUIT
D0901252
SHEET 3 OF 3