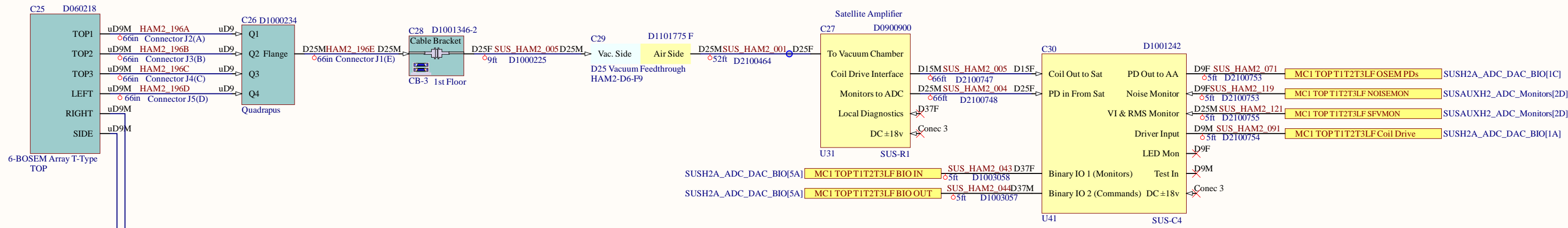
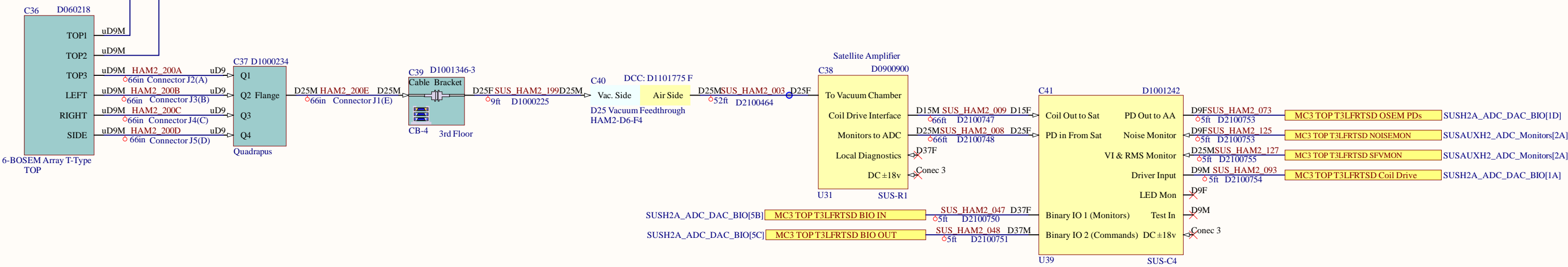


MC1 and MC3 TOP MASSES

MC1

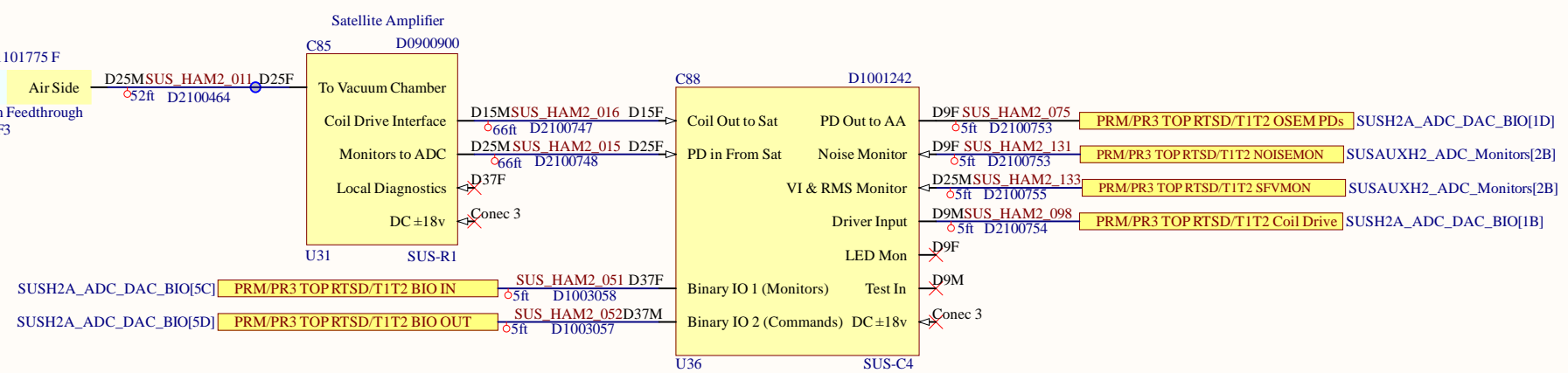
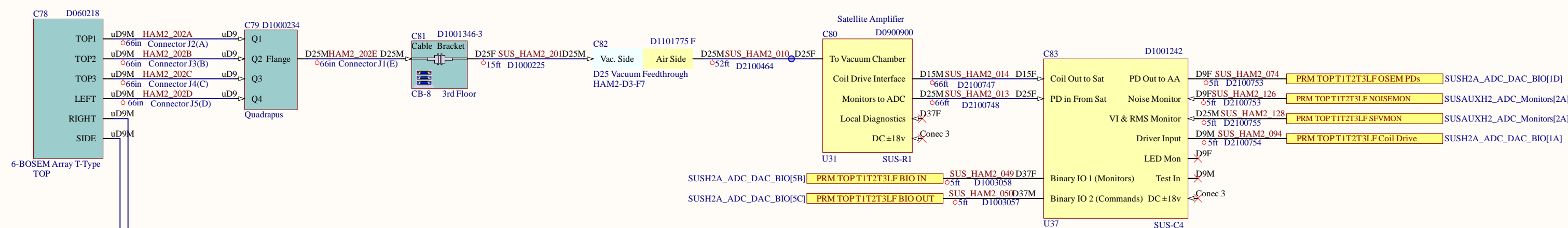


MC3

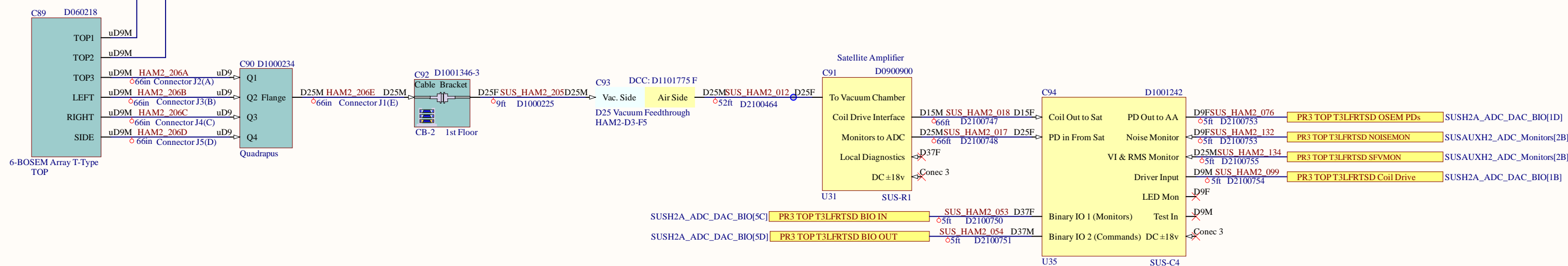


PRM and PR3 TOP MASSES

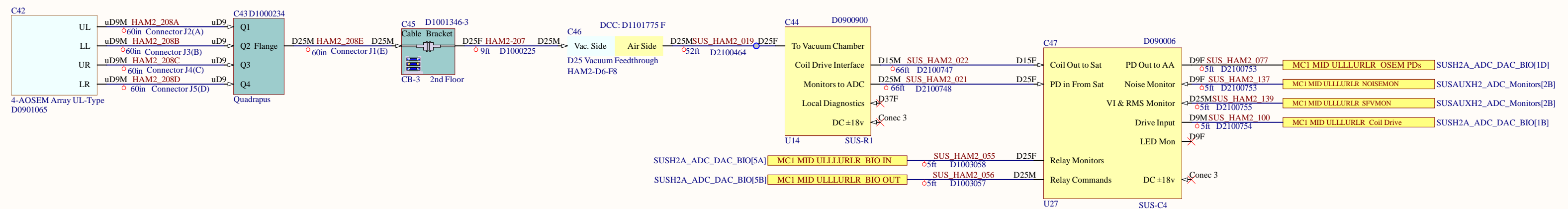
PRM



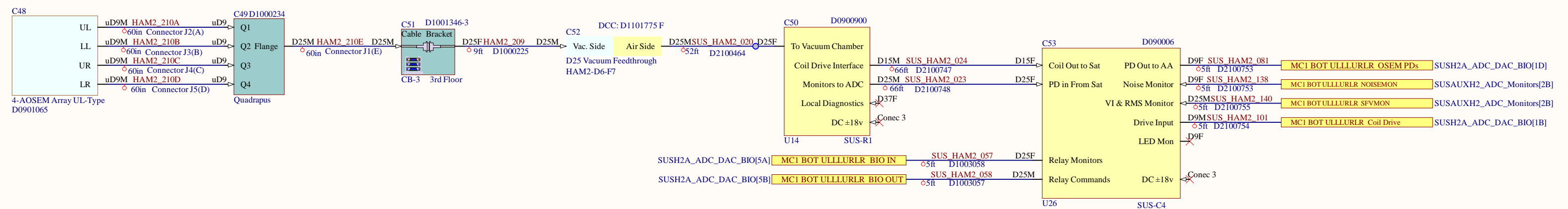
PR3



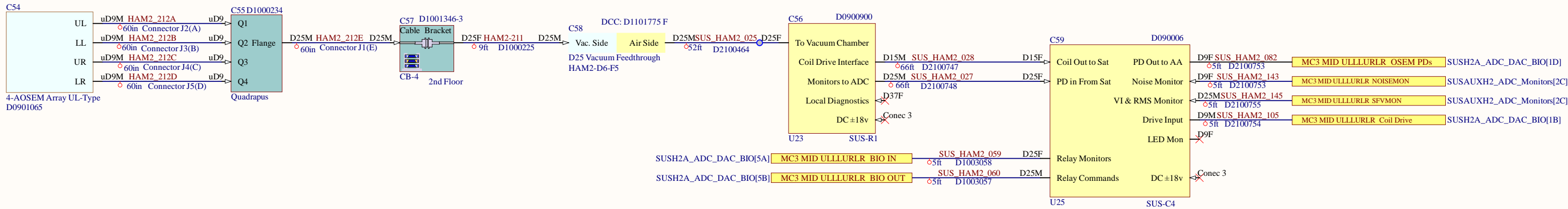
MC1 MIDDLE MASS



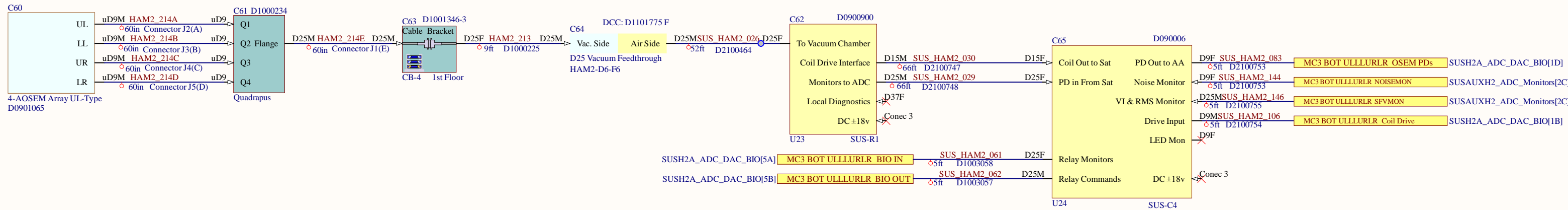
MC1 BOTTOM MASS



MC3 MIDDLE MASS

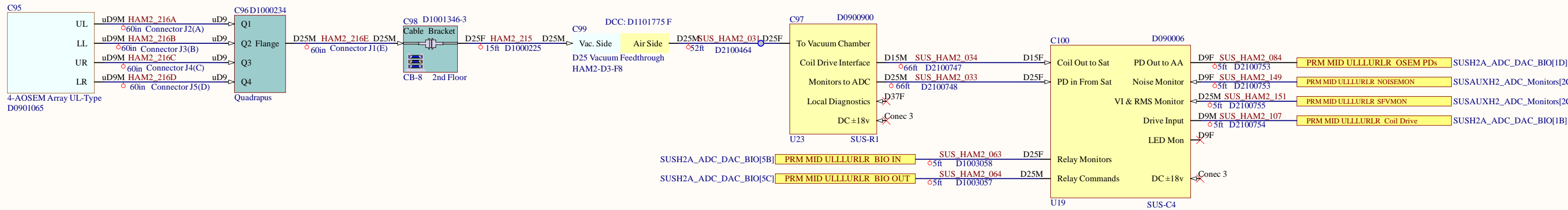


MC3 BOTTOM MASS

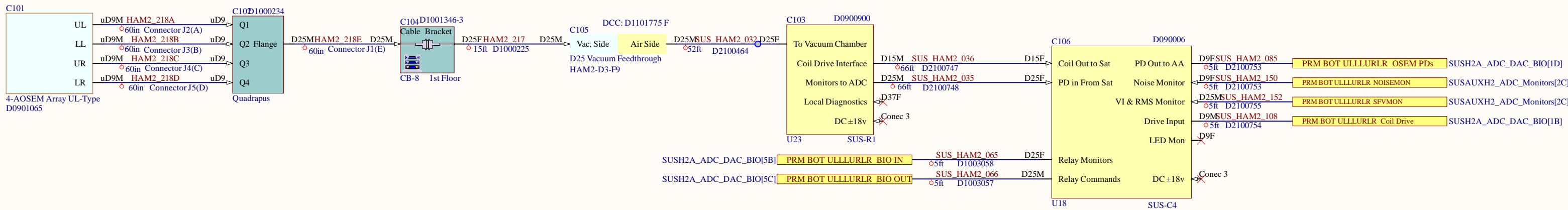


Last Edited: 4/15/2025

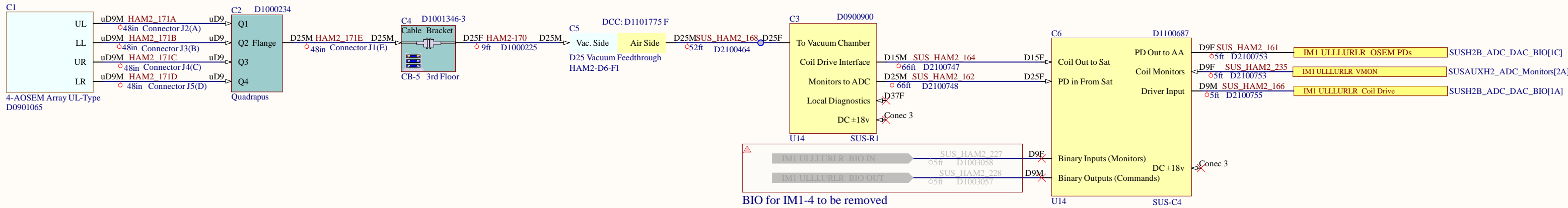
PRM MIDDLE MASS



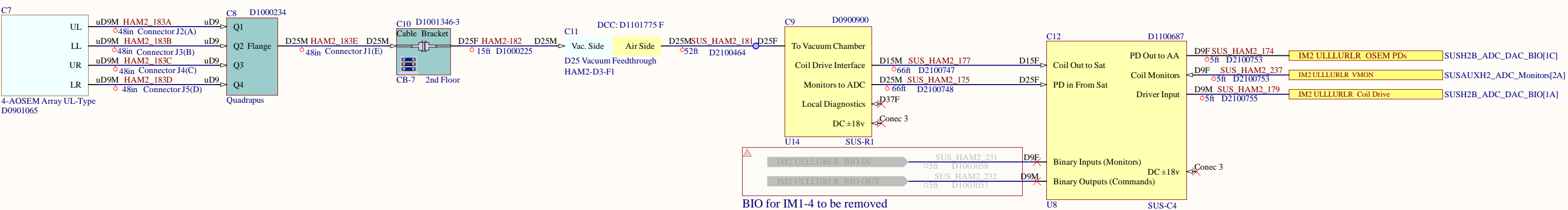
PRM BOTTOM MASS



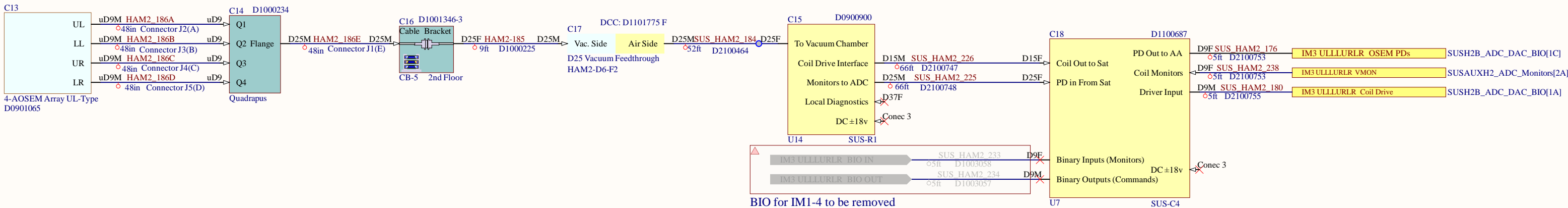
IM1



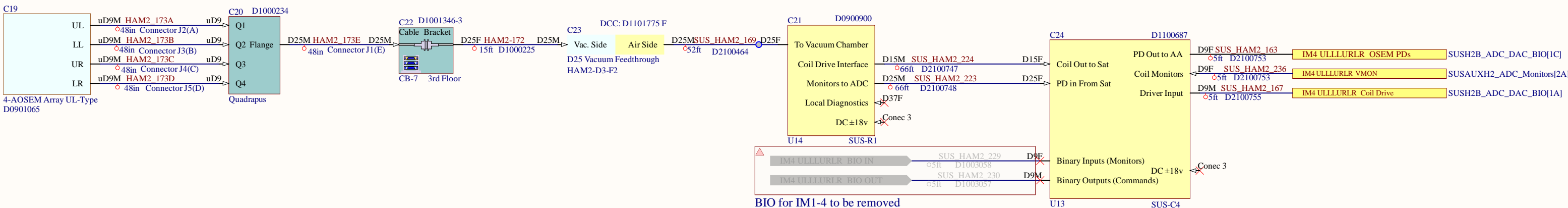
IM2



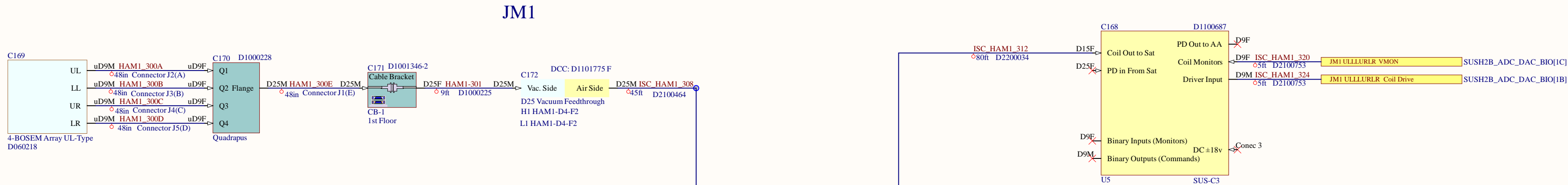
IM3



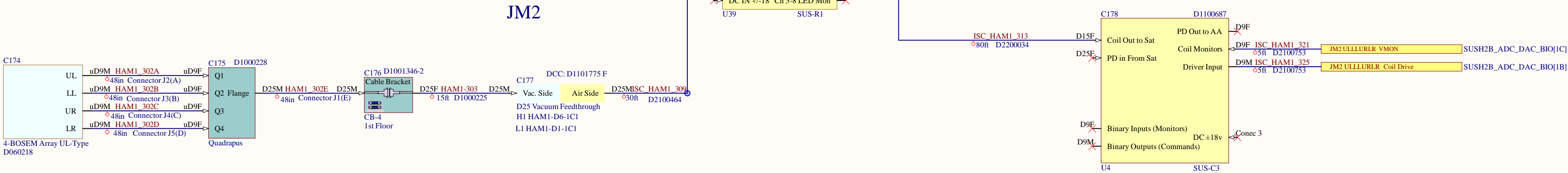
IM4



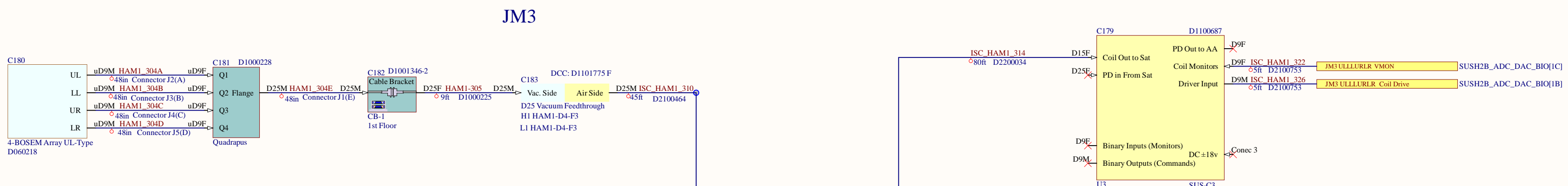
A



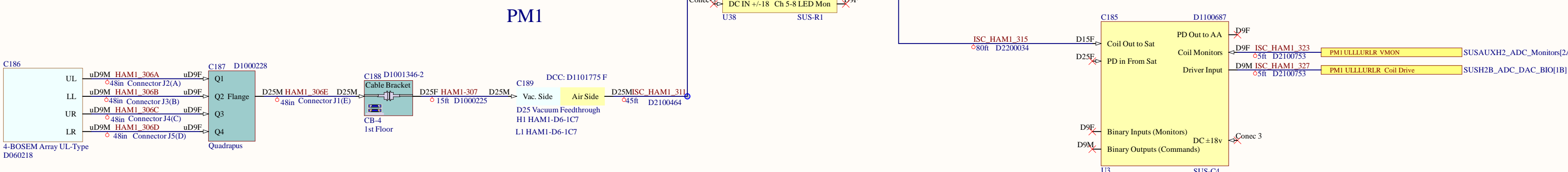
B



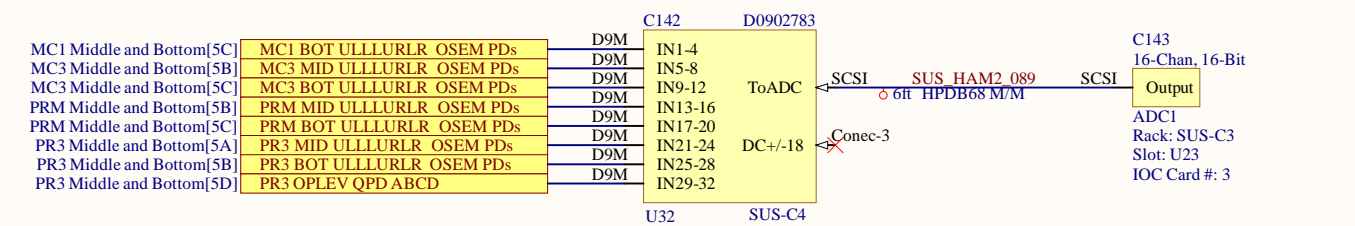
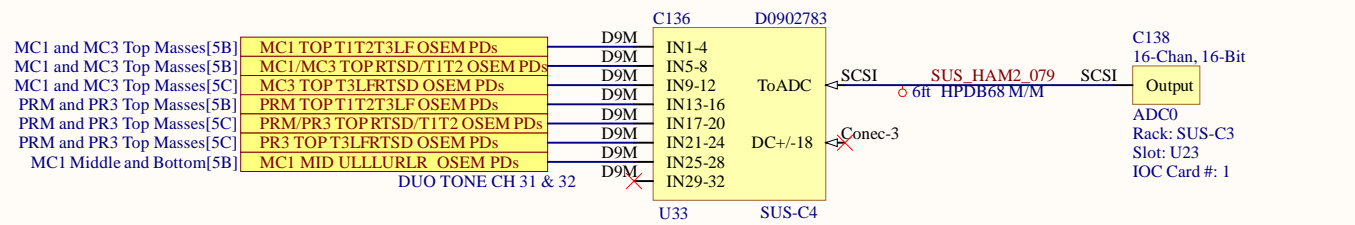
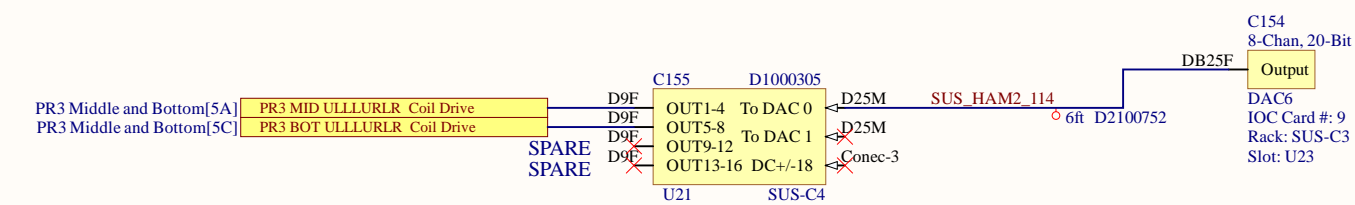
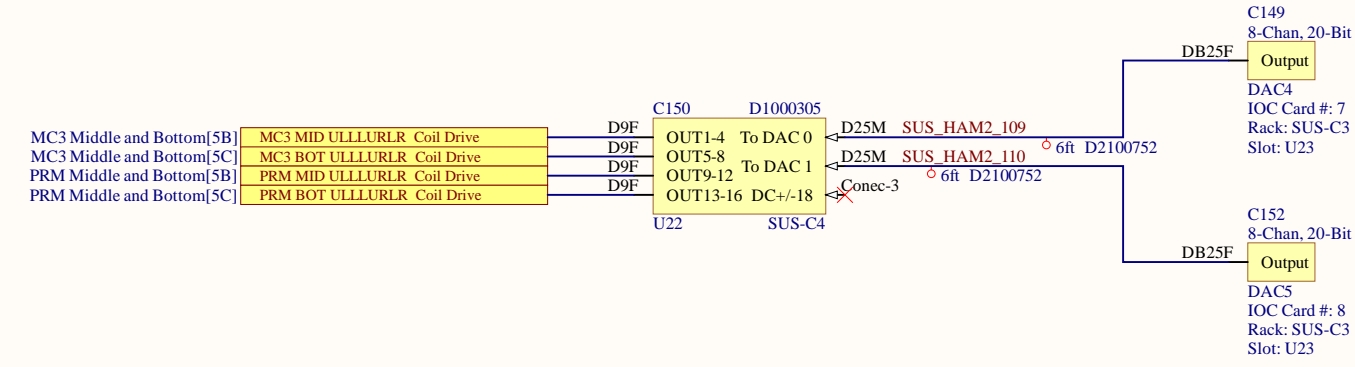
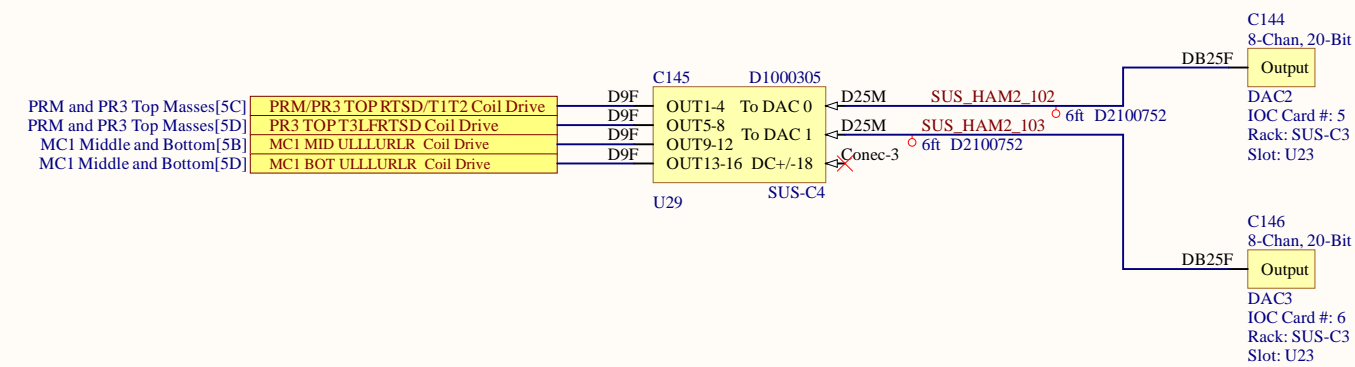
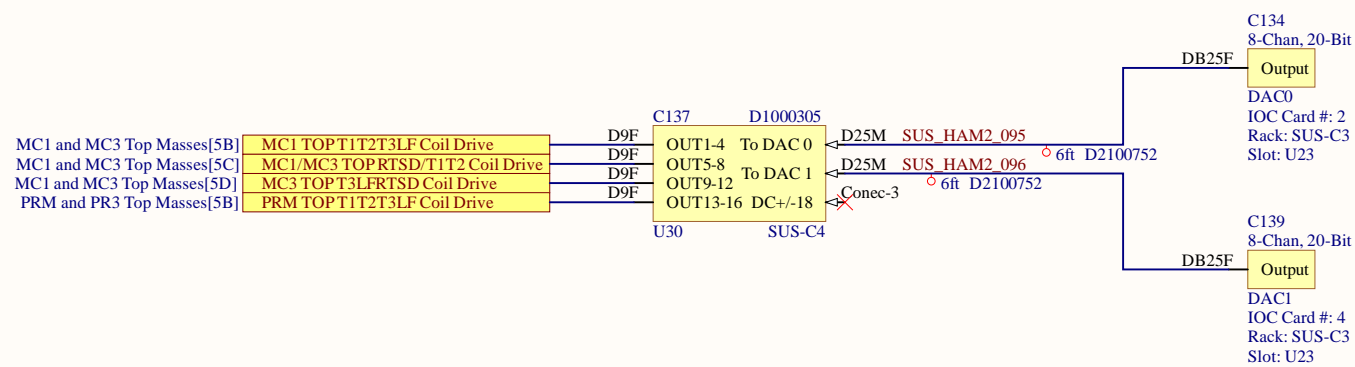
C



D



A



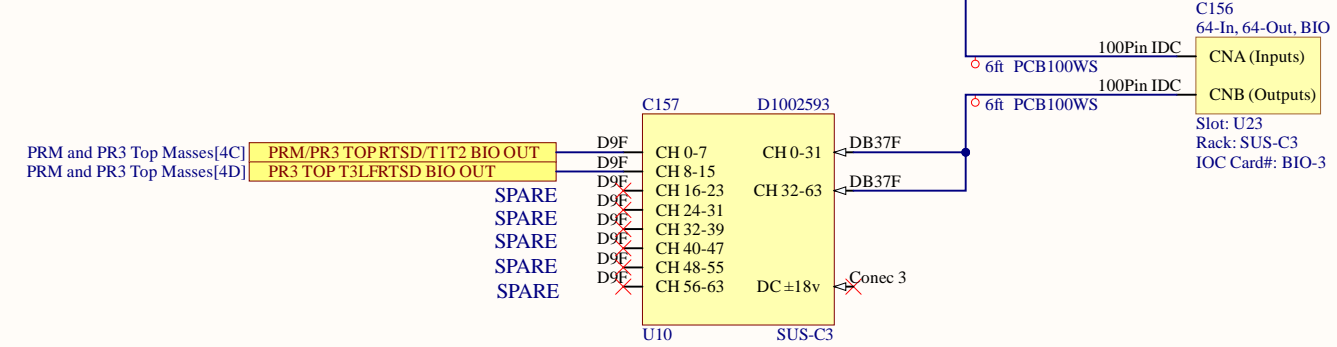
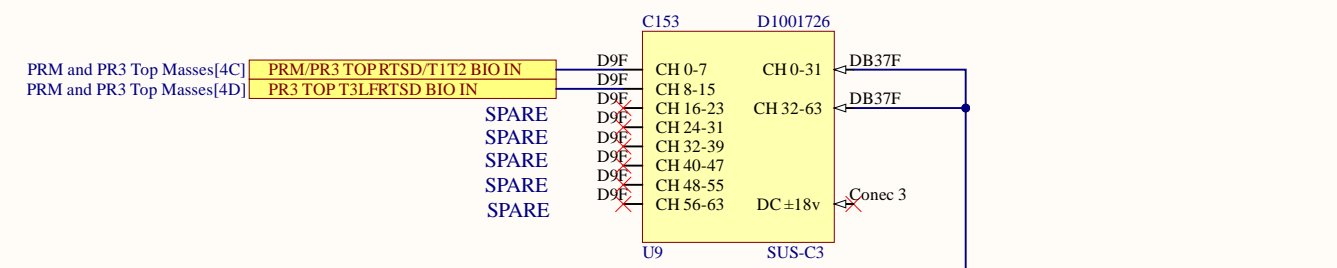
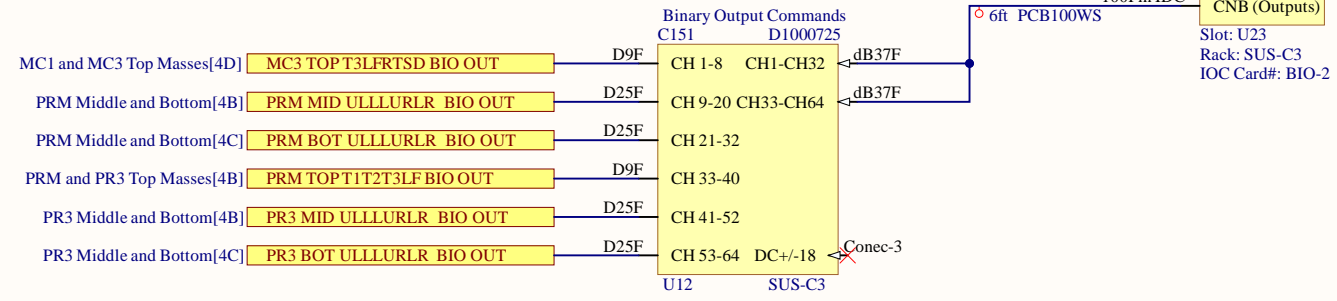
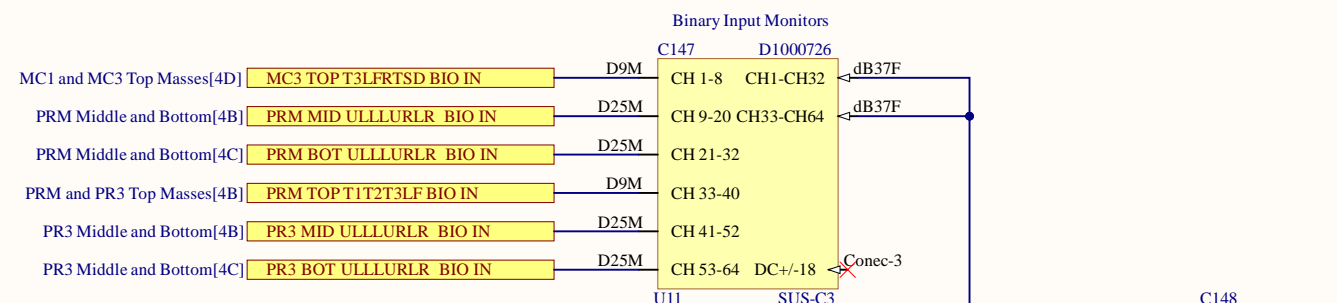
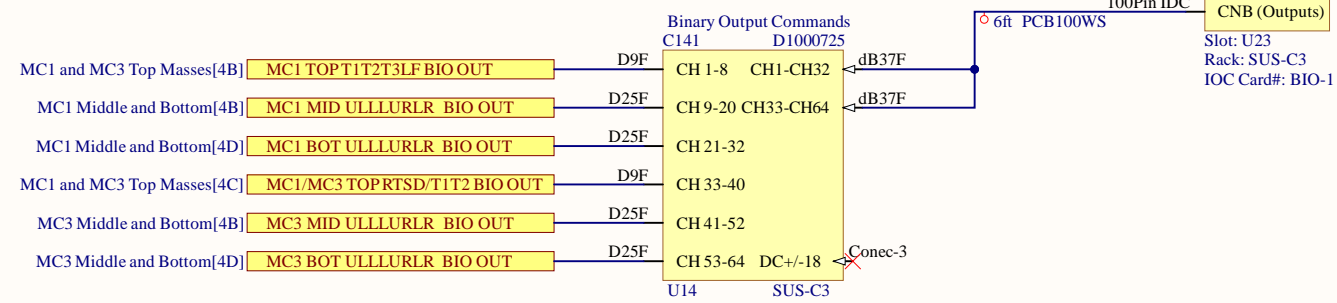
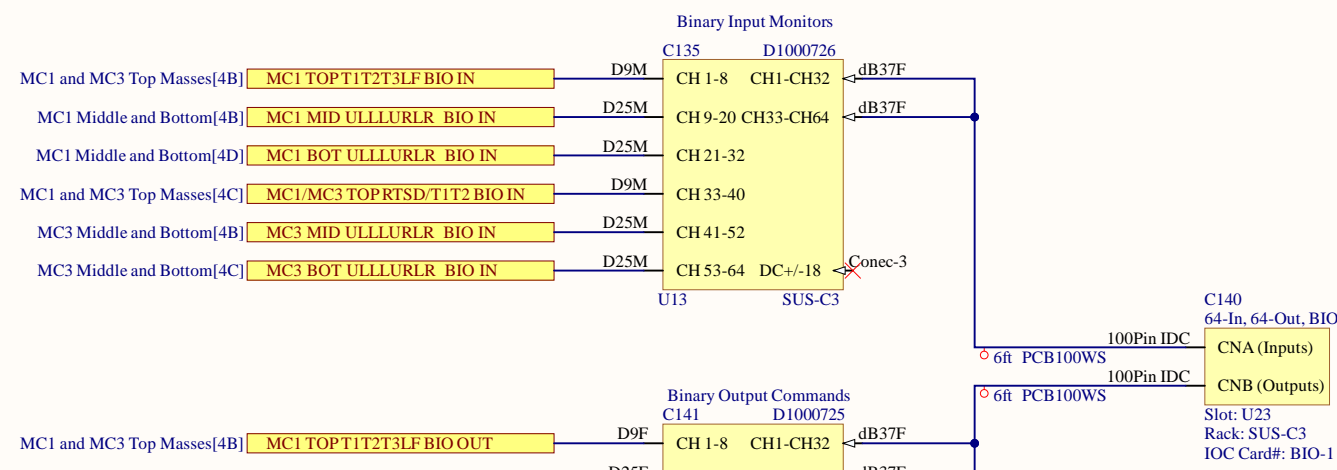
D

A

B

C

D



A

A

B

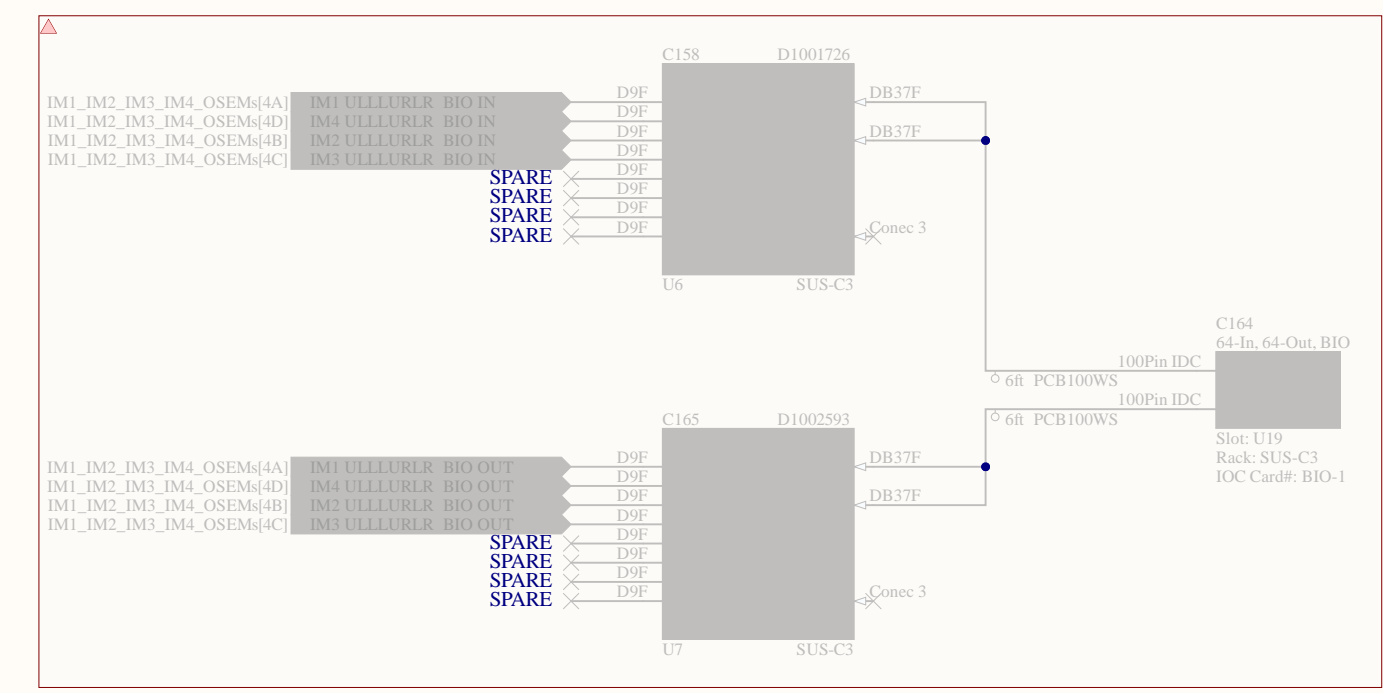
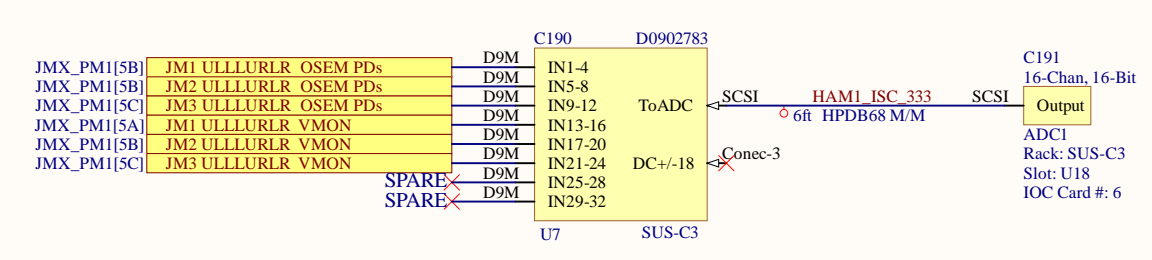
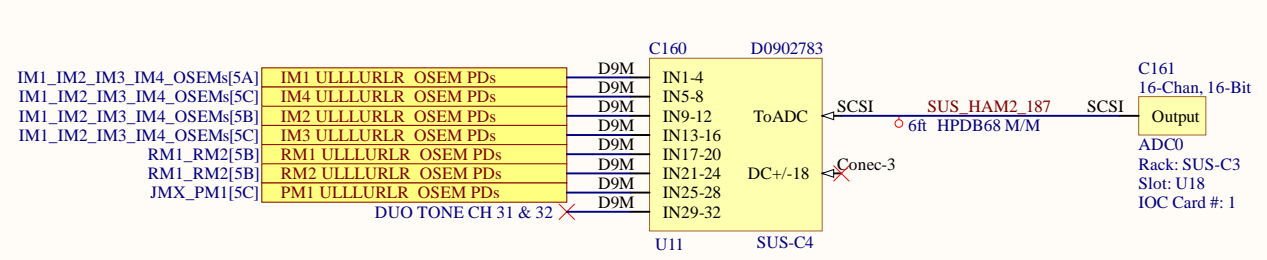
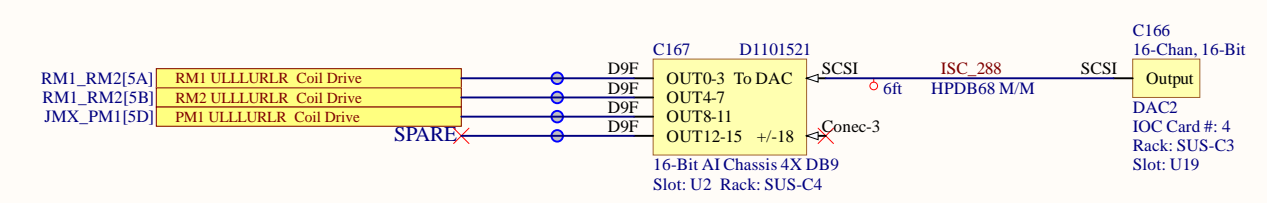
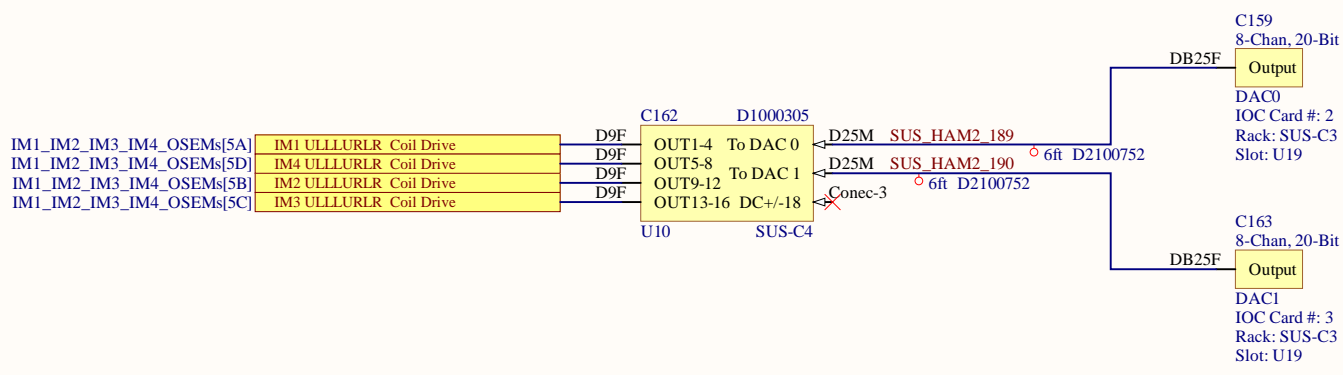
B

C

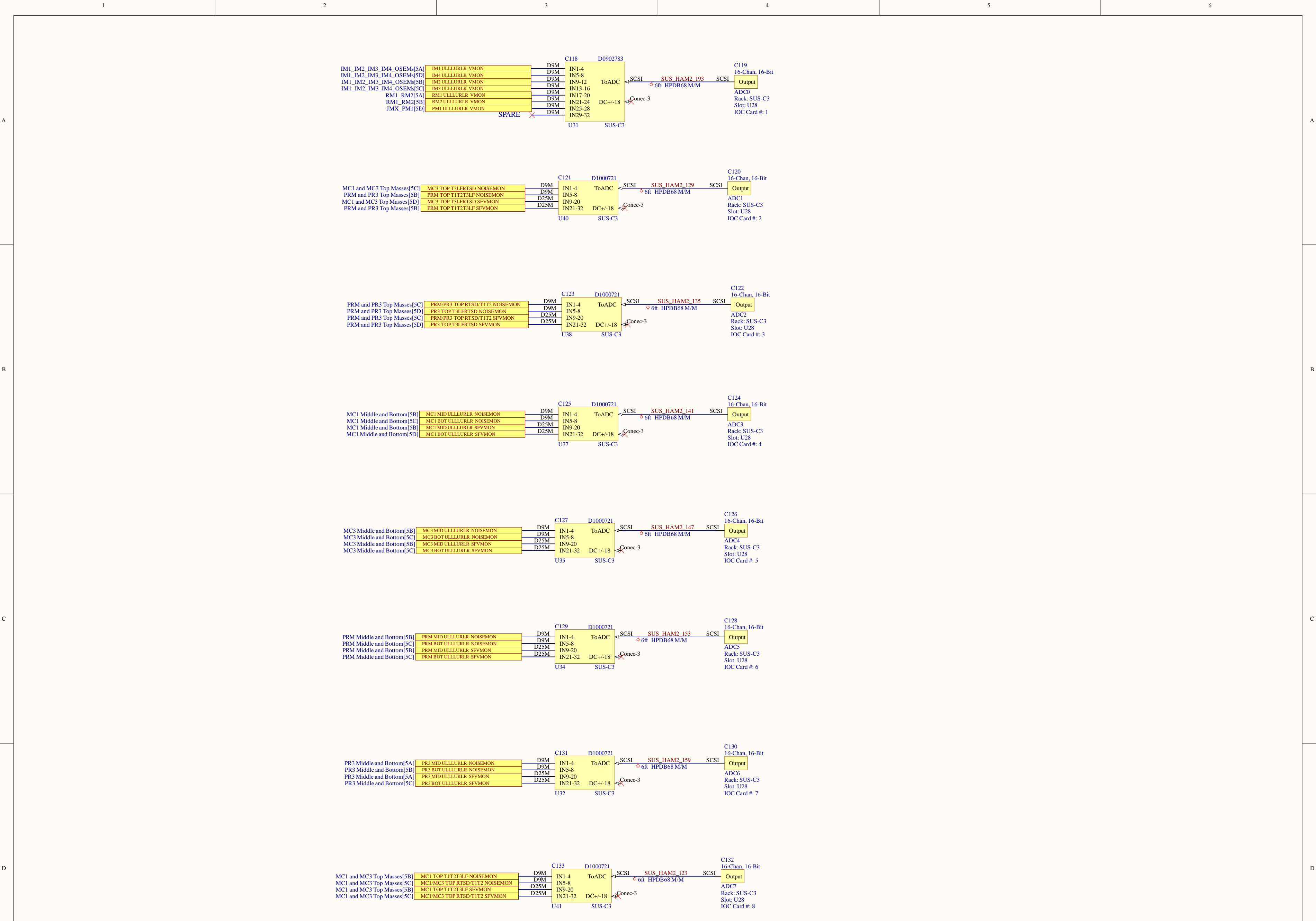
C

D

D



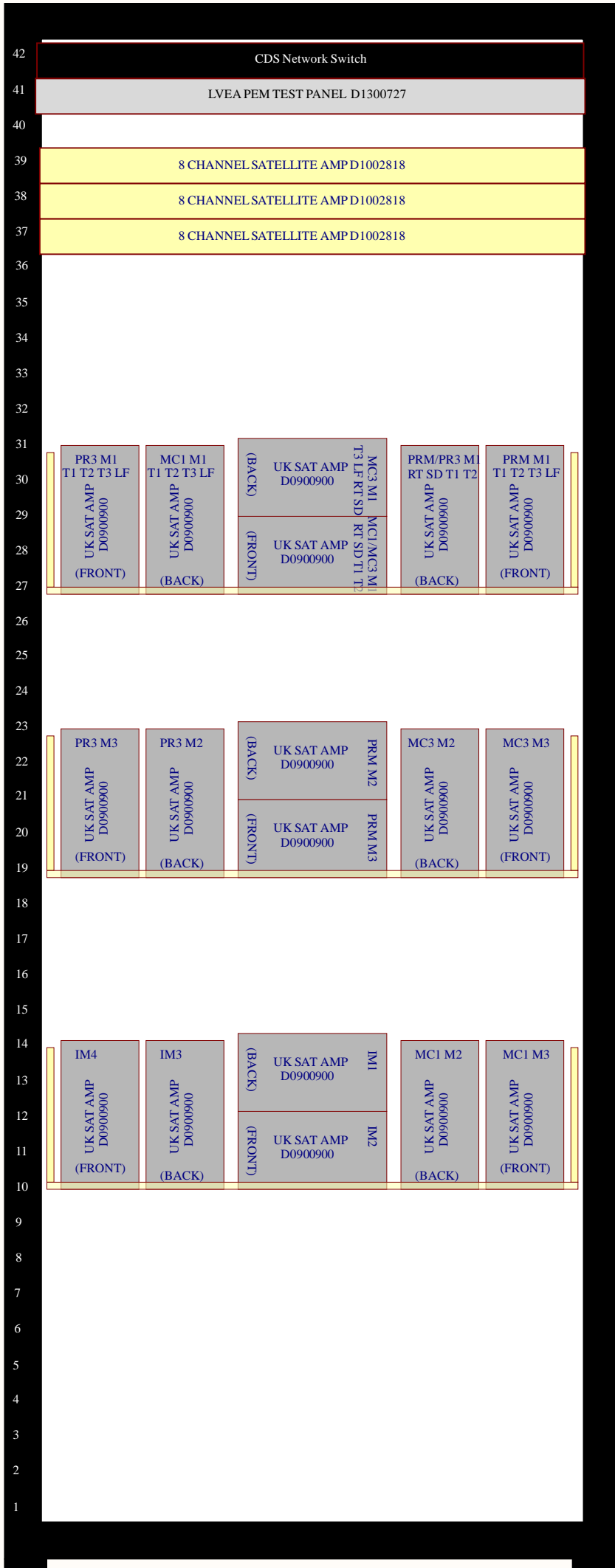
BIO for IM1-4 to be removed



Front View
FLOOR SUS-R1 HAM1

JM1, JM2
JM3, PM1
RM1, RM2

PR3 OPLEV located in SUS-R2 U12



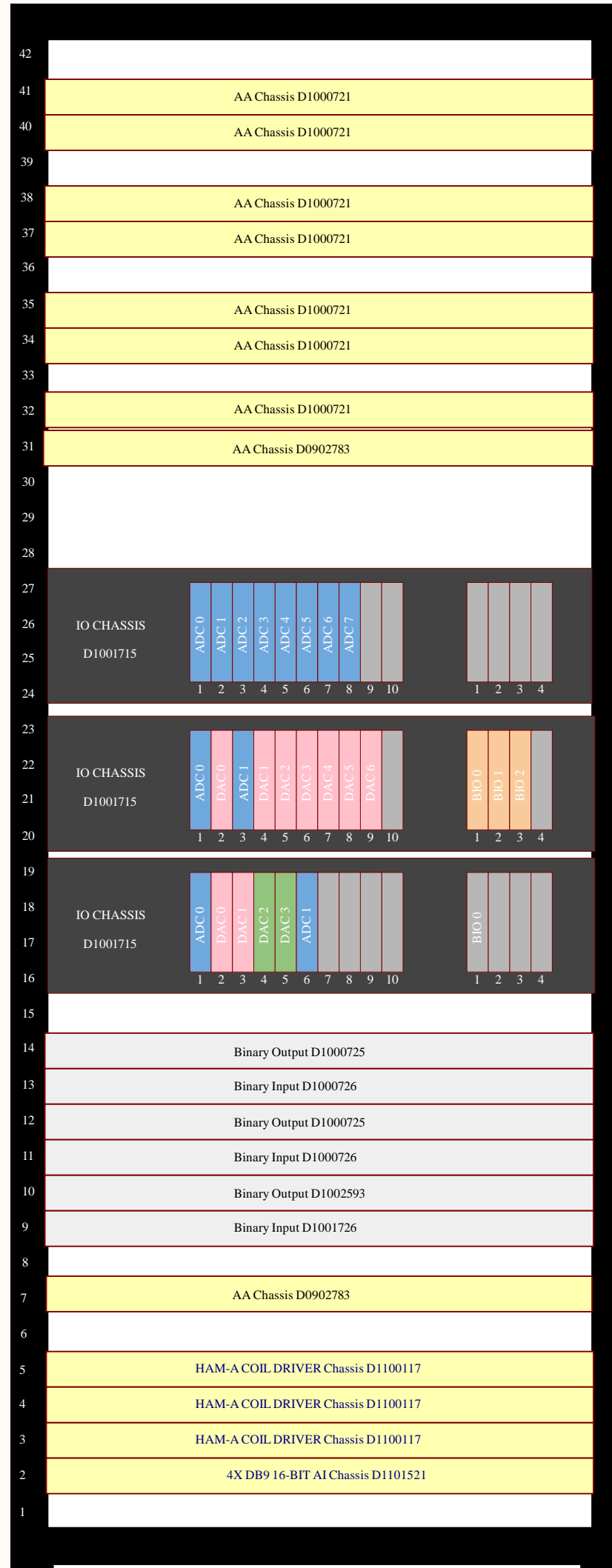
Front view

SUS C4



Front view

SUS C3



MC1 TOP T1T2T3LF
MC1/MC3 TOP RTSD/T1T2
MC3 TOP T3LFRSD

PRM TOP T1T2T3LF
PRM/PR3 TOP RTSD/T1T2
PR3 TOP T3LFRSTD

	MC1 TOP, MC1/MC3 TOP, MC3 TOP, PRM TOP, PRM/PR3 TOP, PR3 TOP, MC1 MID, Spare MC1 BOT, MC3 MID, MC3 BOT, PRM MID, PRM BOT, PR3 MID, PR3 BOT, PR3 OL MC2 TOP, MC2/PR2 TOP, PR2 TOP, SR2 TOP MC1 TOP, MC1/MC3 TOP, MC3 TOP, PRM TOP PRM/PR3 TOP, PR3 TOP, MC1 MID, MC1 BOT
B	

MC1 MID
MC1 BOT
MC3 MID
MC3 BOT

MC3 MID, MC3 BOT, PRM MID, PRM BOT
PR3 MID, PR3 BOT, Spare, Spare

PRM MID
PRM BOT
PR3 MID
PR3 BOTIM1
IM4

IM1, IM4, IM2, IM3, RM1, RM2, PM1, Spare
IM1, IM4, IM2, IM3

IM2
IM3

RM1
RM2
PM1

RM1, RM2, PM1, Spare

If we use the new 32-channel DAC, we can combine SUS12A and SUS12B into a single IO chassis:

- DAC channels needed: 96
- ADC channels needed: 100 (+12 channel VMON)
- Binary IO: <192 inputs/outputs

