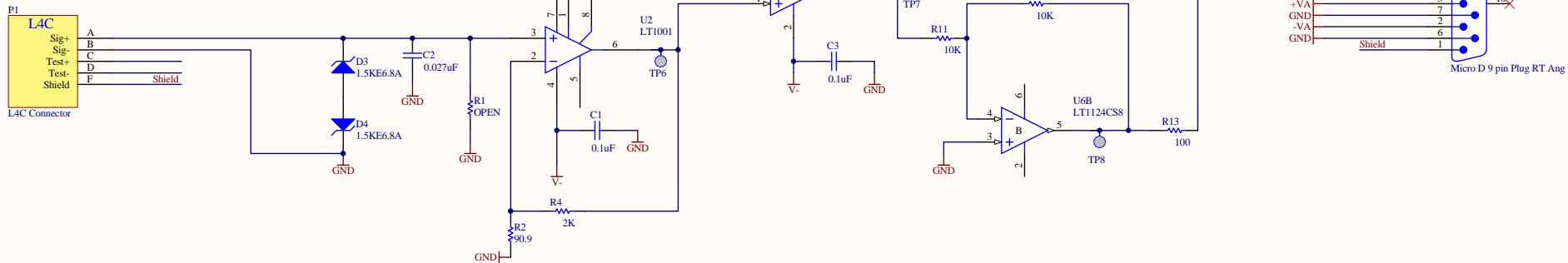
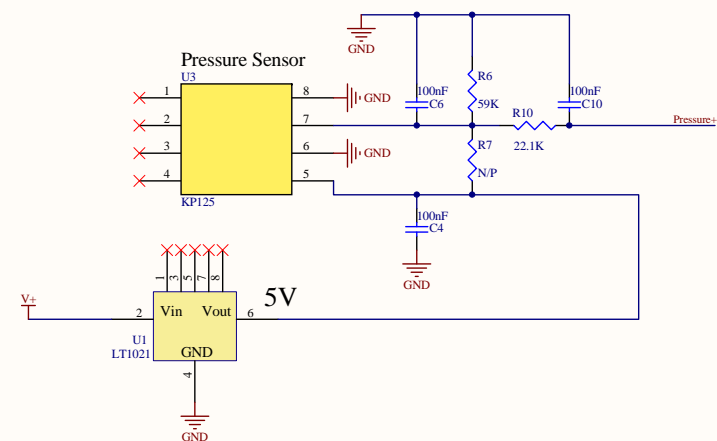
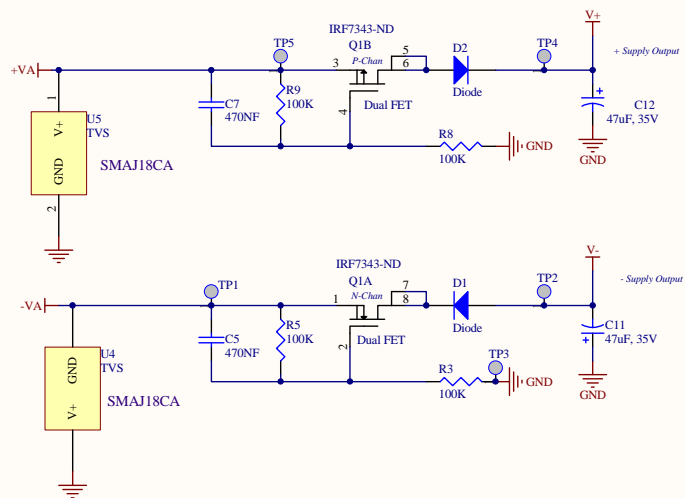



Pin A--- Coil +
Pin B--- Coil -
Pin C--- Cal Coil +
Pin D--- Cal Coil -
Pin F--- Case GND/Shield



Inrush Current Limiter, Transient spike, and reverse Voltage protection.



Title ISI L4C Interface Board			LIGO Project California Institute of Technology Massachusetts Institute of Technology		LIGO 
Size: B	DCC Number: D1000059	SCH / PCB Revision: v2	* Engineer: Sam Abbott	Date: 10/26/2010	
File: C:\Documents and Settings\sabbott\My Documents\My Designs\NewLACPreamp (1-6-2010 2:50-11 PM)\LACPreamp3-182006.kicad			Time: 9:49:35 AM		