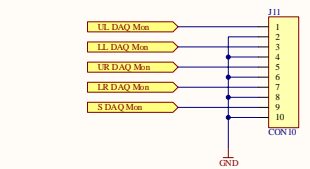
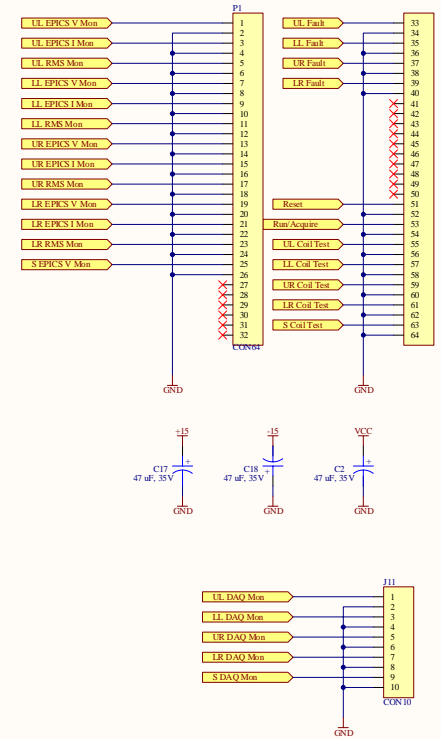


DB15 CONNECTOR MOUNTED ON IF OF MODULE. CONNECTED TO IDC16 CONNECTOR MOUNTED ON BOARD VIA MASS TERMINATION CABLE.

Pin designations as follows:

Name	IDC Pin #	15 D pin #
UL Out	1	1
UL Rtn	2	9
LL Out	3	2
LL Rtn	4	10
UR Out	5	3
UR Rtn	6	11
LR Out	7	4
LR Rtn	8	12
S Out	9	5
S Rtn	10	13
N/C	11	6
N/C	12	14



Rev	Description	Date
A	Initial Release	10/00
B	1. Changed connection of K5 to correct polarity for coil (Pins 1&6 reversed) 2. PCB traces rerouted for K5 and C64. 3. Hole sizes for PA85 increased to allow sockets. 4. J12 rotated on PCB.	6/01
B1	Swapped pins 10 and 12 on relays K1, K2, K3, K4 and K5 to invert enable signal. Change was done by removing relays from board and adding an interface socket to make the switch.	10/01
B2	1. Changed C47, C45, C74, C72 to 47pF. 2. Changed R88, R83, R137, R129 to 110 ohms. (Better compensation of PA85s)	1/02
B3	Rev B3 revision notes: Rev B3 typically applies to BS, RM, ITMs and ETMs 1. 7.2K feedback resistor is changed to 21.6K ohms (3 ea. 7.2K in series) 2. 1Kohm in series with 5uF is changed to 100K in series with 5uF for BS, RM and ETMs. Remains at 1K for ITMs. 3. 7.2K in series with coil output is changed to 3 ea. 1K in series. 4. 10 ohm current limit resistors on PA85 changed to make a 1.4ohm current limit resistor. 5. DAQ coil output Monitors changed to have 0.05uF capacitor and 10K resistor, making a 300Hz HPF.	9/02
C	Board release incorporating all Rev B changes	1/03
C1	As built changes for LLO ETMs. Cadd1-Cadd4, Dadd1-Dadd16 added to existing rev C boards using wires and direct connections to pins. Diodes added for PA85 protection per APEX app note 25.	1/05