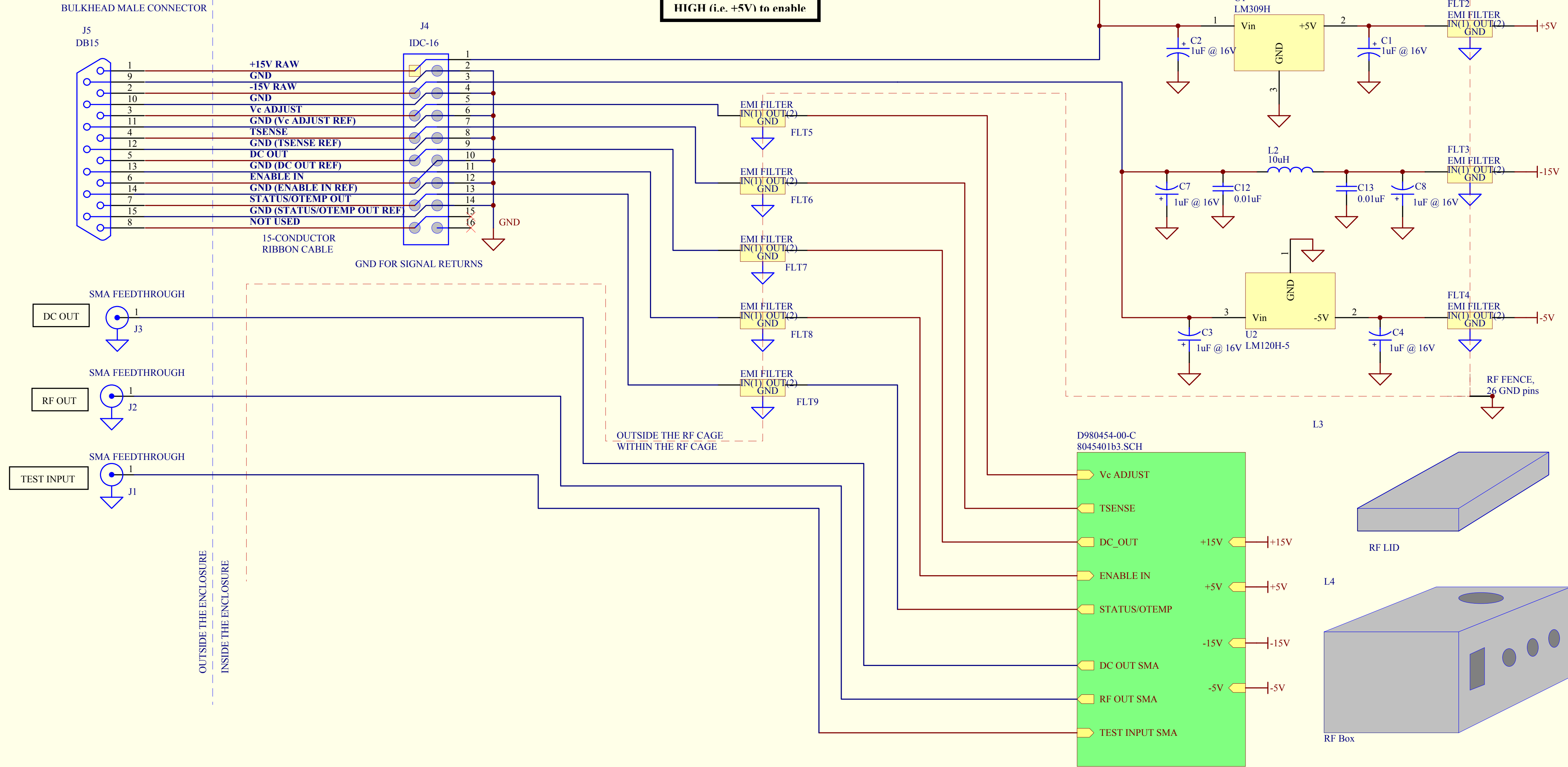


PSL - PMC

35.5 MHz RF Photo-Diode

Engineering Change Order History		
ECO number	Date	Originator
1		
2		
3		
4		
5		

ENABLE IN must be HIGH (i.e. +5V) to enable



REV	DATE	DRAWN BY	CHECKED BY	DCC	DCN / DESCRIPTION
1					
2					
3					
4					
5					

Title: **PSL 35.5 Mhz RF Photodiode Board**

Size: B DCC Number: D000386-00-C Revision: Engineer: D. Ouimette Date: 11/28/2000

File: A:\PSL 35.5 MHz RFPD sh 1 D000386.sch Time: Sheet 1 of 2

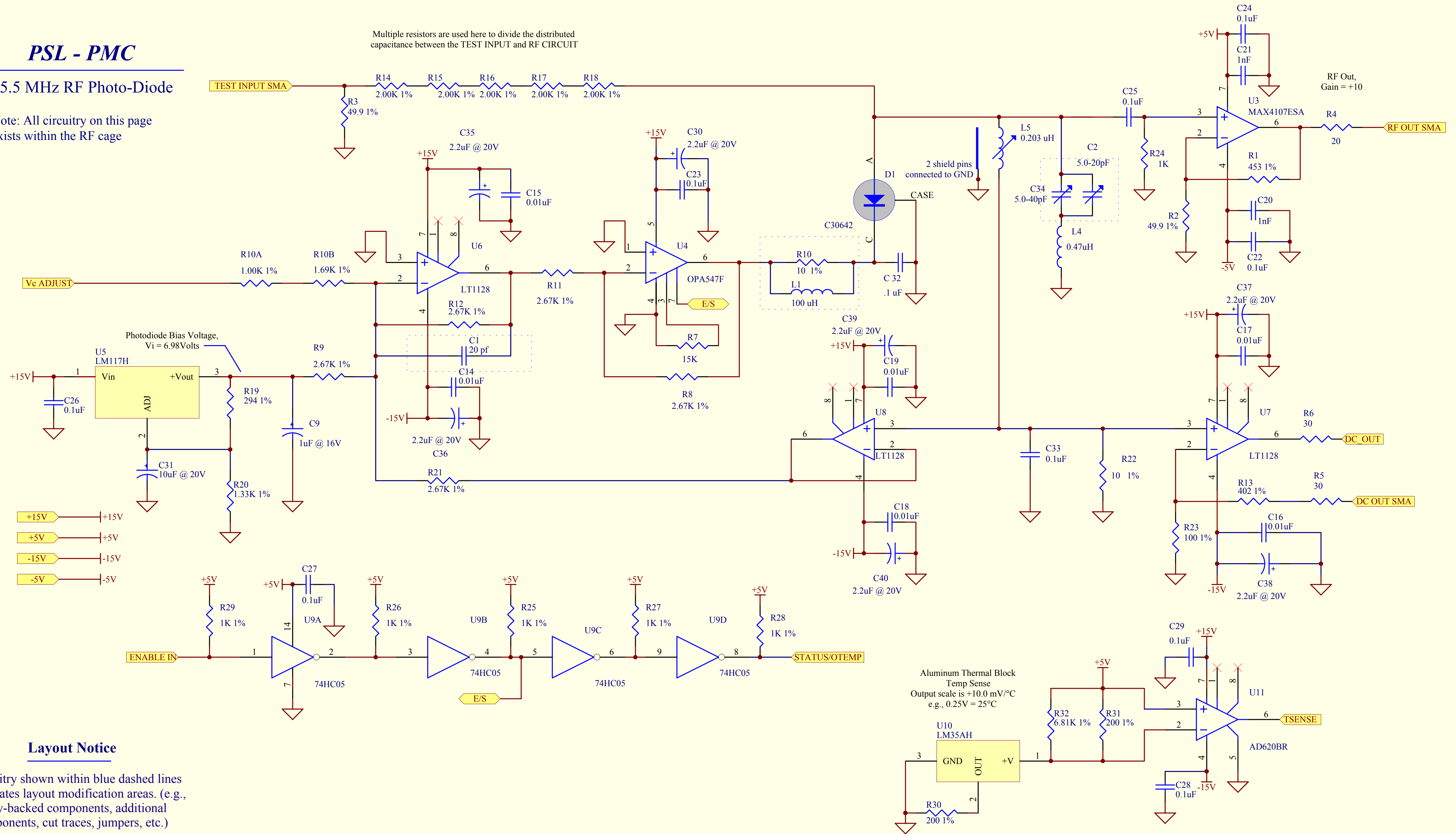


PSL - PMC

35.5 MHz RF Photo-Diode

Note: All circuitry on this page exists within the RF cage

Multiple resistors are used here to divide the distributed capacitance between the TEST INPUT and RF CIRCUIT



Layout Notice

- Circuitry shown within blue dashed lines indicates layout modification areas. (e.g., piggy-backed components, additional components, cut traces, jumpers, etc.)
- Further detail is provided on the ECOs referenced against this schematic. The ECO history box is located on the first page of this schematic.

Title PSL 35.5 MHz RF Photodiode Board			Ligo Project California Institute of Technology Massachusetts Institute of Technology Engineer: R. Abbott	
Size: B	Number: D000386-00-C	Revision:		
File: A:\PSL 35.5 MHz RFPD sh 2 D000386 Batch31-Dec-2001			Time: 11/28/00	Sheet 2 of 2

