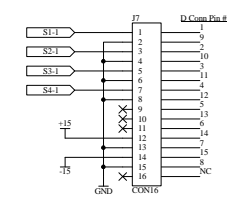
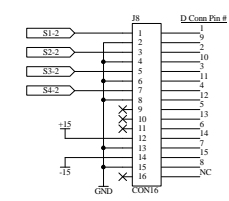
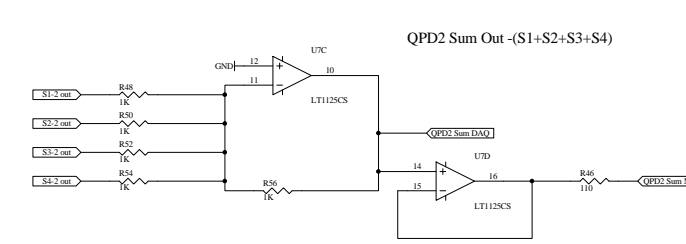
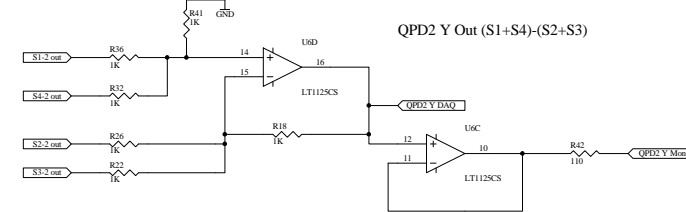
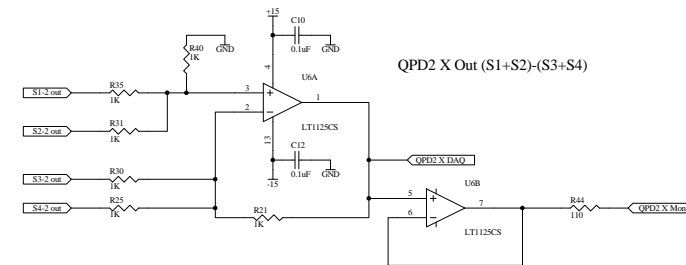
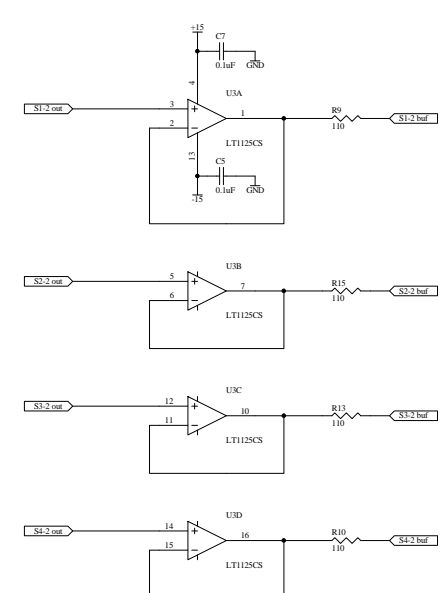
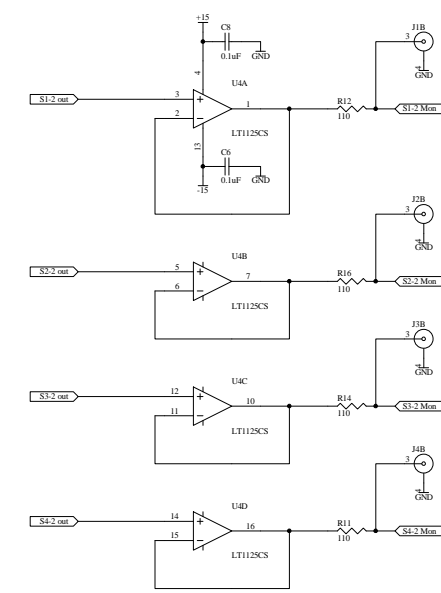
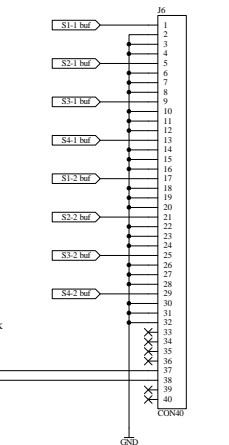
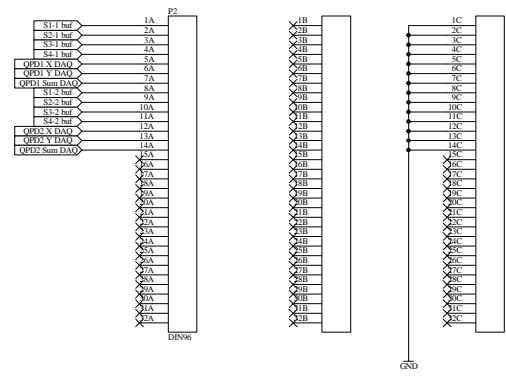


Connections to DAQ System are made via P2 to adjacent DAQ Interface Module



F.P. 4.194304 MHz Input Clock





Opt_vinterface2
Opt_vinterface2.Sch

Opt_vinterface
Opt_vinterface.Sch

- S1-1
- S1-1 Men
- S2-1
- S2-1 Men
- S3-1
- S3-1 Men
- S4-1
- S4-1 Men
- S1-1
- S2-1
- S3-1
- S4-1
- S1-2
- S1-2 Men
- S2-2
- S2-2 Men
- S3-2
- S3-2 Men
- S4-2
- S4-2 Men
- S1-2
- S2-2
- S3-2
- S4-2
- S1-1
- S2-1
- S3-1
- S4-1
- S1-1
- S2-1
- S3-1
- S4-1
- QPD1 X Men
- QPD1 Y Men
- S1-1
- S2-1
- S3-1
- S4-1
- QPD1 Sum Men
- S1-2
- S2-2
- S3-2
- S4-2
- S1-2
- S2-2
- S3-2
- S4-2
- S1-1
- S2-1
- S3-1
- S4-1
- QPD2 X Men
- QPD2 Y Men
- S1-2
- S2-2
- S3-2
- S4-2
- QPD2 Sum Men
- QPD1 X DAQ
- QPD1 Y DAQ
- QPD1 Sum DAQ
- QPD2 X DAQ
- QPD2 Y DAQ
- QPD2 Sum DAQ
- S1-1 Men
- S2-1 Men
- S3-1 Men
- S4-1 Men
- S1-2 Men
- S2-2 Men
- S3-2 Men
- S4-2 Men
- QPD1 X Men
- QPD1 Y Men
- QPD1 Sum Men
- QPD2 X Men
- QPD2 Y Men
- QPD2 Sum Men
- S1-1
- S2-1
- S3-1
- S4-1
- QPD1 X DAQ
- QPD1 Y DAQ
- QPD1 Sum DAQ
- S1-2
- S2-2
- S3-2
- S4-2
- QPD2 X DAQ
- QPD2 Y DAQ
- QPD2 Sum DAQ
- S1-1
- S2-1
- S3-1
- S4-1
- S1-2
- S2-2
- S3-2
- S4-2