

**Release 8.2.03i - XPower Software Version: I.34 Copyright (c) 1995-2006 Xilinx, Inc. All rights reserved.**

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<b>Design:</b>	C:\User\Daniel\Protel\Converter\FPGAUplinkSA\ProjectOutputs\FPGAUplinkSA\fpgauplinksa_map.ncd
<b>Preferences:</b>	C:\User\Daniel\Protel\Converter\FPGAUplinkSA\ProjectOutputs\FPGAUplinkSA\fpgauplinksa_map.pcf
<b>Part:</b>	4vfx60ff672-10
<b>Data version:</b>	PREVIEW,1.0,08-08-05

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**XPower and Datasheet may have some Quiescent Current differences. This is due to the fact that the quiescent numbers in XPower are based on measurements of real designs with active functional elements reflecting real world design scenarios.**

<b>Power summary:</b>	<b>I(mA)</b>	<b>P(mW)</b>
<b>Total estimated power consumption:</b>		<b>5402</b>
<b>Vccint 1.20V:</b>	<b>1376</b>	<b>1652</b>
<b>Vccaux 2.50V:</b>	<b>230</b>	<b>574</b>
<b>Vccauxrx 1.20V:</b>	<b>228</b>	<b>273</b>
<b>Vccauxtx 1.20V:</b>	<b>129</b>	<b>154</b>
<b>Vtrx 1.50V:</b>	<b>15</b>	<b>23</b>
<b>Vttx 1.50V:</b>	<b>62</b>	<b>93</b>
<b>Vcco25 2.50V:</b>	<b>1053</b>	<b>2632</b>
<b>Outputs:</b>		
<b>Vcco25</b>	<b>1053</b>	<b>2632</b>
<b>Logic:</b>	<b>878</b>	<b>1053</b>
<b>Clocks:</b>	<b>4</b>	<b>5</b>
<b>IOs:</b>	<b>0</b>	<b>0</b>
<b>Inputs:</b>	<b>0</b>	<b>0</b>
<b>Signals:</b>	<b>0</b>	<b>0</b>
<b>Quiescent Vccint 1.20V:</b>	<b>494</b>	<b>593</b>
<b>Quiescent Vccaux 2.50V:</b>	<b>230</b>	<b>574</b>

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<b>Thermal summary:</b>	
<b>Estimated junction temperature:</b>	<b>90C</b>
<b>Ambient temp:</b>	<b>25C</b>
<b>Case temp:</b>	<b>89C</b>
<b>Theta J-A range:</b>	<b>12 - 12C/W</b>

<b>Decoupling Network Summary:</b>	<b>Cap Range (uF)</b>	<b>#</b>
<b>Capacitor Recommendations:</b>		
Total for Vccint		28
	470.0-1000.0	1
	4.70- 10.00	1
	0.470- 2.200	3
	0.0470-0.2200	5
	0.0100-0.0470	8
	0.0010-0.0047	10
Total for Vccaux		12
	470.0-1000.0	1
	0.470- 2.200	1
	0.0470-0.2200	2
	0.0100-0.0470	3
	0.0010-0.0047	5
Total for Vcco25		13
	470.0-1000.0	1
	0.470- 2.200	1
	0.0470-0.2200	2
	0.0100-0.0470	4
	0.0010-0.0047	5

**Analysis completed: Mon Oct 09 17:05:39 2006**