

A

A

B

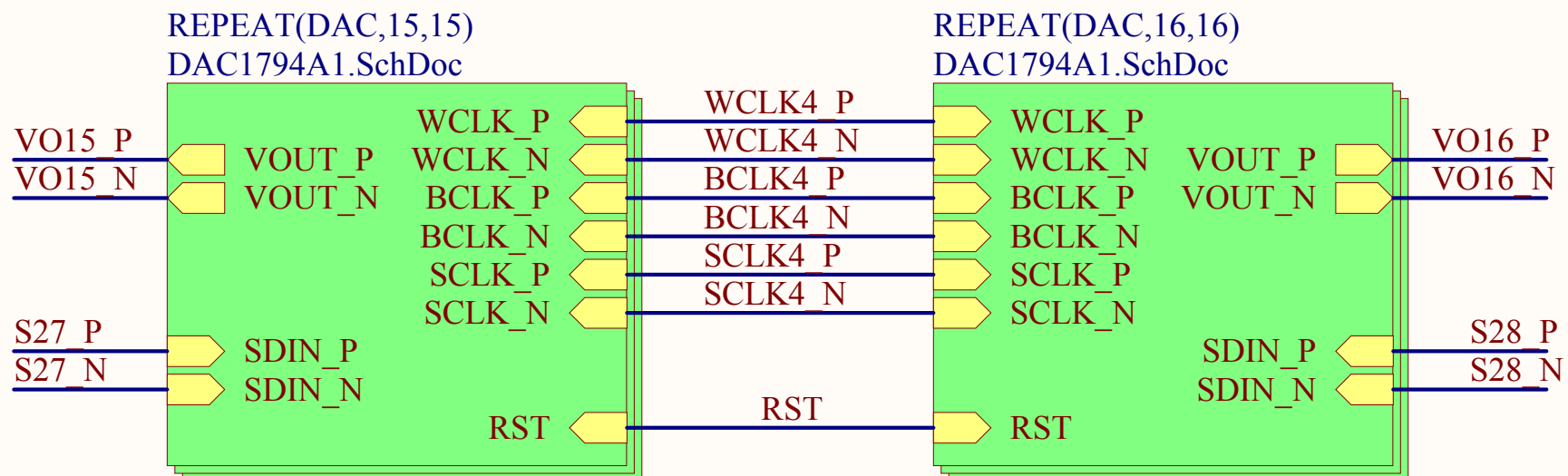
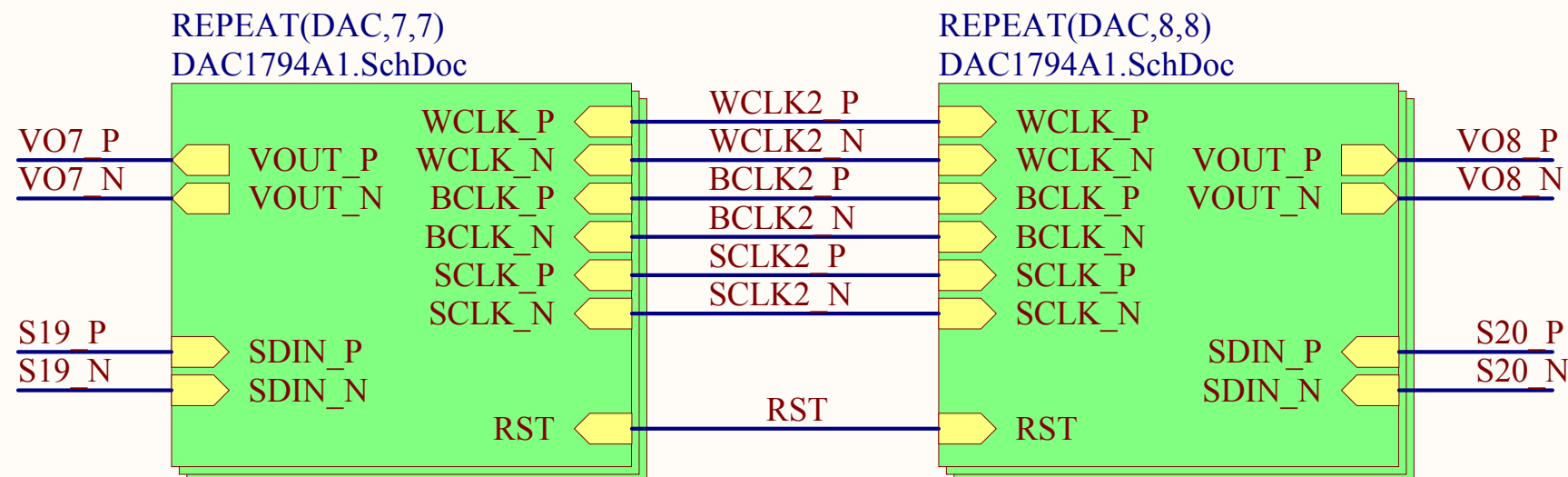
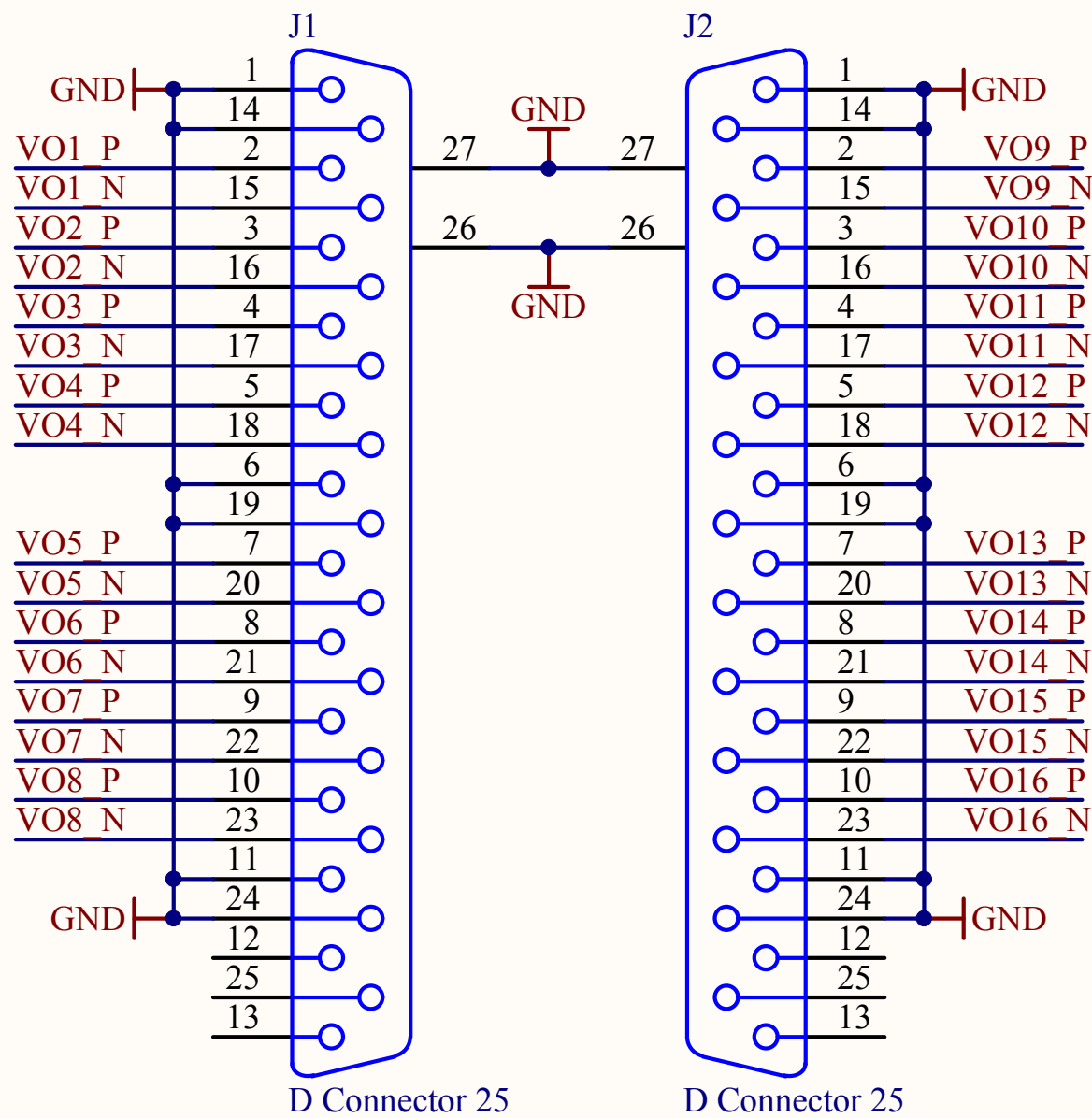
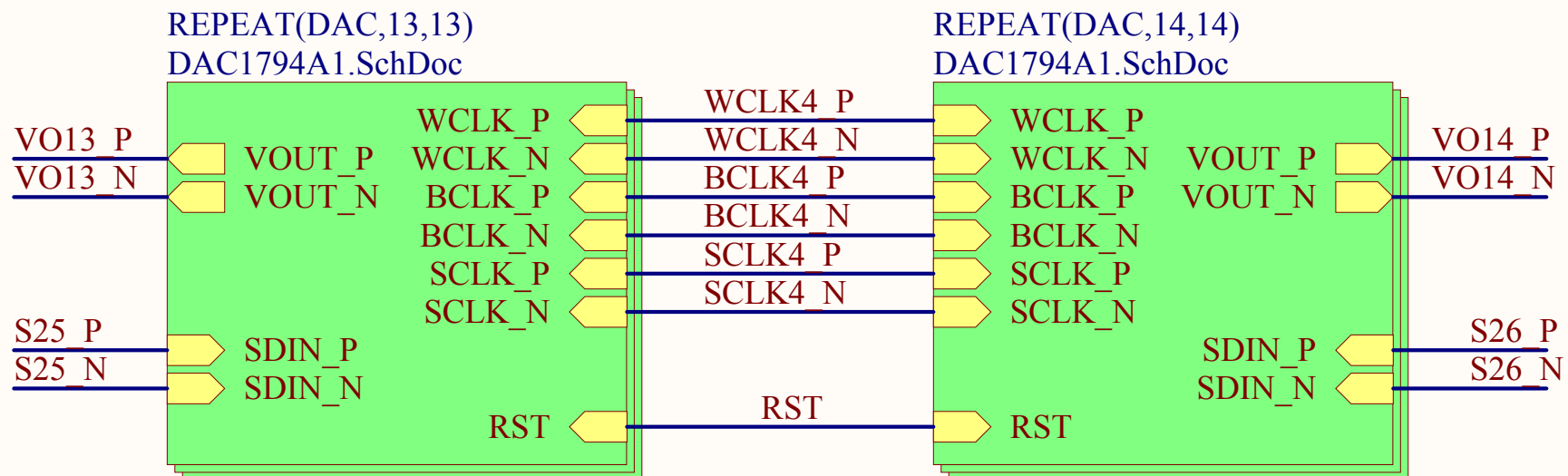
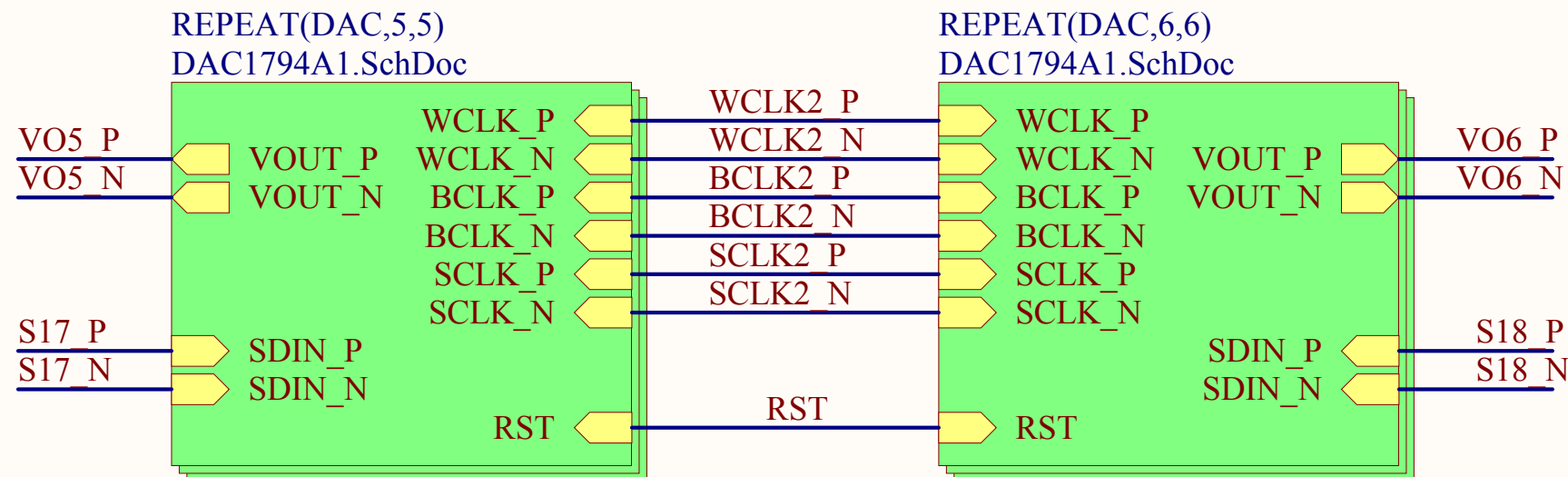
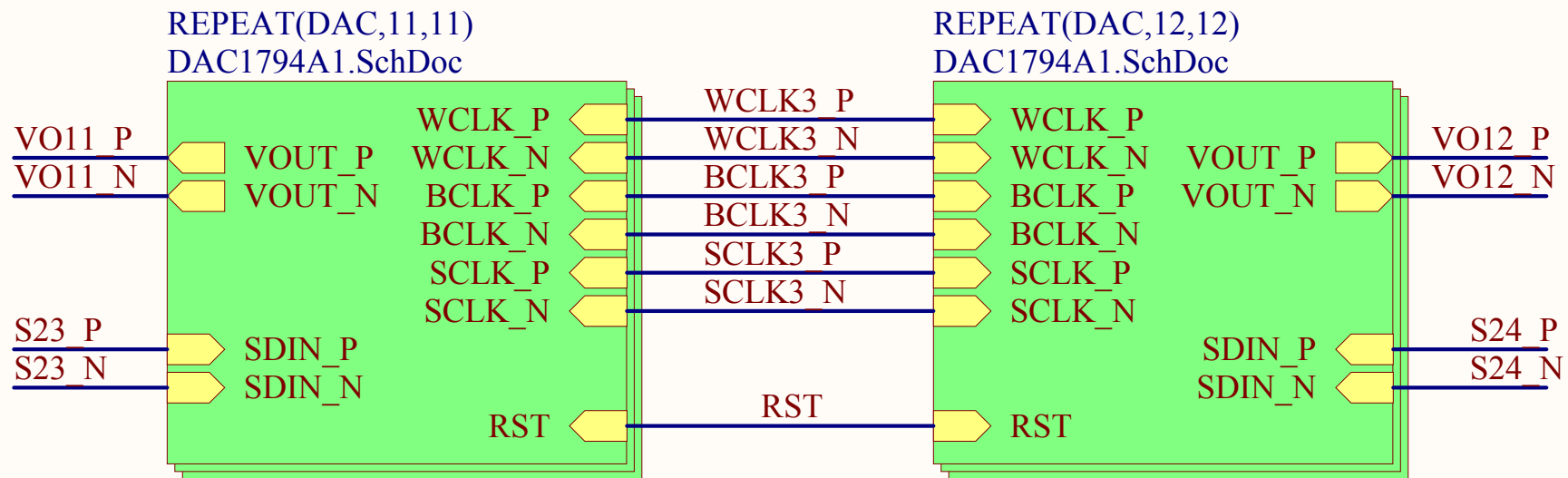
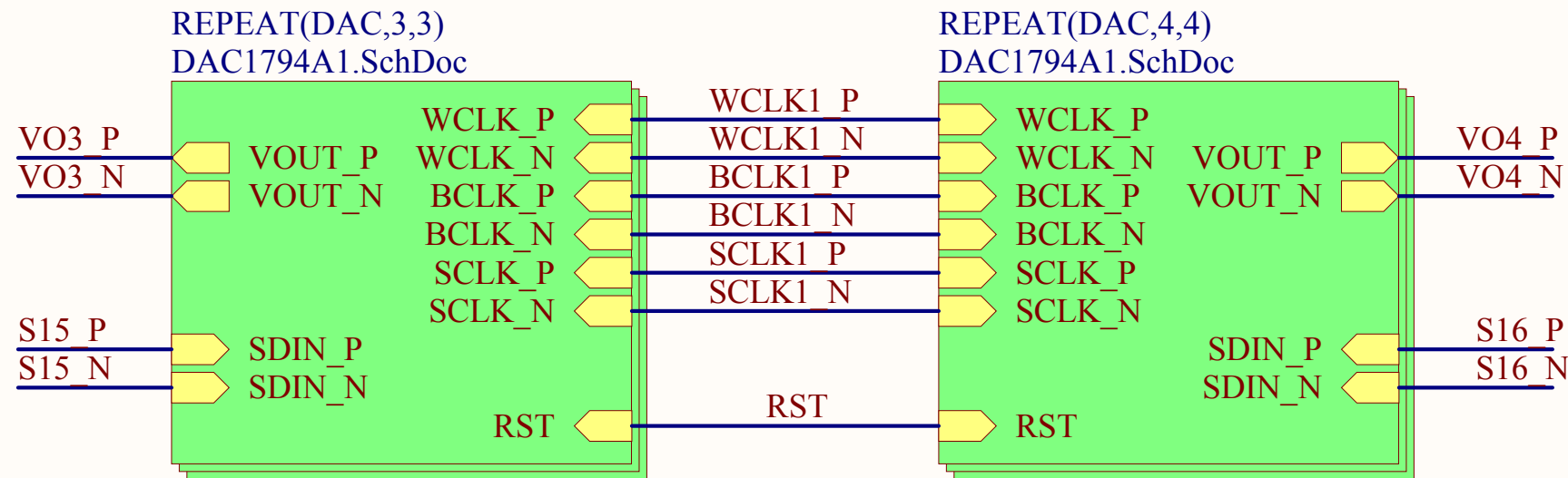
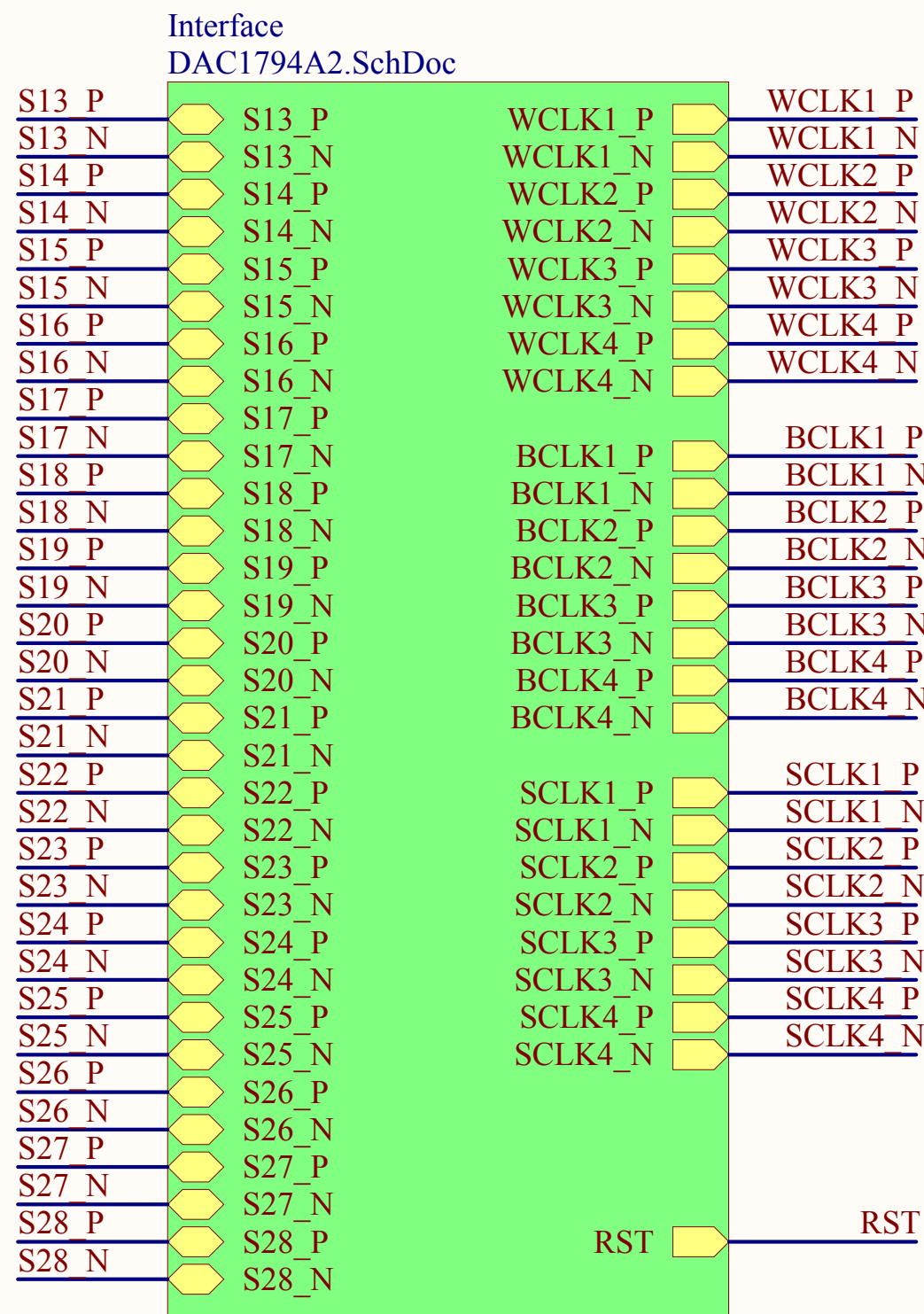
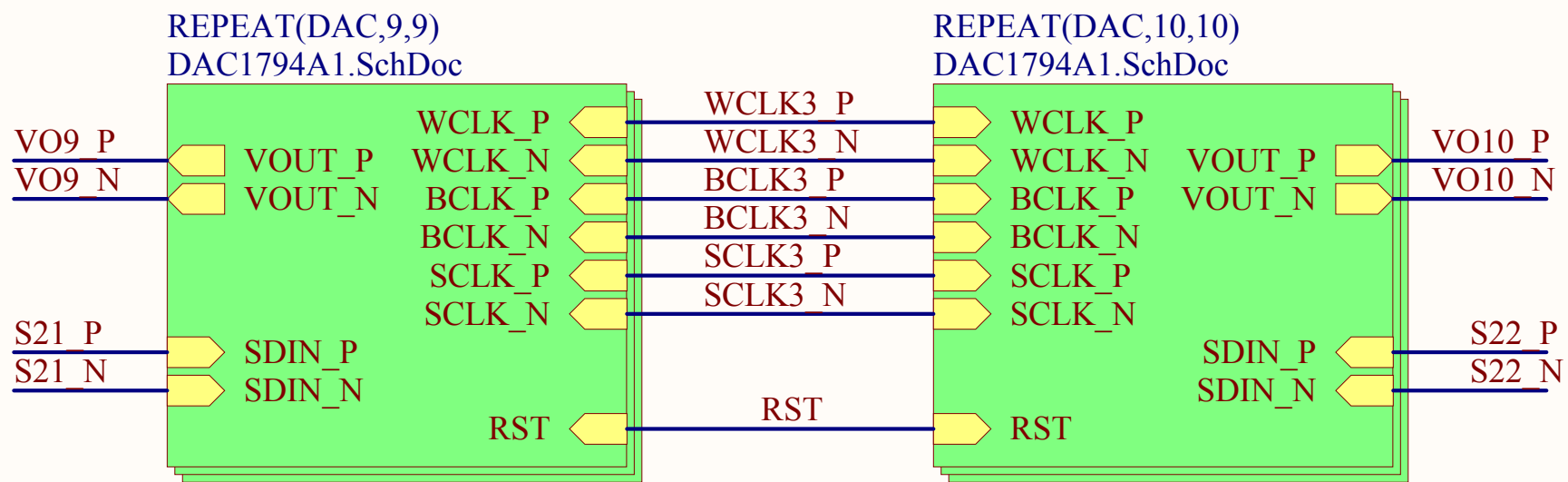
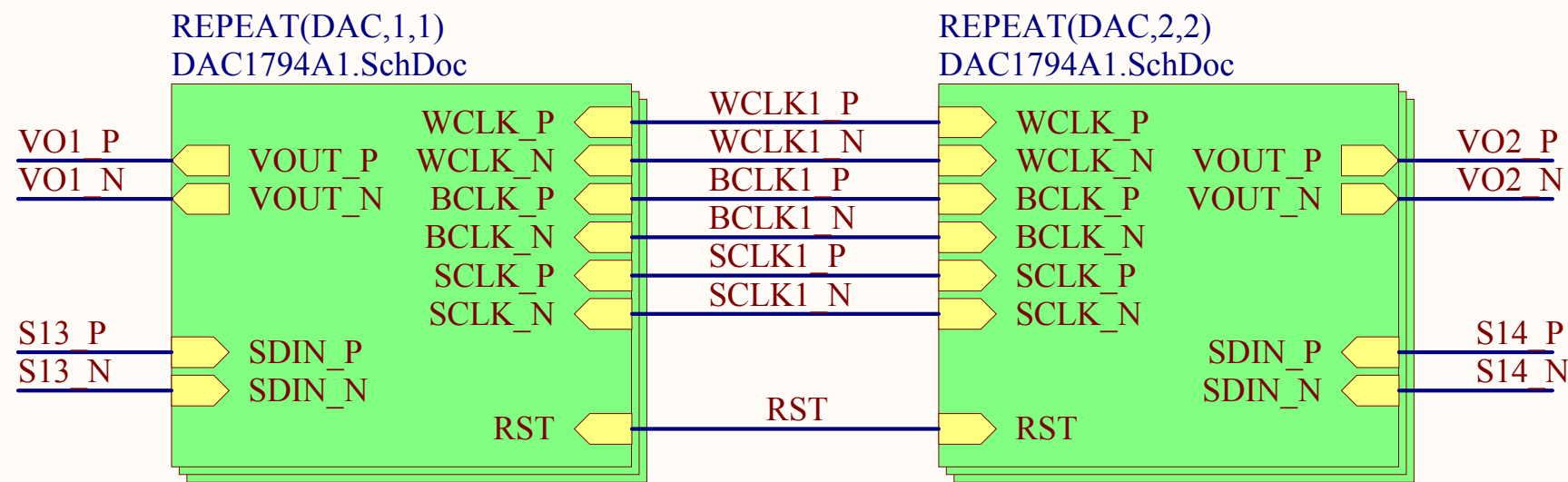
B

C

C

D

D



Title			DAC1794A / Top Sheet	
Size	Number	Revision		
B	D060293-00	A		
Date:	10/5/2006	Sheet4 of	4	
File:	E:\LIGO\DAC1794\DAC1794A4.SchDoc	Drawn By:	Paul Schwinberg	

1

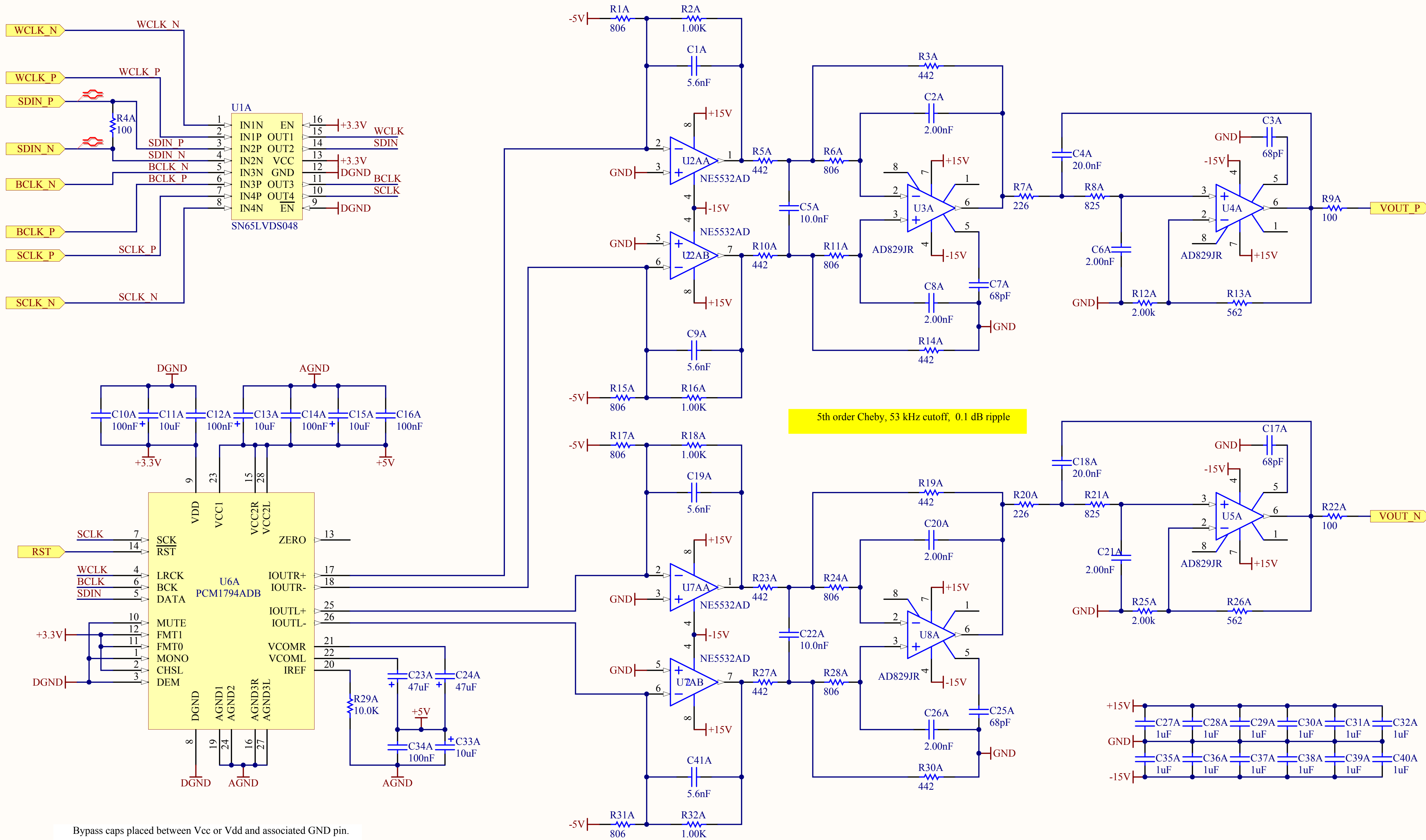
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3

4

5

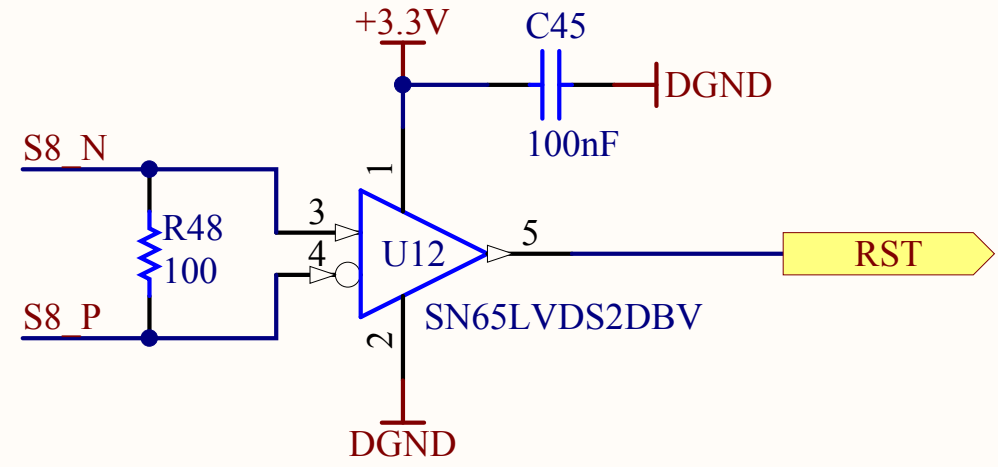
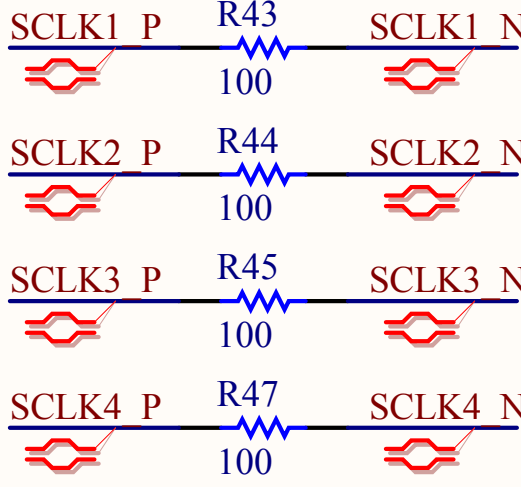
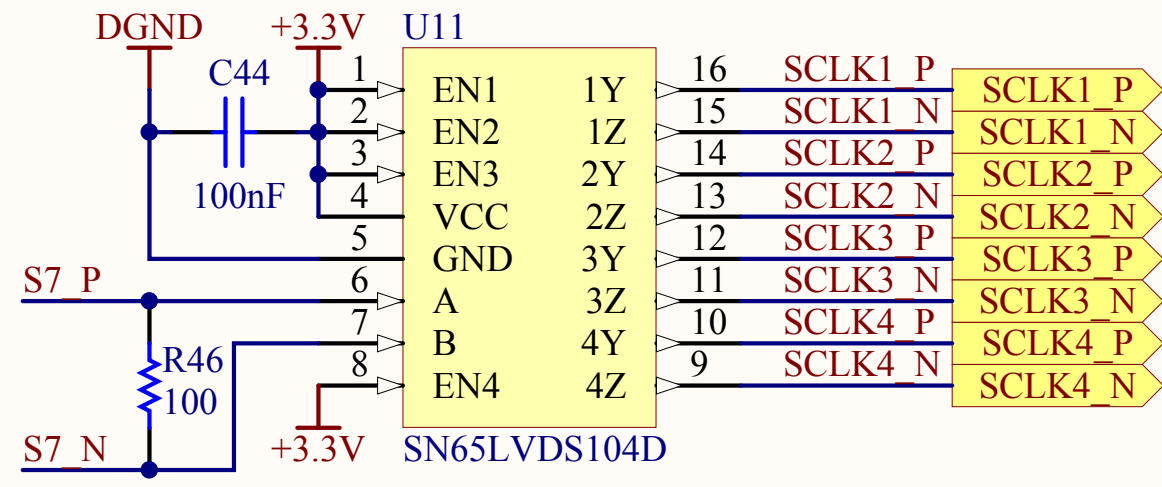
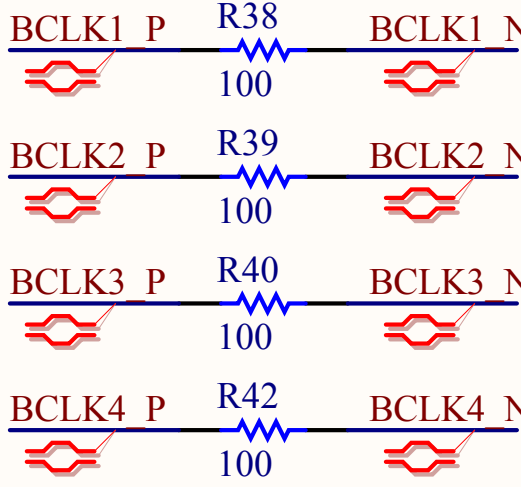
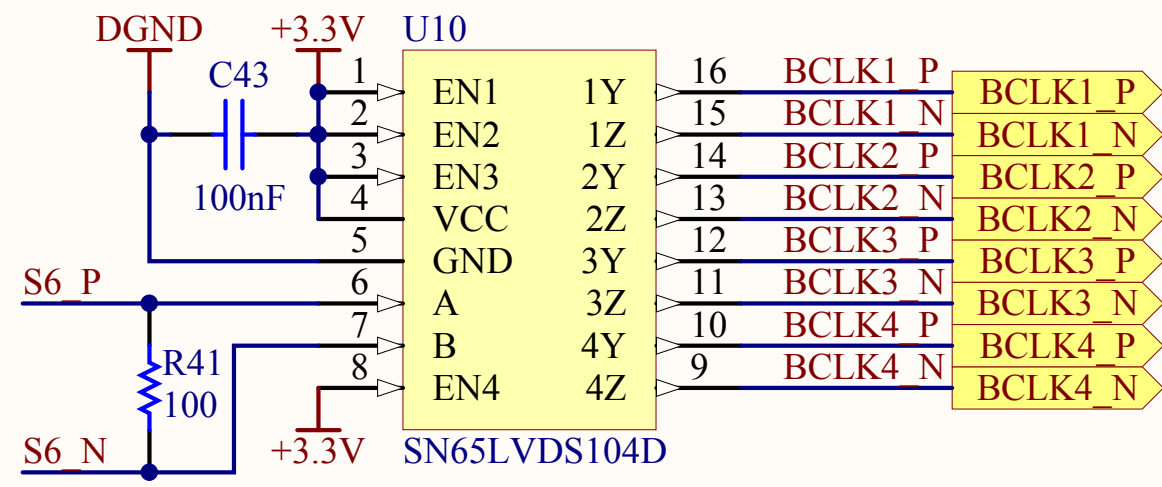
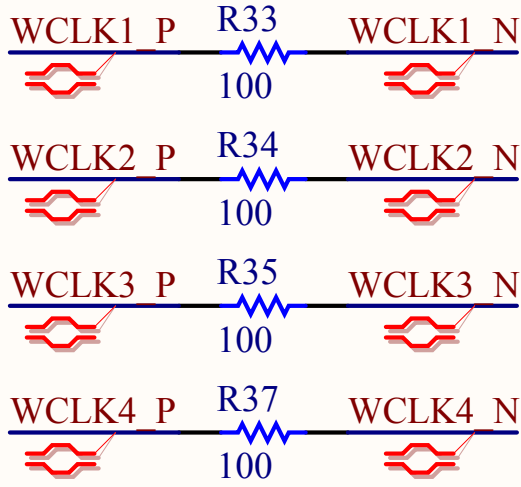
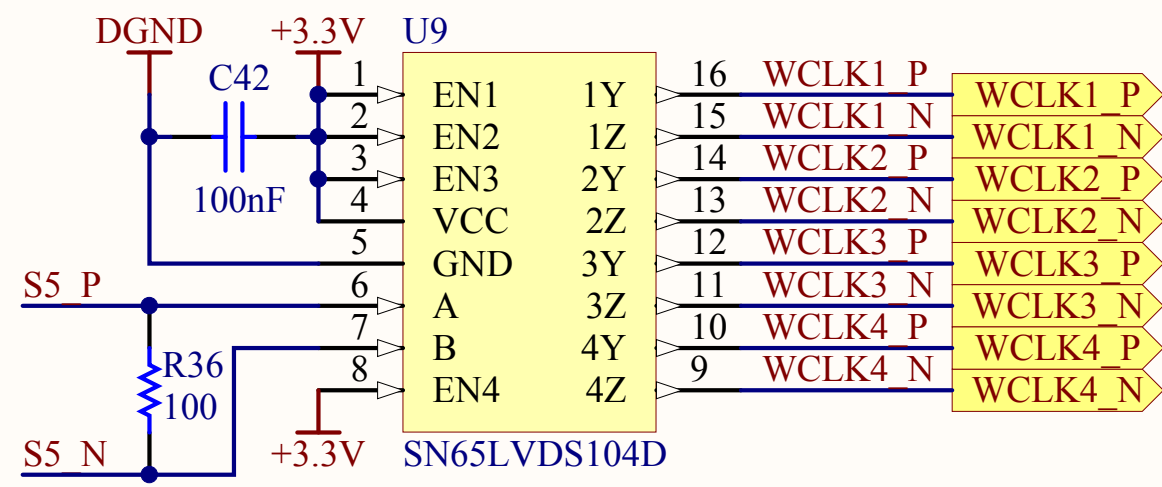
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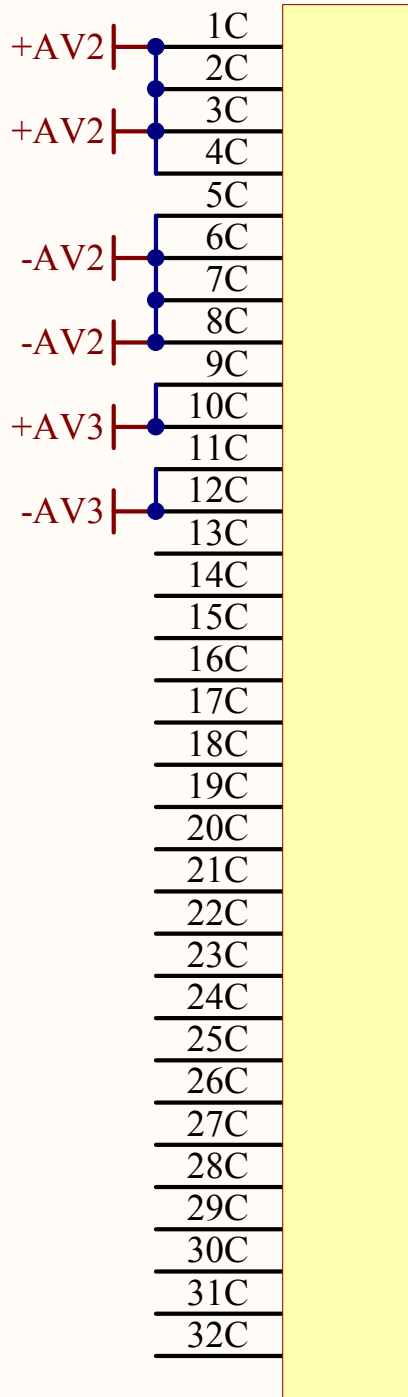
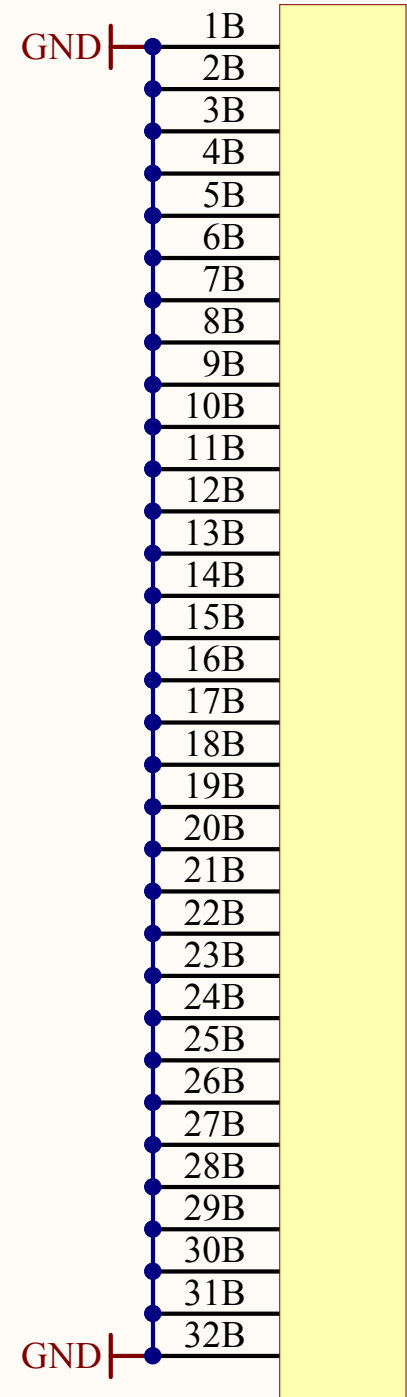
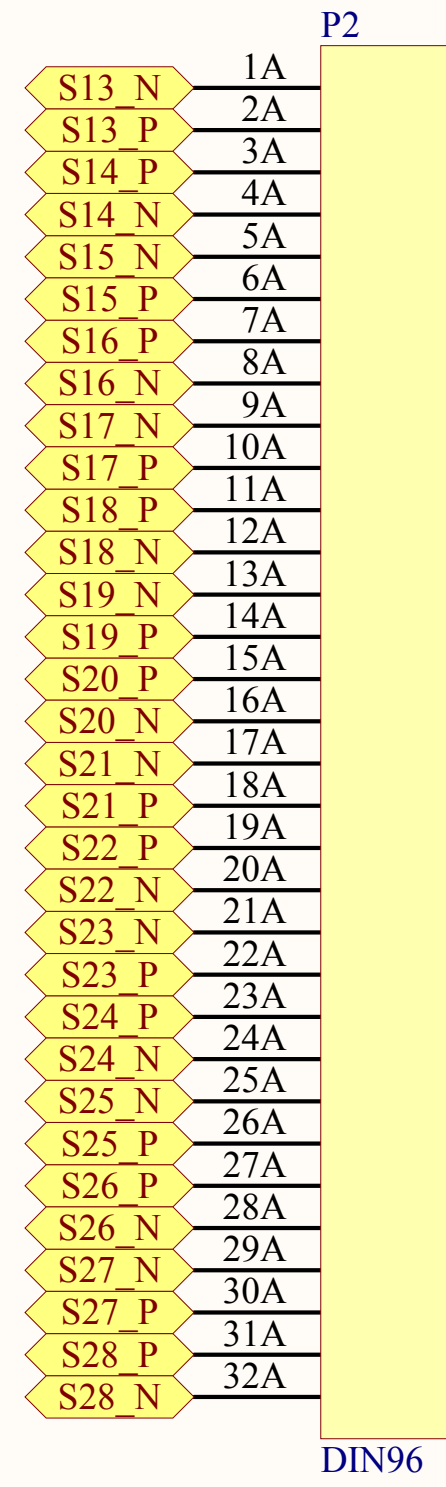
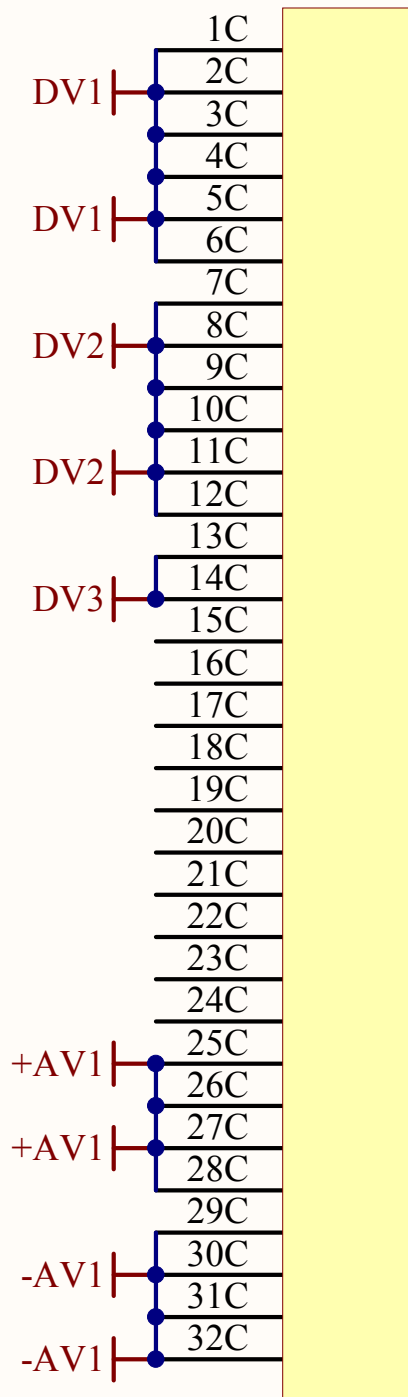
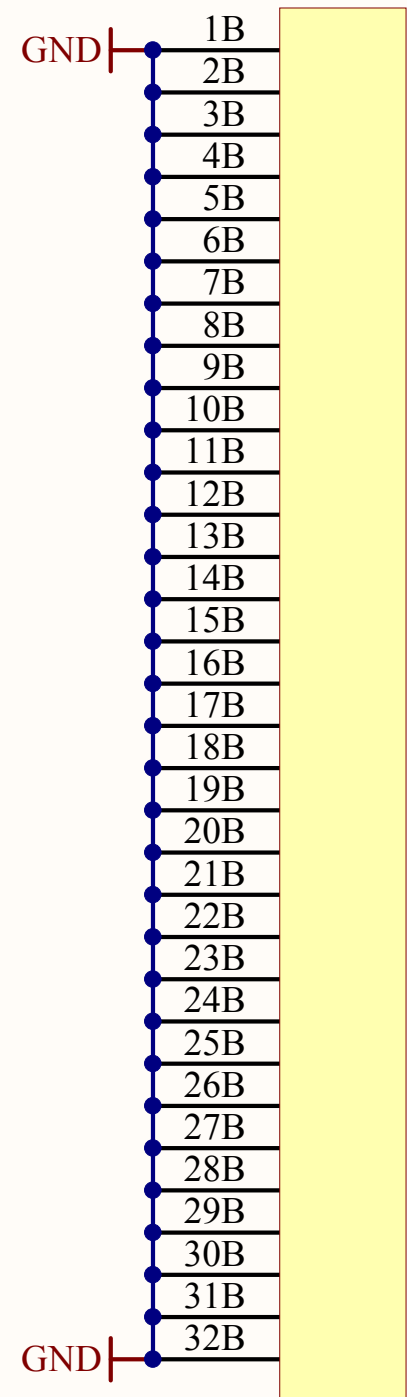
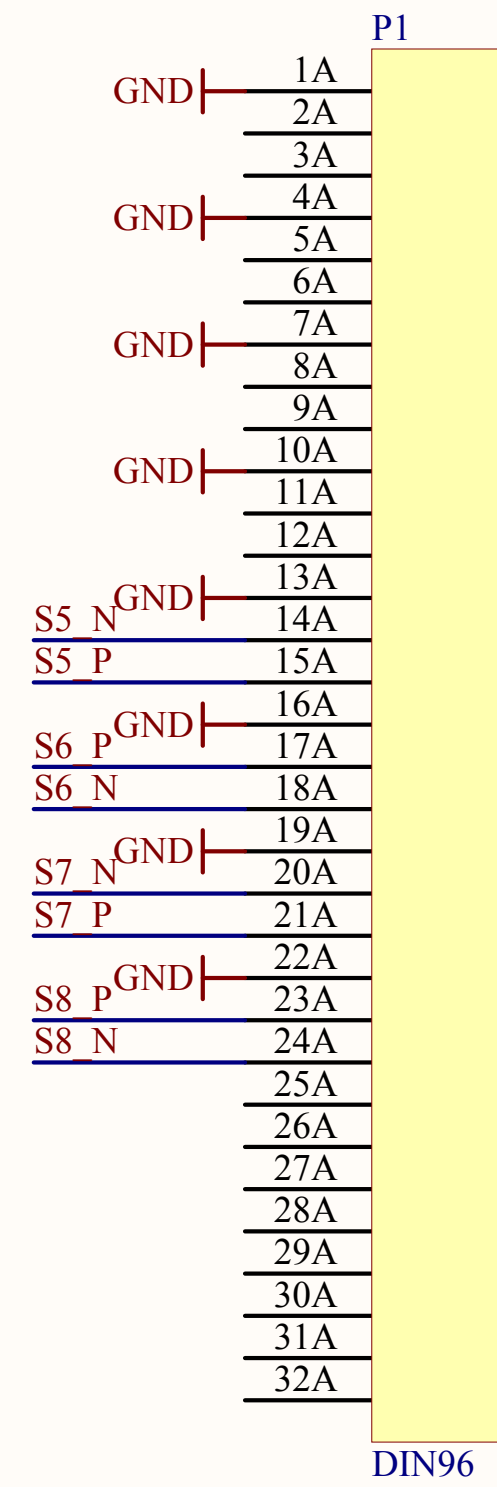
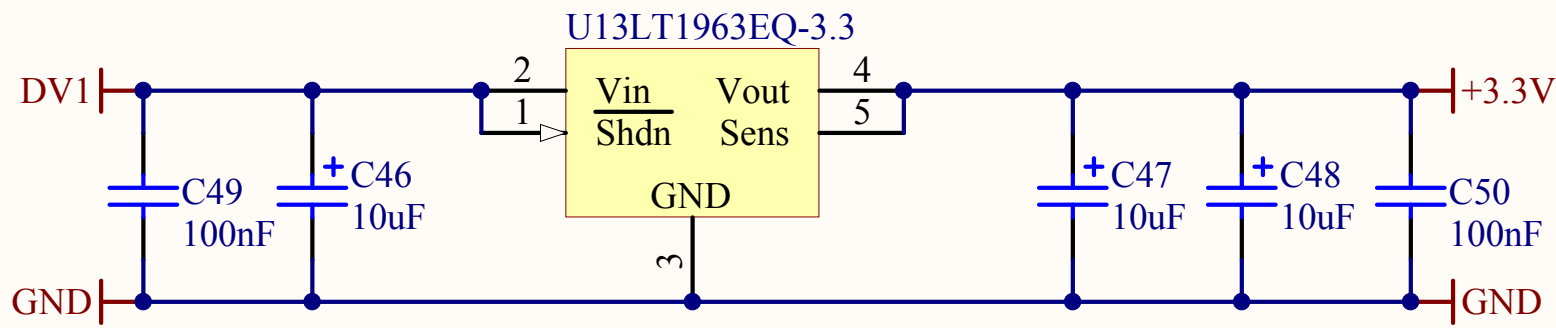
Bypass caps placed between Vcc or Vdd and associated GND pin.

Title			DAC1794A / DAC Channel	
Size	Number	Revision		
B	D060293-00	A		
Date:	10/5/2006	Sheet 1 of 4		
File:	E:\LIGO\DAC1794\DAC1794A1.SchDoc	Drawn By: Paul Schwinberg		

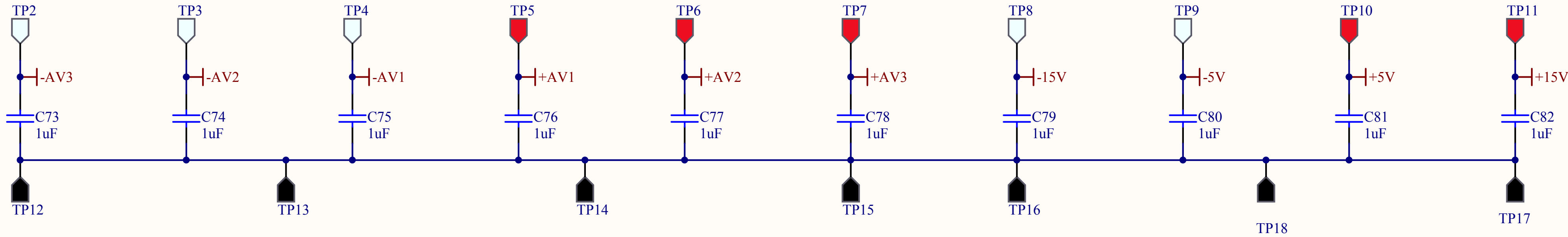
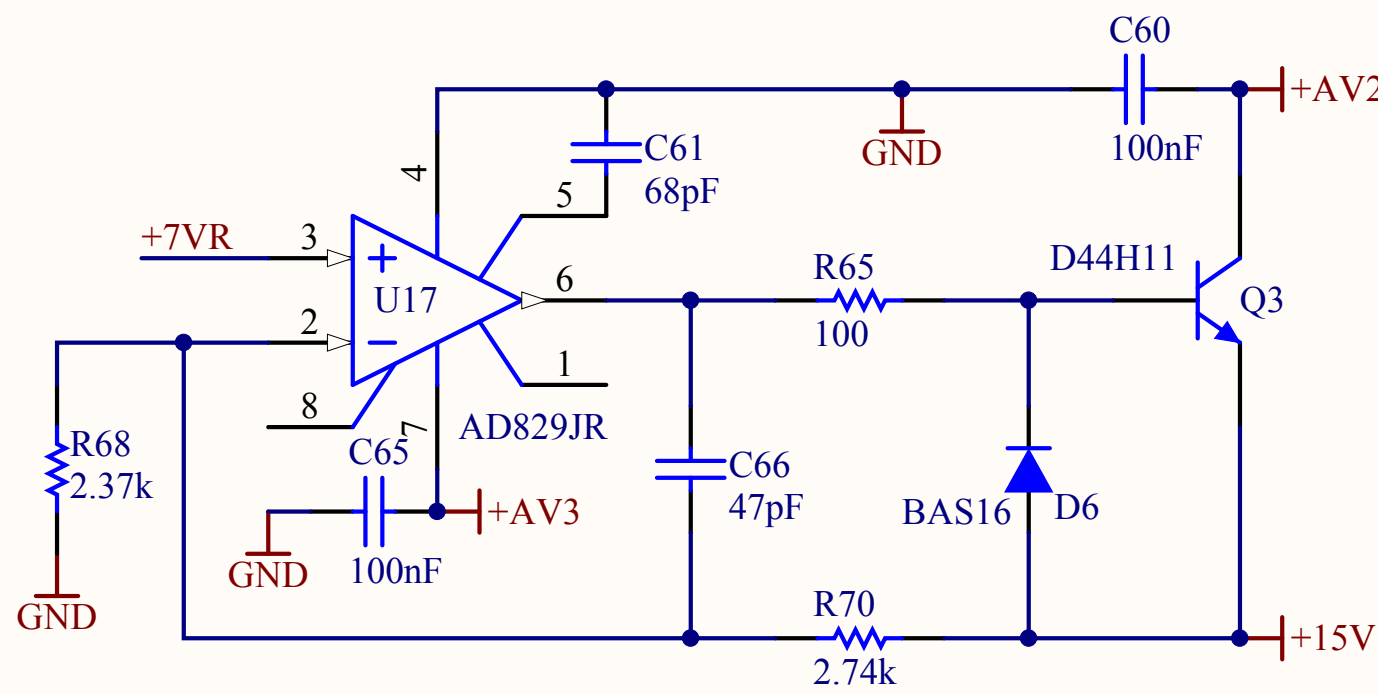
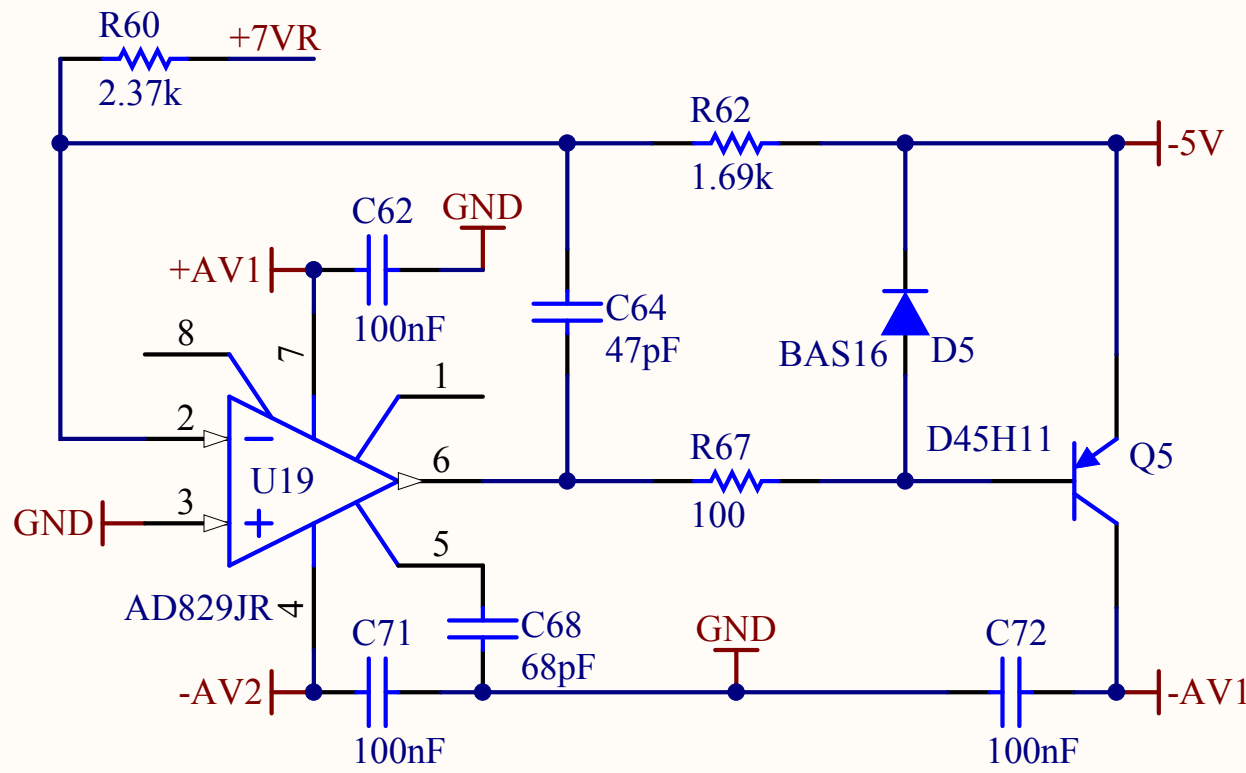
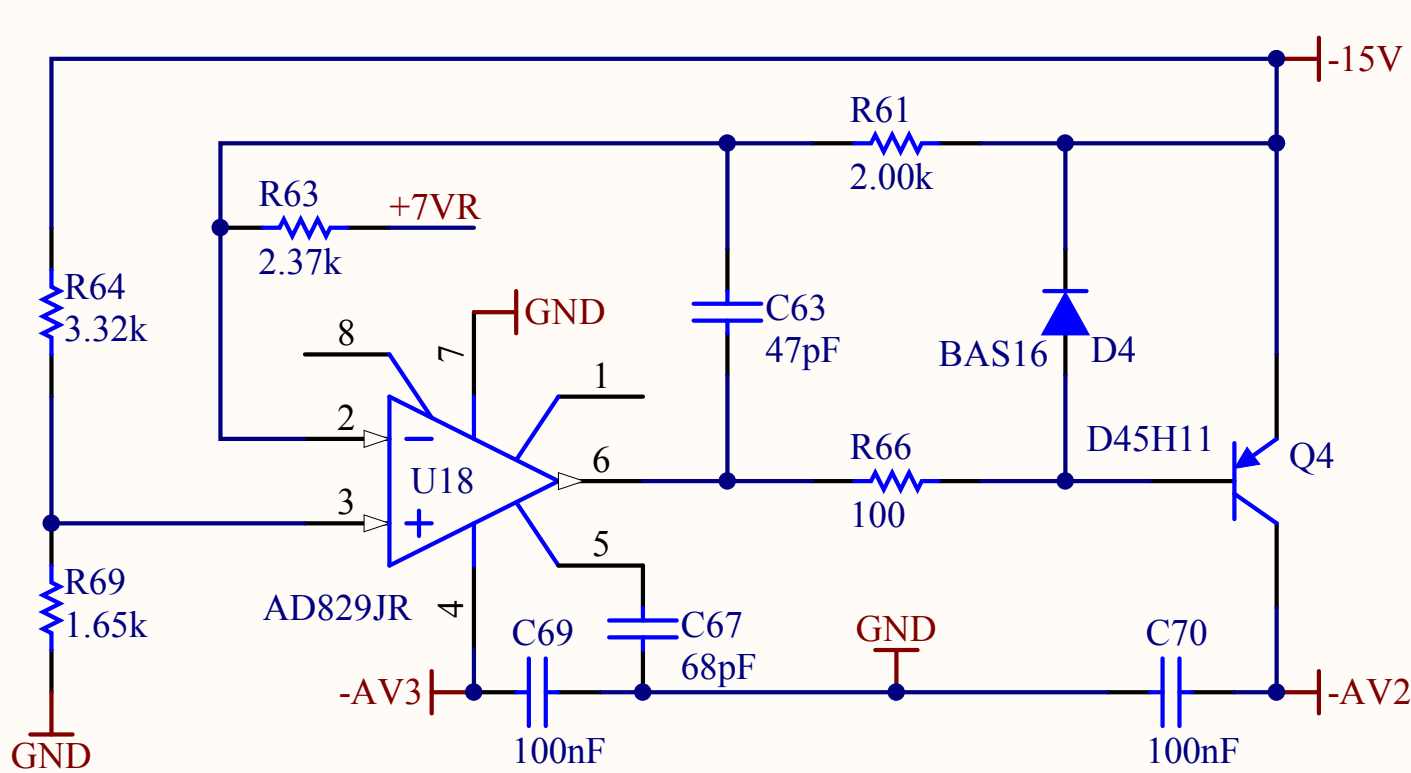
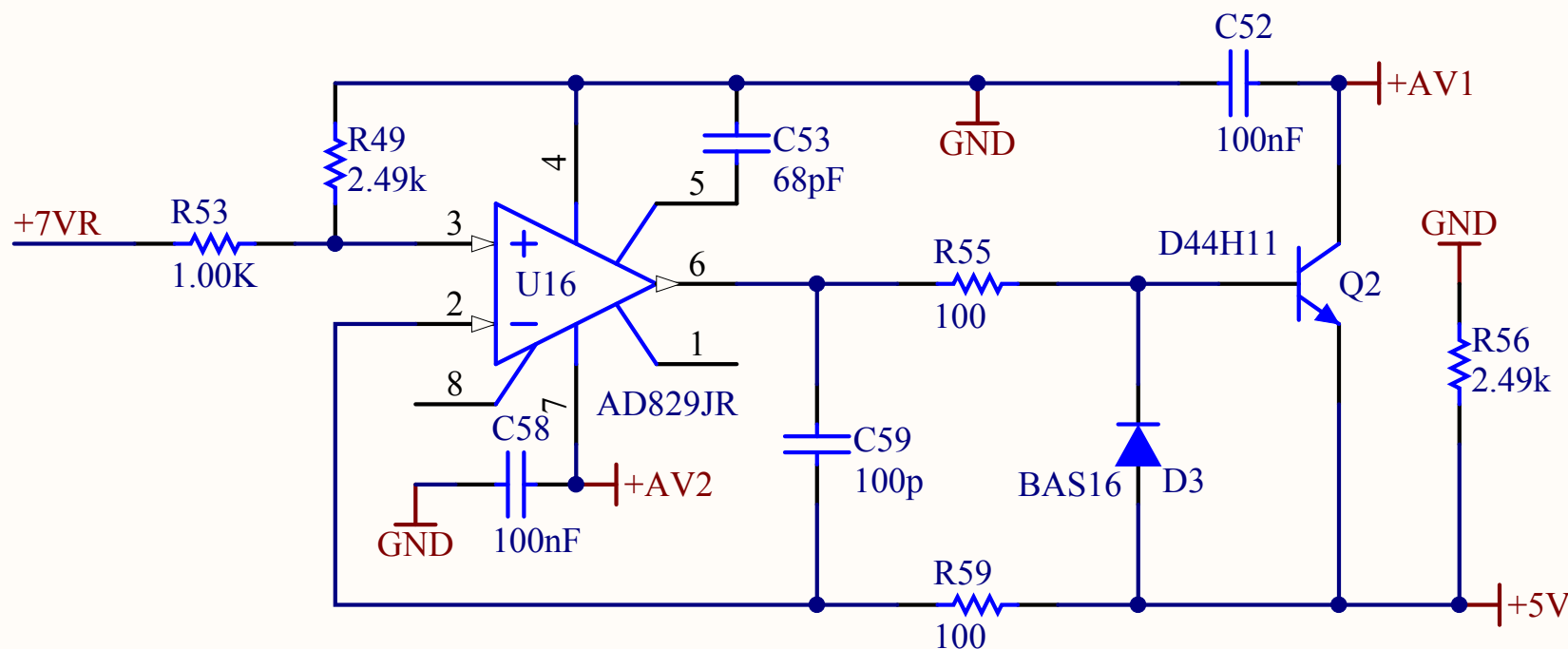
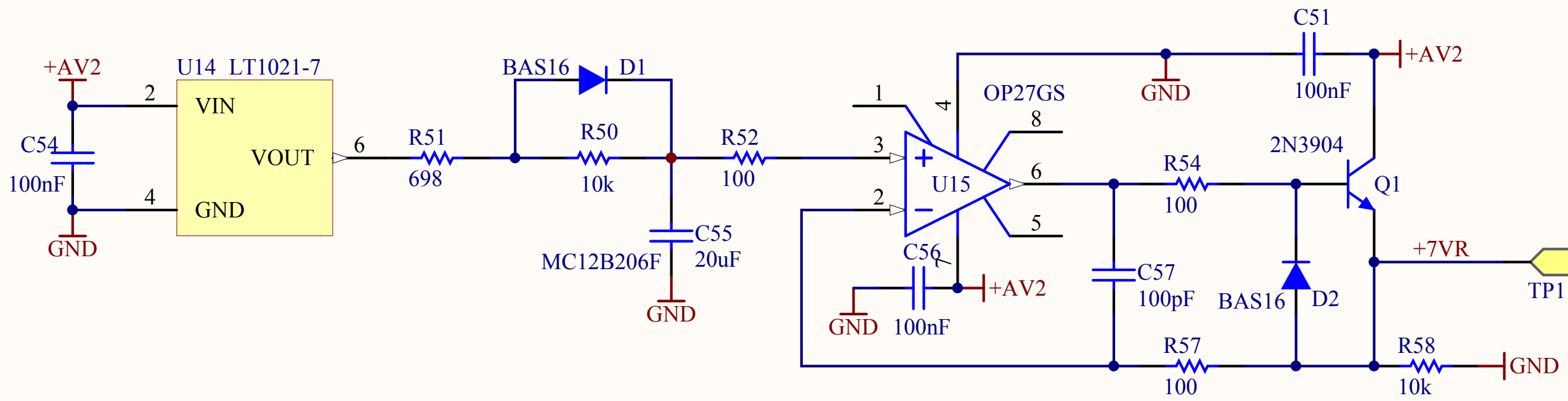




Analog Power  
DAC1794A3.SchDoc



Title <b>DAC1794A / Interface</b>		
Size B	Number <b>D060293-00</b>	Revision <b>A</b>
Date:	10/5/2006	Sheet2 of 4
File:	E:\LIGO\DAC1794\DAC1794A2.SchDoc	Drawn By: Paul Schwinberg



Title			DAC1794A / Analog Power Regulators	
Size	Number	Revision		
B	D060293-00	A		
Date:	10/5/2006	Sheet3 of 4		
File:	E:\LIGO\DAC1794\DAC1794A3.SchDoc	Drawn By: Paul Schwinberg		