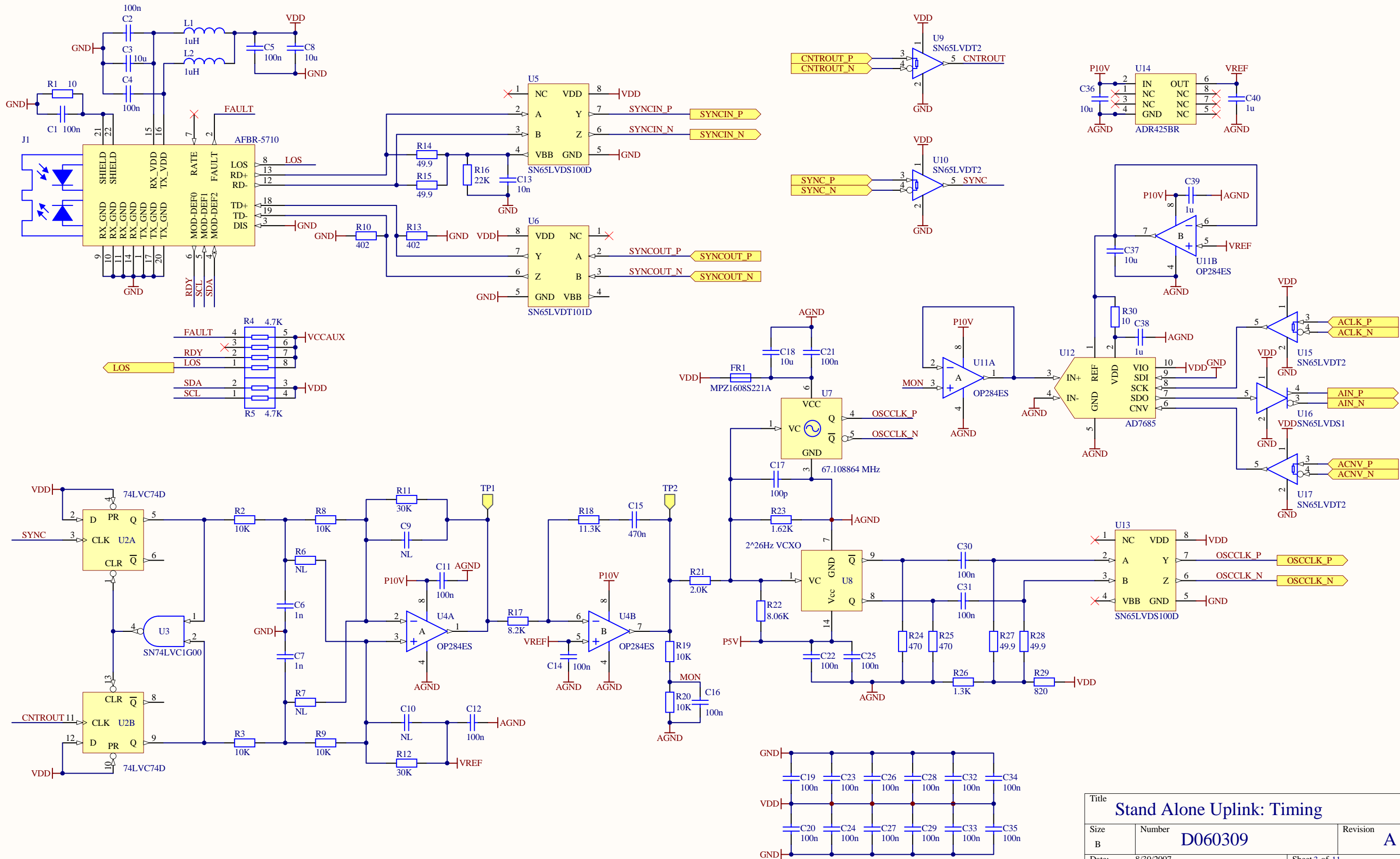


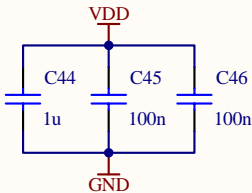
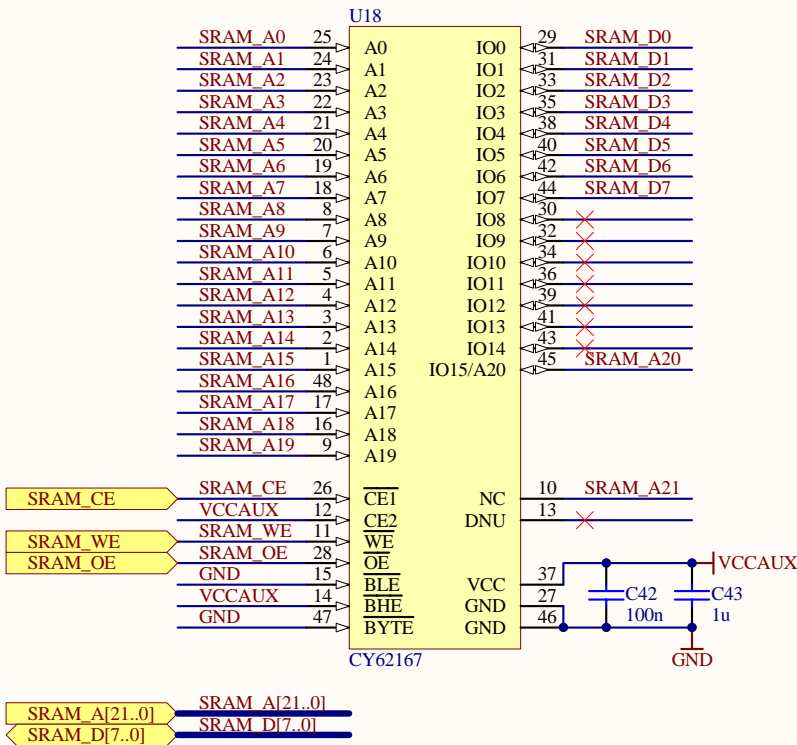
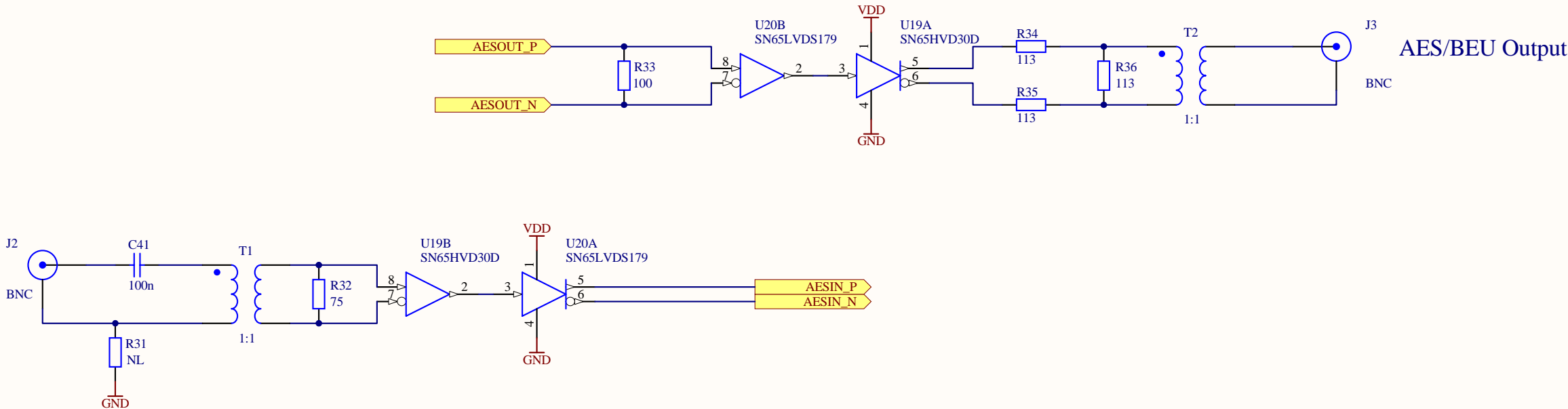
Title		
Stand Alone Uplink: Top		
Size	Number	Revision
B	D060309	A
Date:	8/30/2007	Sheet 1 of 11
File:	C:\User\...\SAUplink1.SchDoc	Drawn By: Daniel Sigg





Title Stand Alone Uplink: Timing		
Size B	Number D060309	Revision A
Date: 8/30/2007	Sheet 3 of 11	
File: C:\User\...\SAUplink2.SchDoc	Drawn By: Daniel Sigg	

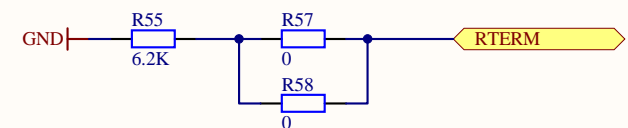
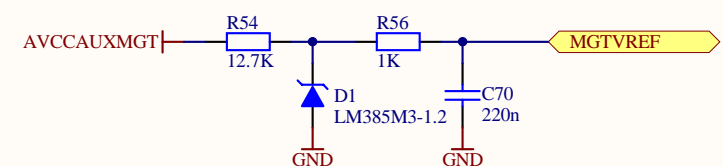
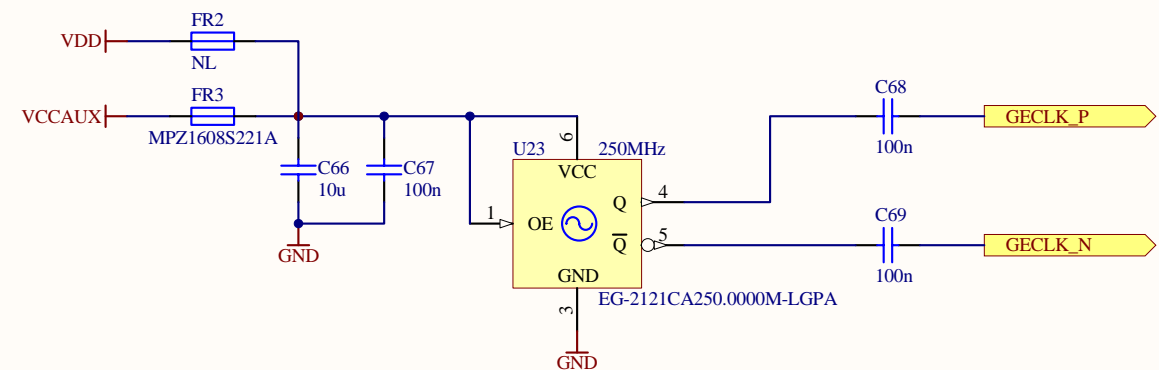
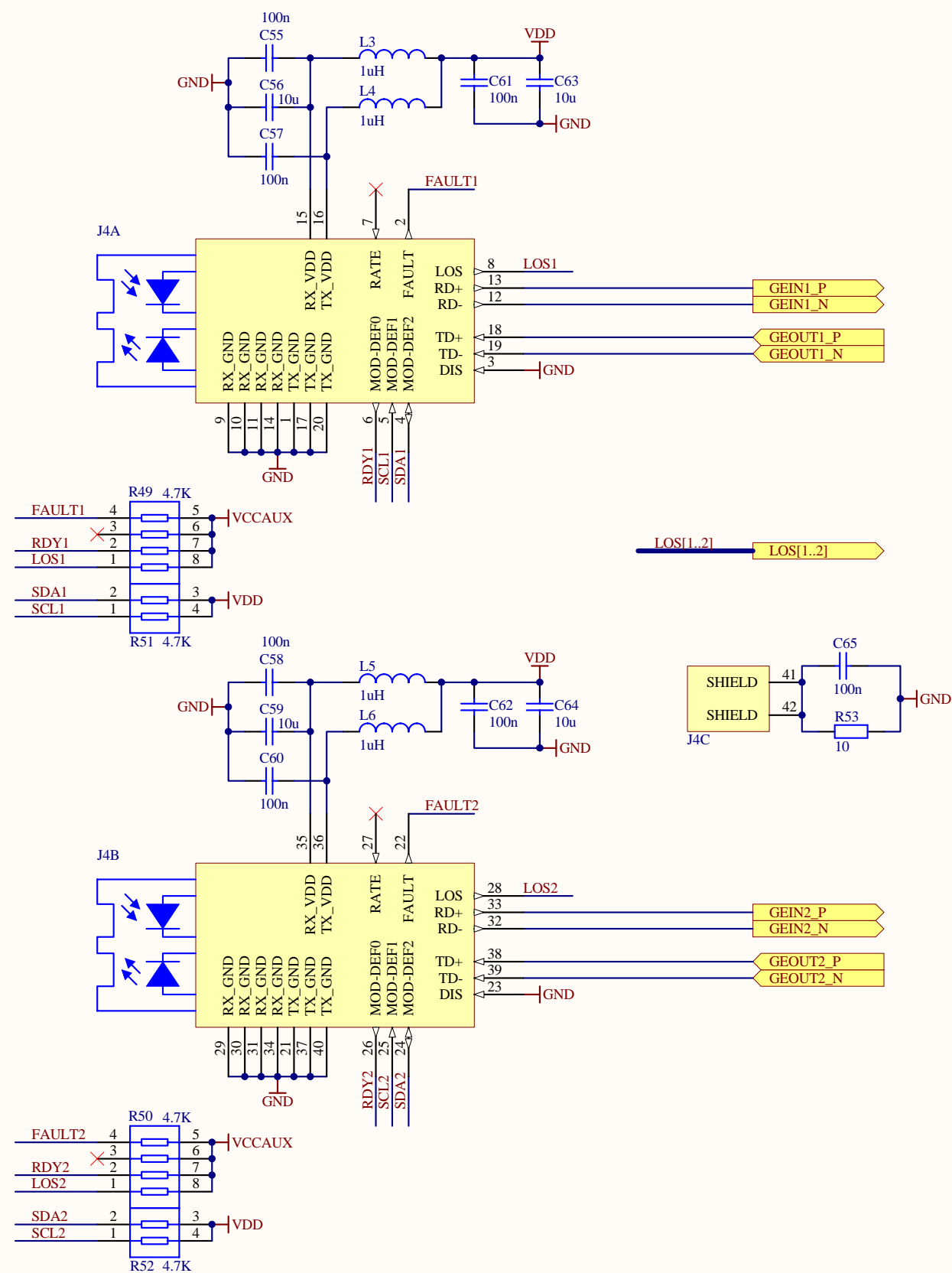
AES/BEU Input



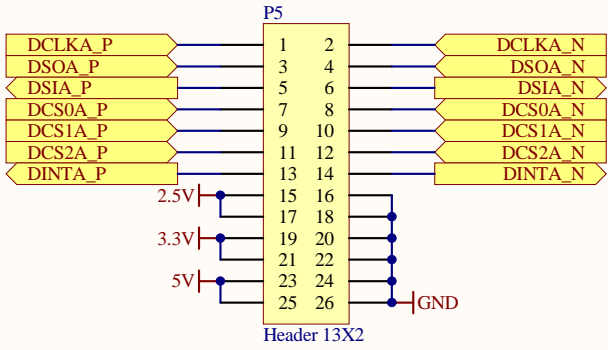
Stand Alone Uplink: AES3/SDRAM

Size B Number D060309 Revision A

Date: 8/30/2007 Sheet 4 of 11
File: C:\User\...\SAUplink3.SchDoc Drawn By: Daniel Sigg



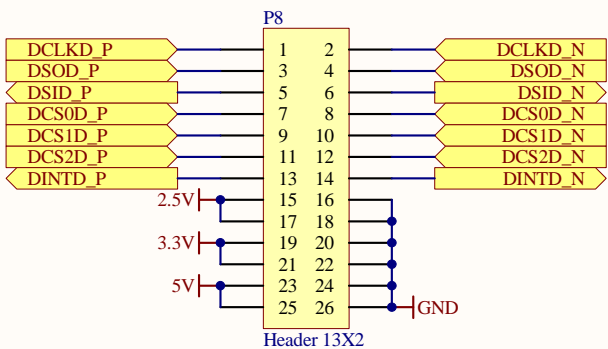
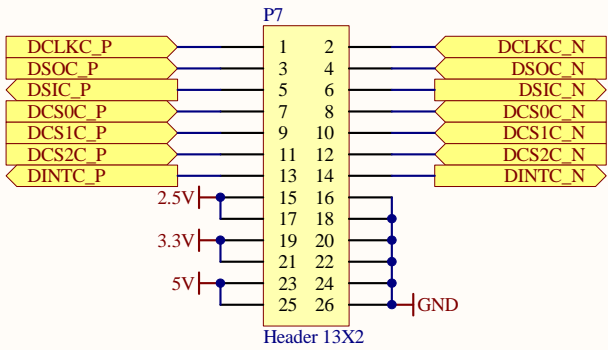
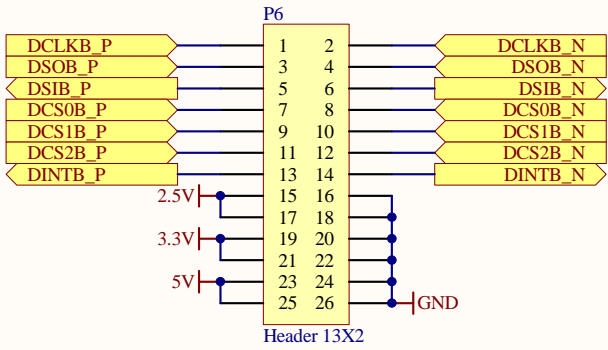
Title Stand Alone Uplink: Gigabit Links		
Size B	Number D060309	Revision A
Date:	8/30/2007	Sheet 6 of 11
File:	C:\User\...\SAUplink5.SchDoc	Drawn By: Daniel Sigg



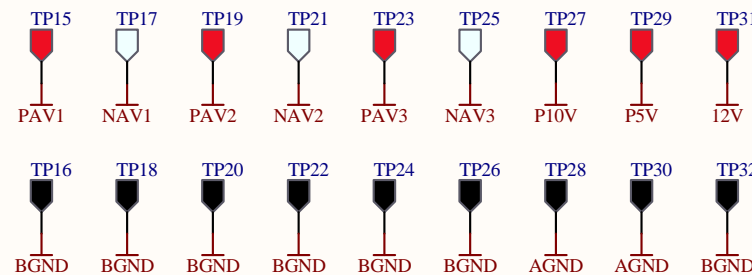
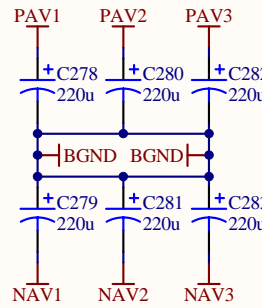
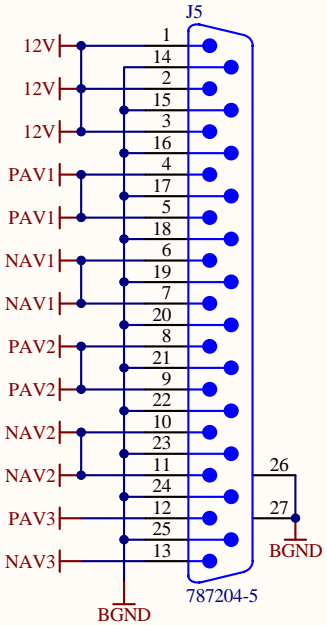
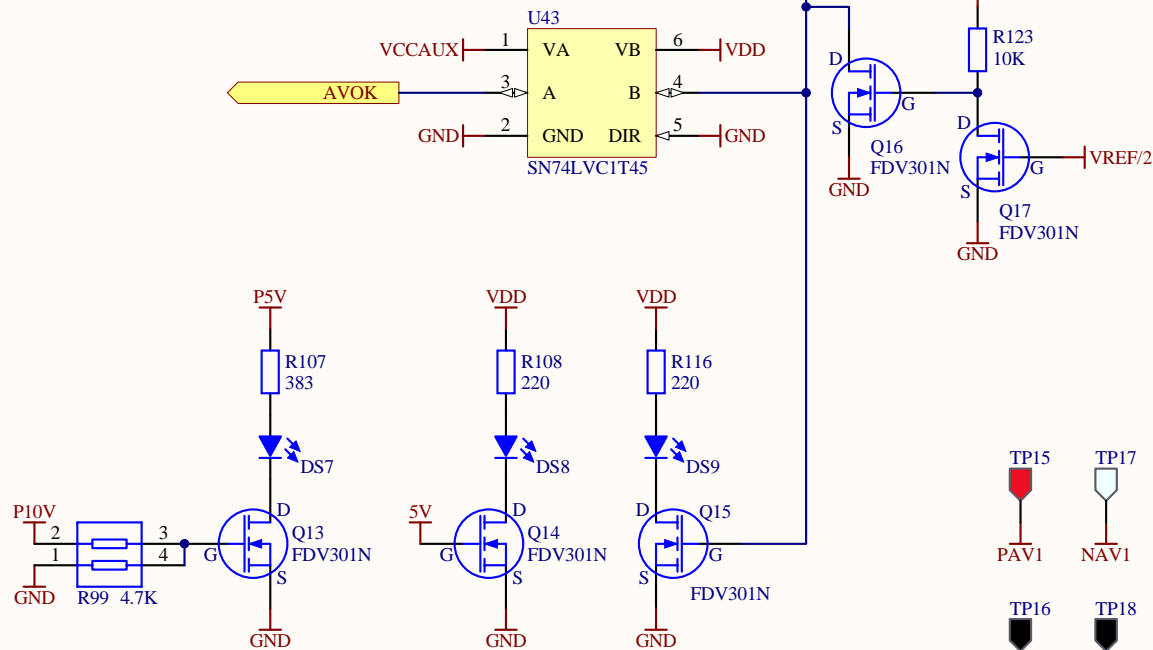
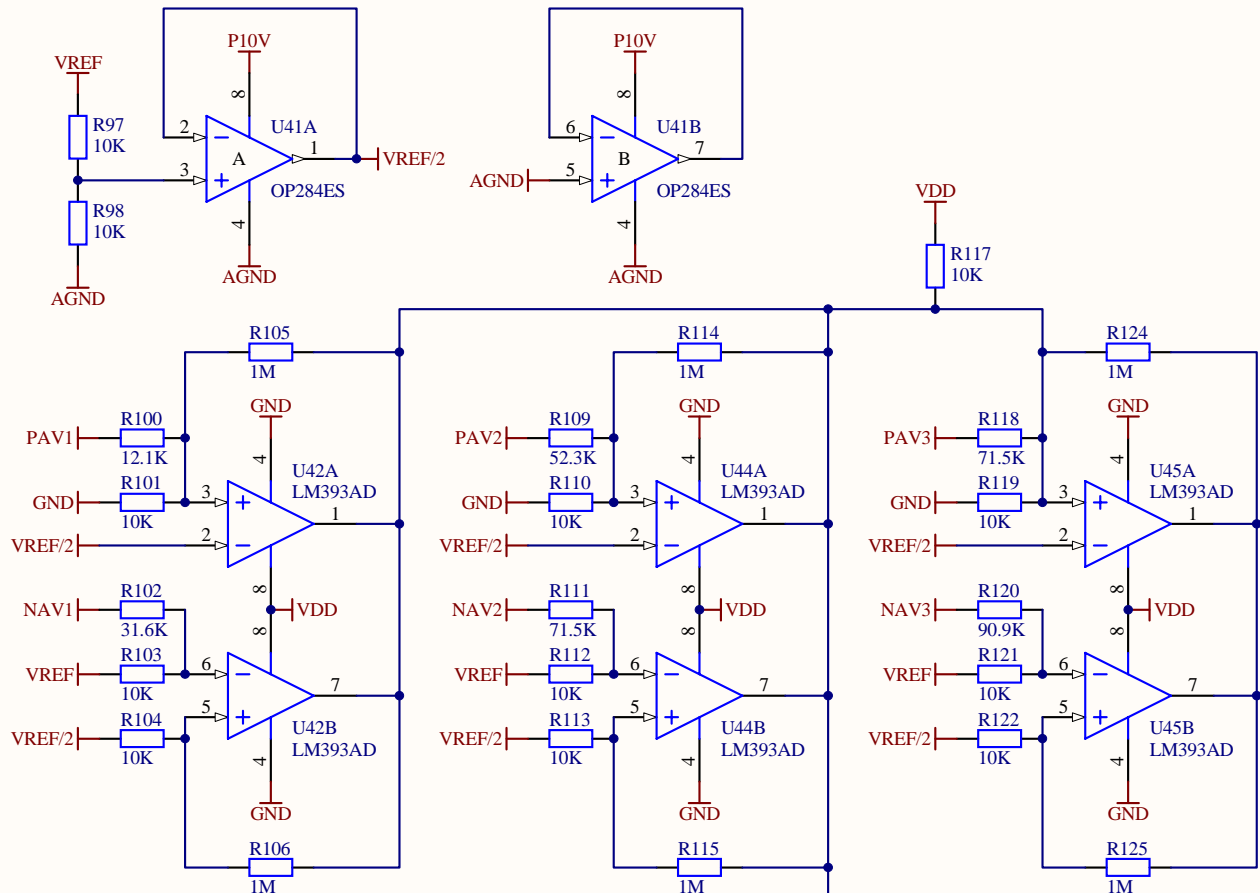
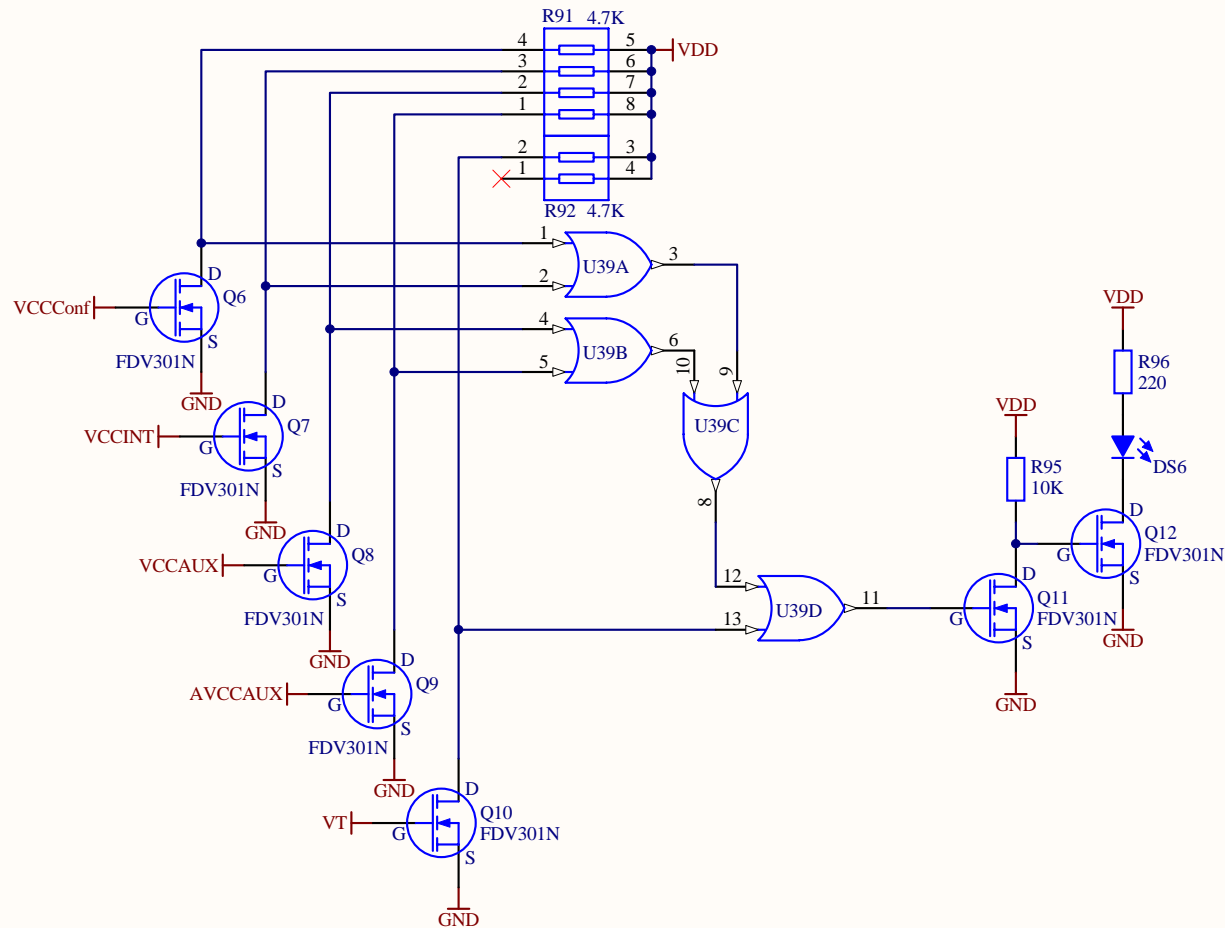
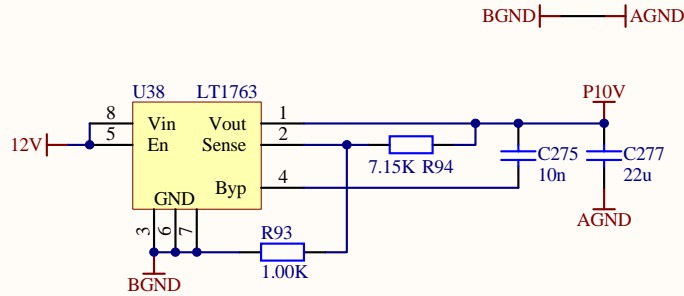
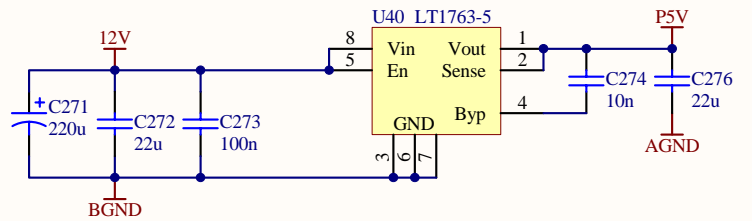
4 SPI Interfaces

CLK: SPI clock line
SO: serial data out
SI: serial data in
CS[2..0]: chip select
INT: interrupt line

Interface with Microship MCP23S17 to
generate binary IO signals



Title Stand Alone Uplink: Binary IO		
Size B	Number D060309	Revision A
Date:	8/30/2007	Sheet 8 of 11
File:	C:\User\...\SAUplink7.SchDoc	Drawn By: Daniel Sigg



Title		
Stand Alone Uplink: Analog Power		
Size	Number	Revision
B	D060309	A
Date:	8/30/2007	Sheet 1 of 11
File:	C:\User\...\SAUplink10.SchDoc	Drawn By: Daniel Sigg