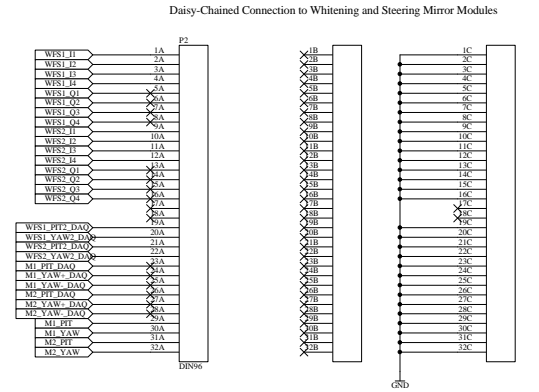
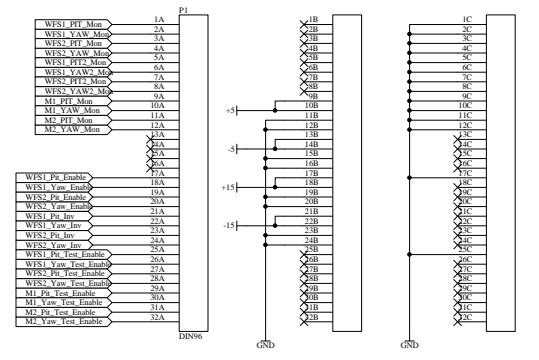
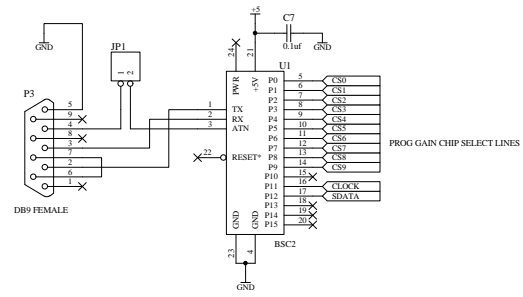
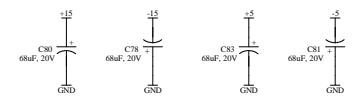


Revision	Description of Change	Date
01	1. R70 and R75 are removed from original design to correct Pitch calculation. 2. R97 rewired on board from original design to correct Yaw calculation. R98 and R68 are removed. 3. R56 and R61 are removed from original design to correct Pitch calculation. 4. R95 rewired on board from original design to correct Yaw calculation. R96 and R54 are removed. 5. Radd1, Radd2, Radd3, Radd4 added to original design as pull downs for U11 switch outputs.	6/99
A	1. All changes described in Rev 01 implemented 2. Jumpers JP1-4 and corresponding resistors added to allow field changes to output matrix calculation.	6/99



MC Servo Amp Rev A
 MC Servo Amp Rev A Sch

- CS0
- SDATA
- CLOCK
- CS1
- SDATA
- CLOCK
- CS2
- SDATA
- CLOCK
- CS3
- SDATA
- CLOCK
- CS4
- SDATA
- CLOCK
- CS5
- SDATA
- CLOCK
- CS6
- WIS1_Pi
- WIS1_Yaw
- WIS2_Pi
- WIS2_Yaw
- WIS1_I1
- WIS1_I2
- WIS1_I3
- WIS1_I4
- WIS2_I1
- WIS2_I2
- WIS2_I3
- WIS2_I4
- WIS1_Pi_Enable
- WIS1_Yaw_Enable
- WIS2_Pi_Enable
- WIS2_Yaw_Enable
- WIS1_Pi_Mon
- WIS1_YAW_Mon
- WIS2_Pi_Mon
- WIS2_YAW_Mon

MC Servo Amp Rev A
 MC Servo Amp Rev A Sch

- CS6
- SDATA
- CLOCK
- CS7
- SDATA
- CLOCK
- CS8
- SDATA
- CLOCK
- CS9
- SDATA
- CLOCK
- CS10
- MI_PIT
- MI_YAW
- M2_PIT
- M2_YAW
- WIS1_Pi2
- WIS1_Pi2
- WIS1_Yaw2
- WIS1_Yaw2
- WIS2_Pi2
- WIS2_Pi2
- WIS1_Yaw2
- WIS2_Yaw2
- WIS1_Pi_Inv
- WIS1_Pi
- WIS1_Pi_Test_Enable
- WIS1_Pi2
- WIS1_Yaw_Inv
- WIS1_Yaw
- WIS1_Yaw_Test_Enable
- WIS1_Yaw2
- WIS2_Pi_Inv
- WIS2_Pi
- WIS2_Pi_Test_Enable
- WIS2_Pi2
- WIS2_Yaw_Inv
- WIS2_Yaw
- WIS2_Yaw_Test_Enable
- WIS2_Yaw2
- MI_Pi_Test_Enable
- MI_Yaw_Test_Enable
- M2_Pi_Test_Enable
- M2_Yaw_Test_Enable
- WIS1_PIT2_Mon
- WIS1_YAW2_Mon
- WIS2_PIT2_Mon
- WIS2_YAW2_Mon
- MI_PIT_Mon
- MI_YAW_Mon
- M2_PIT_Mon
- M2_YAW_Mon
- WIS1_PIT2_DAO
- WIS1_YAW2_DAO
- WIS2_PIT2_DAO
- WIS2_YAW2_DAO

MC Servo Amp Rev A
 MC Servo Amp Rev A Sch

- WIS1_I1
- WIS1_I2
- WIS1_I3
- WIS1_I4
- WIS2_I1
- WIS2_I2
- WIS2_I3
- WIS2_I4
- CS1
- MI_PIT
- MI_YAW
- M2_PIT
- M2_YAW
- WIS1_PIT_Mon
- WIS1_YAW_Mon
- WIS2_PIT_Mon
- WIS2_YAW_Mon
- WIS1_PIT2_Mon
- WIS1_YAW2_Mon
- WIS2_PIT2_Mon
- WIS2_YAW2_Mon
- MI_PIT_Mon
- MI_YAW_Mon
- M2_PIT_Mon
- M2_YAW_Mon
- WIS1_Pi_Enable
- WIS1_Yaw_Enable
- WIS2_Pi_Enable
- WIS2_Yaw_Enable
- WIS1_Pi_Inv
- WIS1_Yaw_Inv
- WIS2_Pi_Inv
- WIS2_Yaw_Inv
- WIS1_Pi_Test_Enable
- WIS1_Yaw_Test_Enable
- WIS2_Pi_Test_Enable
- WIS2_Yaw_Test_Enable
- MI_Pi_Test_Enable
- MI_Yaw_Test_Enable
- M2_Pi_Test_Enable
- M2_Yaw_Test_Enable
- WIS1_Q1
- WIS1_Q2
- WIS1_Q3
- WIS1_Q4
- WIS2_Q1
- WIS2_Q2
- WIS2_Q3
- WIS2_Q4
- MI_PIT_DAO
- MI_YAW_DAO
- M2_PIT_DAO
- M2_YAW_DAO
- MI_YAW_DAO
- WIS1_PIT2_DAO
- WIS1_YAW2_DAO
- WIS2_PIT2_DAO
- WIS2_YAW2_DAO