

Rack Mount Components w/Locations

Loc	Description	Vendor	Model	Designator
02	+/- 0-30VDC Power Supply	Sorenson		PS-1
03	+/- 0-30VDC Power Supply	Sorenson		PS-2
04	+/- 0-30VDC Power Supply	Sorenson		PS-3
05	+/- 0-30VDC Power Supply	Sorenson		PS-4
07	DIN Rail w/terminal blocks (rear mount)			DIN-1
08	VME Crate (Data Acquisition)			VME-1
17	DAQS Interface Chassis (LEMO)	LIGO		DAQIC-1
18	DAQS Interface Chassis (LEMO)	LIGO		DAQIC-2
20	DAQS Interface Chassis (BNC/1KFilter)	LIGO		DAQIC-3
21	Accelerometer Signal Conditioner	Endevco		ACCSC-1
22	Accelerometer Signal Conditioner	Endevco		ACCSC-2
24	DAQS Interface Chassis (BNC/1KFilter)	LIGO		DAQIC-4
29	VME Crate (Global Diagnostics)			VME-2
38	GDS BNC Patch Panel	LIGO		GDSIC-1

NOTES:

1) This drawing is PRELIMINARY and is only to be used for initial installation of rack mount components and connection of PEM signals. Cabling to other Detector components and GDS are pending final equipment designs for those subsystems.

VME1 Modules / Slot Assignments

Slot	Description	Vendor	Model	Designator
1	MIPS Processor	Heurikon	4700	CPU-1
2	MIPS Processor	Heurikon	4700	CPU-2
3	Reflected Memory	VMIC	5588DMA	RFM-1
4	GPS Slave	Brandywine		GPS-1
5	Timing Slave	LIGO		TS-1
6	32 channel ADC	ICS	110B1	ADC-1
7	32 channel ADC	ICS	110B1	ADC-2
8	32 channel ADC	ICS	110B1	ADC-3
9	32 channel ADC	ICS	110B1	ADC-4
10				
11	Backplane Split -----	-----	-----	-----
12	68040 Processor	Motorola	162-333	CPU-3
13-21	Empty	-----	-----	-----

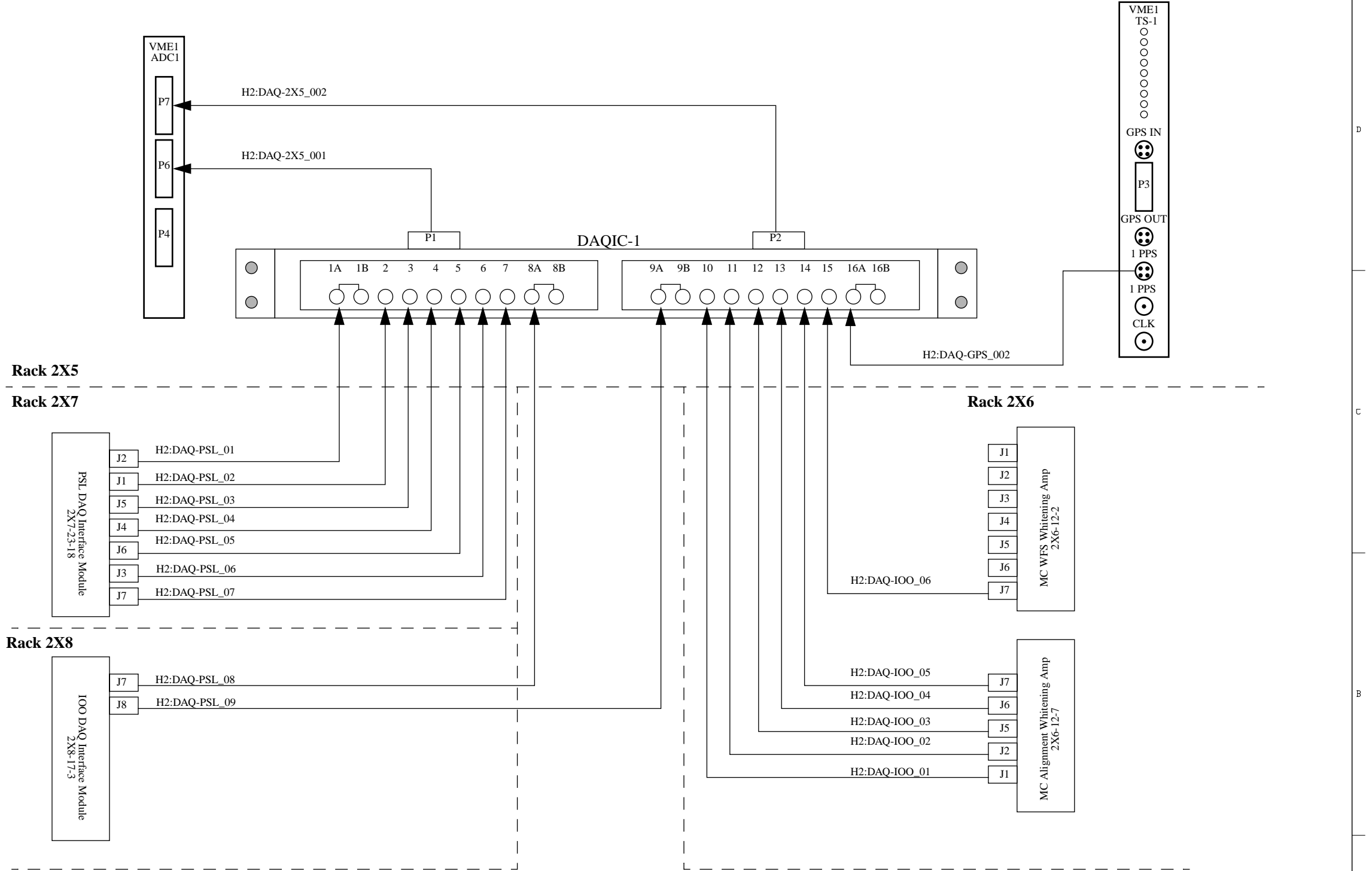
VME2 Modules / Slot Assignments

Slot	Description	Vendor	Model	Designator
1	MIPS Processor	Heurikon	4700	CPU-1
2	MIPS Processor	Heurikon	4700	CPU-2
3	Reflected Memory	VMIC	5588DMA	RFM-1
4	GPS Slave	Brandywine		GPS-1
5	Timing Slave	LIGO		TS-1
6	32 channel DAC	ICS	115	DAC-1

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR RADIUS ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS IN OUT REMOVE ALL BURRS		CURRENT REVISION APPROVAL		LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY	
DRAWN R. Bork	GROUP	SIGNATURE	DATE 12/8/99	Hanford CDS Rack Layout - 2X5 Chassis Layouts	
CHECKED				SCALE	SIZE DWG. NO. B D990016-01-C
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6	REFERENCE DRAWINGS	5			

ADC-1

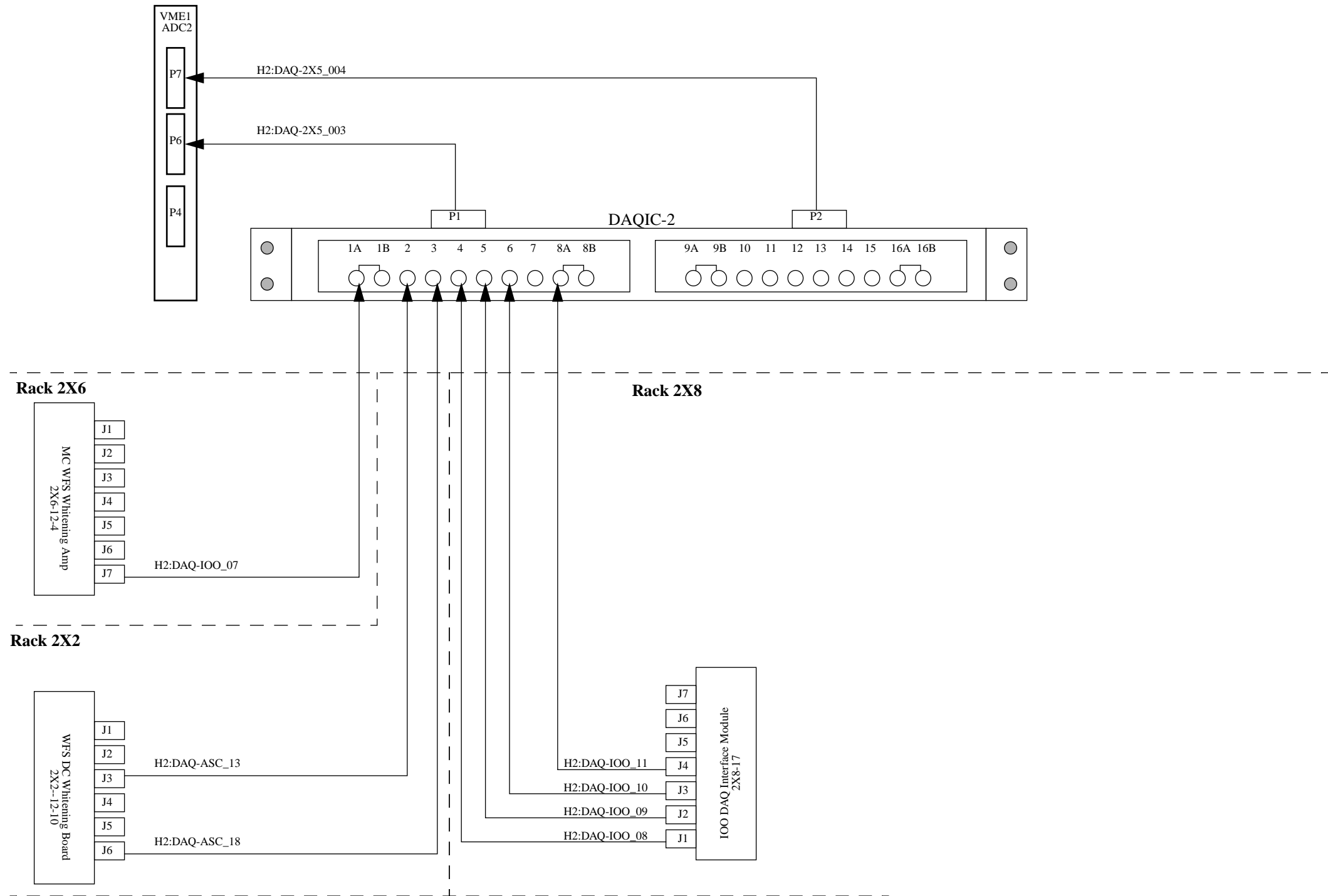
Conn	Chan	Name	Rate
1A	00		
	01	H2:PSL-FSS_FAST_F	16384
2	02	H2:PSL-FSS_MIXERM_F	16384
	03	H2:PSL-FSS_PCDRIVE_F	16384
3	04	H2:PSL-FSS_RFPDDC_F	256
	05	H2:PSL-FSS_RCTRANSPD_F	256
4	06	H2:PSL-PMC_ERR_F	16384
	07	H2:PSL-PMC_PZT_F	2048
5	08	H2:PSL-PMC_RFPDDC_F	256
	09	H2:PSL-PMC_TRANSPD_F	256
6	10	H2:PSL-ISS_ISERR_F	16384
	11	H2:PSL-ISS_ACTM_F	16384
7	12	H2:PSL-ISS_MC1_F	16384
	13	H2:PSL-ISS_MC2_F	16384
8A	14	H2:PSL-QPD2_1F	2048
	15	H2:PSL-QPD2_2F	2048
9A	16	H2:PSL-QPD2_3F	2048
	17	H2:PSL-QPD2_4F	2048
10	18	H2:IOO-WFS1_P	2048
	19	H2:IOO-WFS1_Y	2048
11	20	H2:IOO-WFS2_P	2048
	21	H2:IOO-WFS2_Y	2048
12	22	H2:IOO-MC1_P	2048
	23	H2:IOO-MC1_Y	2048
13	24	H2:IOO-MC1_REF	2048
	25	H2:IOO-MC2_P	2048
14	26	H2:IOO-MC2_Y	2048
	27	H2:IOO-MC2_REF	2048
15	28	H2:IOO-WFS1_DCP	2048
	29	H2:IOO-WFS1_DCY	2048
16A	30	H2:GDS-GPS_RAMP2	16384
	31	H2:GDS-GPS_TRIG2	16384
TOTAL (BTYES/SEC)			399360



				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS IN REMOVE ALL BURRS				CURRENT REVISION APPROVAL			
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ADC-2

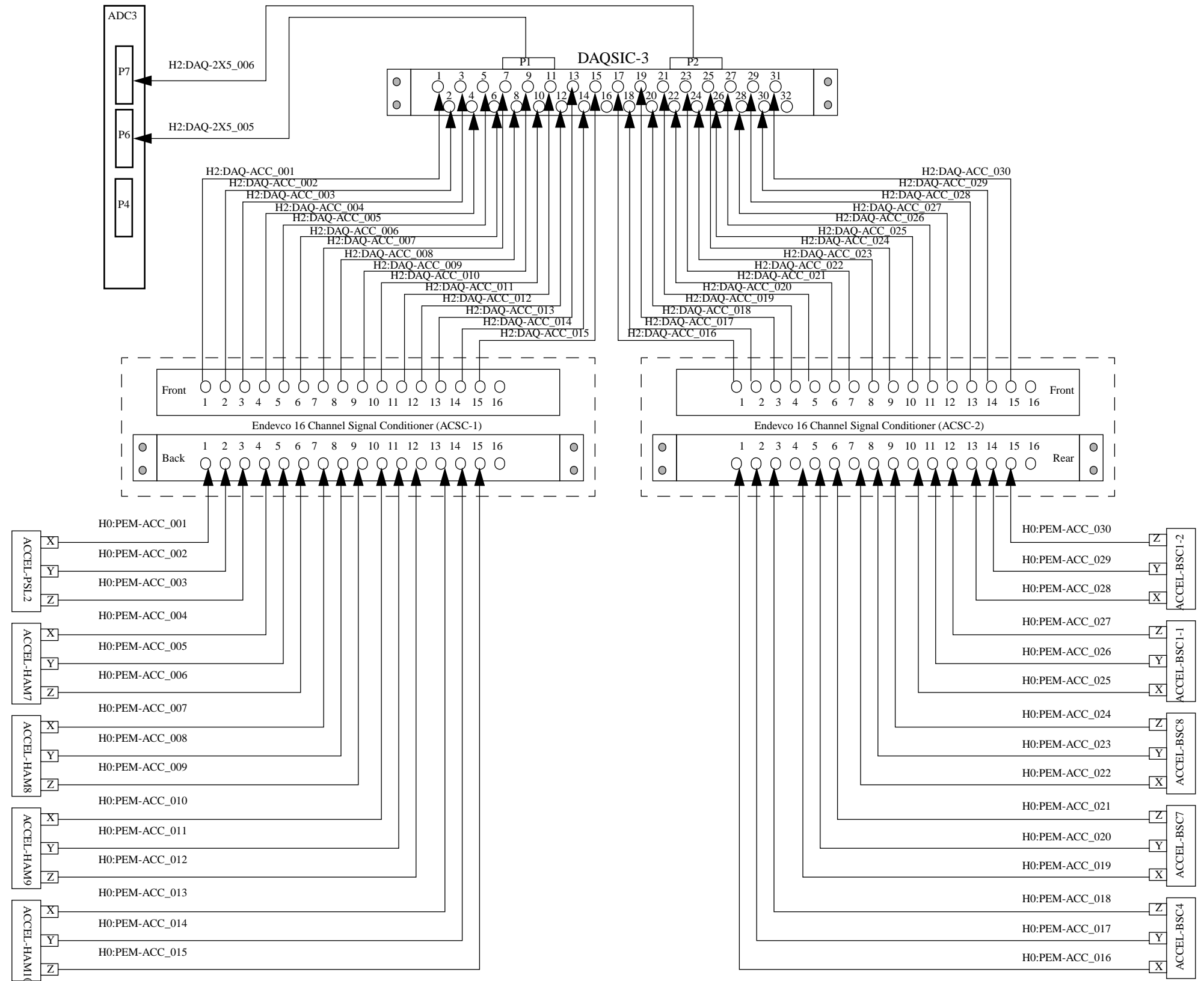
Conn	Chan	Name	Rate
1A	00	H2:IOO-WFS2_DCP	2048
1B	01	H2:IOO-WFS2_DCY	2048
2	02	H2:ASC-WFS3_DCP	2048
	03	H2:ASC-WFS3_DCY	2048
3	04	H2:ASC-WFS4_DCP	2048
	05	H2:ASC-WFS4_DCY	2048
4	06	H2:IOO-MC_I	2048
	07	H2:IOO-MC_LOOP	2048
5	08	H2:IOO-MC_AO	2048
	09	H2:IOO-MCA_TEST_MON	2048
6	10	H2:IOO-PSL_TEST_MON	2048
	11	H2:IOO_MCA_OUT_MON	2048
7	12		
	13		
8A	14	H2:IOO_PSL_OUT_MON	16384
8B	15		
9A	16		
9B	17		
10	18		
	19		
11	20		
	21		
12	22		
	23		
13	24		
	25		
14	26		
	27		
15	28		
	29		
16A	30		
16B	31		
TOTAL (BTYES/SEC)			114688



				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR MACH ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS ON OUT. REMOVE ALL BURRS				CURRENT REVISION APPROVAL			
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USED ON:				NEXT ASS'Y:				SHEETS EFFECTED			
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								Hanford CDS Rack Layout - 2X5 DAQIC-2 Connections			
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								STD			
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ADC-3

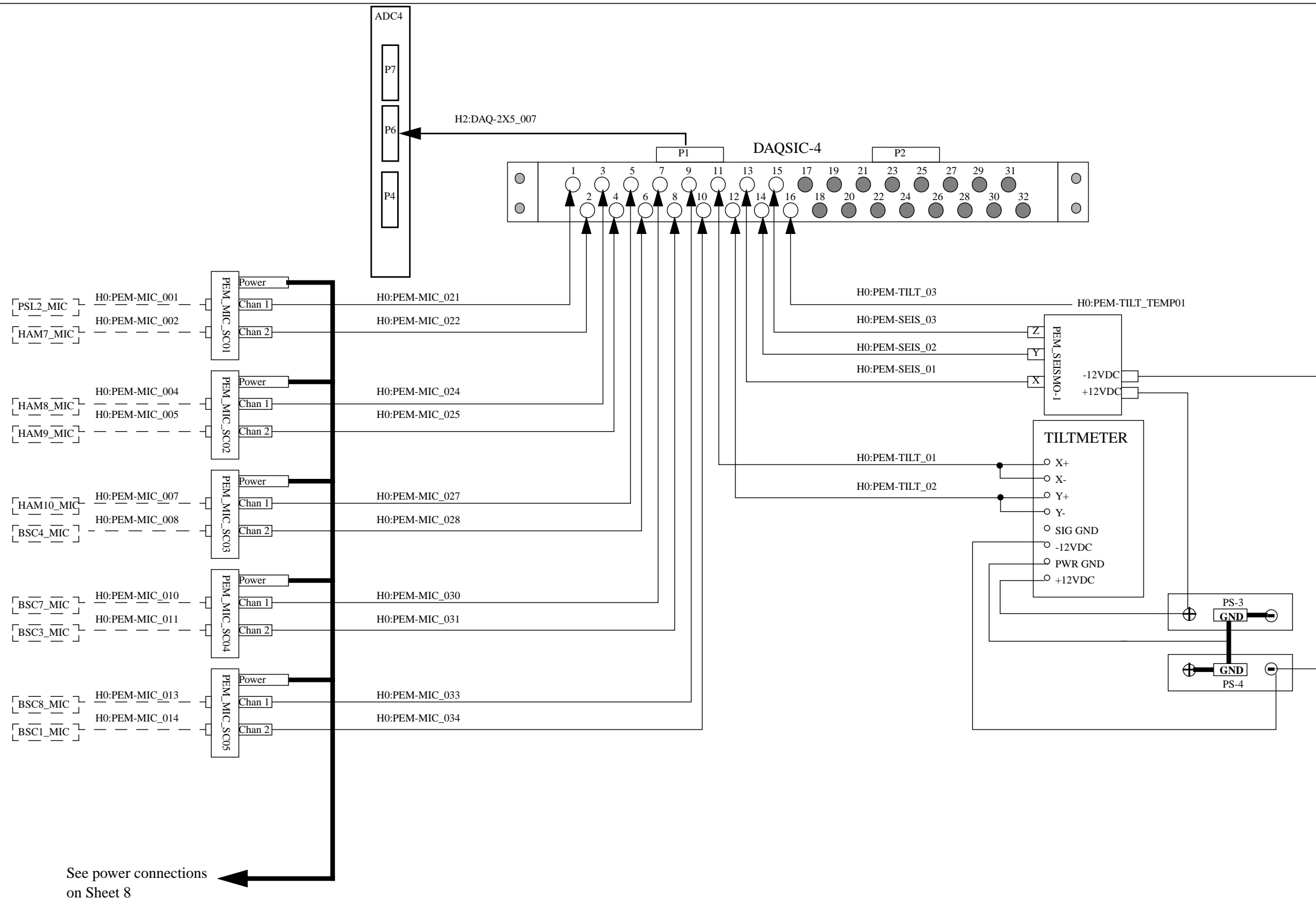
Chan	Name	Rate
00	H0:PEM-PSL2_ACCX	2048
01	H0:PEM-PSL2_ACCY	2048
02	H0:PEM-PSL2_ACCZ	2048
03	H0:PEM-HAM7_ACCX	2048
04	H0:PEM-HAM7_ACCY	2048
05	H0:PEM-HAM7_ACCZ	2048
06	H0:PEM-HAM8_ACCX	2048
07	H0:PEM-HAM8_ACCY	2048
08	H0:PEM-HAM8_ACCZ	2048
09	H0:PEM-HAM9_ACCX	2048
10	H0:PEM-HAM9_ACCY	2048
11	H0:PEM-HAM9_ACCZ	2048
12	H0:PEM-HAM10_ACCX	2048
13	H0:PEM-HAM10_ACCY	2048
14	H0:PEM-HAM10_ACCZ	2048
15		
16	H0:PEM-BSC4_ACCX	2048
17	H0:PEM-BSC4_ACCY	2048
18	H0:PEM-BSC4_ACCZ	2048
19	H0:PEM-BSC7_ACCX	2048
20	H0:PEM-BSC7_ACCY	2048
21	H0:PEM-BSC7_ACCZ	2048
22	H0:PEM-BSC8_ACCX	2048
23	H0:PEM-BSC8_ACCY	2048
24	H0:PEM-BSC8_ACCZ	2048
25	H0:PEM-BSC1_ACC1X	2048
26	H0:PEM-BSC1_ACC1Y	2048
27	H0:PEM-BSC1_ACC1Z	2048
28	H0:PEM-BSC1_ACC2X	2048
29	H0:PEM-BSC1_ACC2Y	2048
30	H0:PEM-BSC1_ACC2Z	2048
31		
TOTAL (BTYES/SEC)		122880



				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR RADIUS ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS ON REMOVE ALL BURRS				CURRENT REVISION APPROVAL				LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
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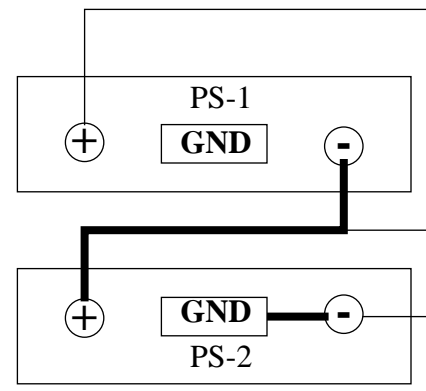
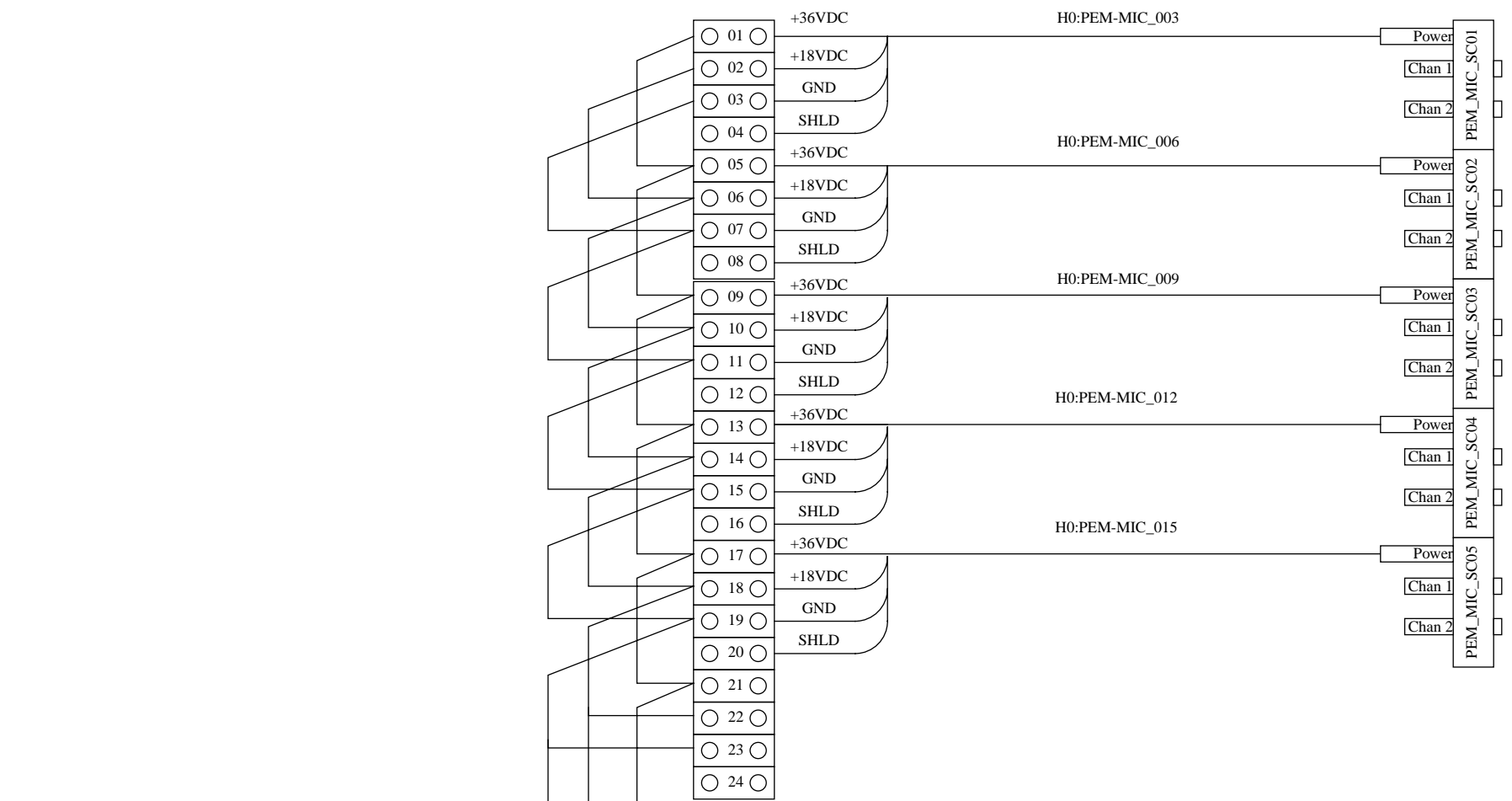
ADC-4

Chan	Name	Rate
00	H0:PEM-PSL2_MIC	2048
01	H0:PEM-HAM7_MIC	2048
02	H0:PEM-HAM8_MIC	2048
03	H0:PEM-HAM9_MIC	2048
04	H0:PEM-HAM10_MIC	2048
05	H0:PEM-BSC4_MIC	2048
06	H0:PEM-BSC7_MIC	2048
07	H0:PEM-BSC3_MIC	2048
08	H0:PEM-BSC8_MIC	2048
09	H0:PEM-BSC1_MIC	2048
10	H0:PEM-LVEA_TILTX	256
11	H0:PEM-LVEA_TILTY	256
12	H0:PEM-LVEA_SEISX	256
13	H0:PEM-LVEA_SEISY	256
14	H0:PEM-LVEA_SEISZ	256
15	H0:PEM-LVEA_TEMP6	256
16	H2:GDS-DAC1	16384
17	H2:GDS-DAC2	16384
18	H2:GDS-LVEA2_TO1	16384
19	H2:GDS-LVEA2_TO2	16384
20	H2:GDS-LVEA2_TO3	16384
21	H2:GDS-LVEA2_TO4	16384
22	H2:GDS-LVEA2_TO5	2048
23	H2:GDS-LVEA2_TO6	2048
24	H2:GDS-LVEA2_TO7	2048
25	H2:GDS-LVEA2_TO8	2048
26	H2:GDS-LVEA2_TO9	2048
27	H2:GDS-LVEA2_T10	2048
28	H2:GDS-LVEA2_T11	2048
29	H2:GDS-LVEA2_T12	2048
30	H2:GDS-LVEA2_T13	2048
31	HPEM-NBR_2K	16384
TOTAL (BTYES/SEC)		144384

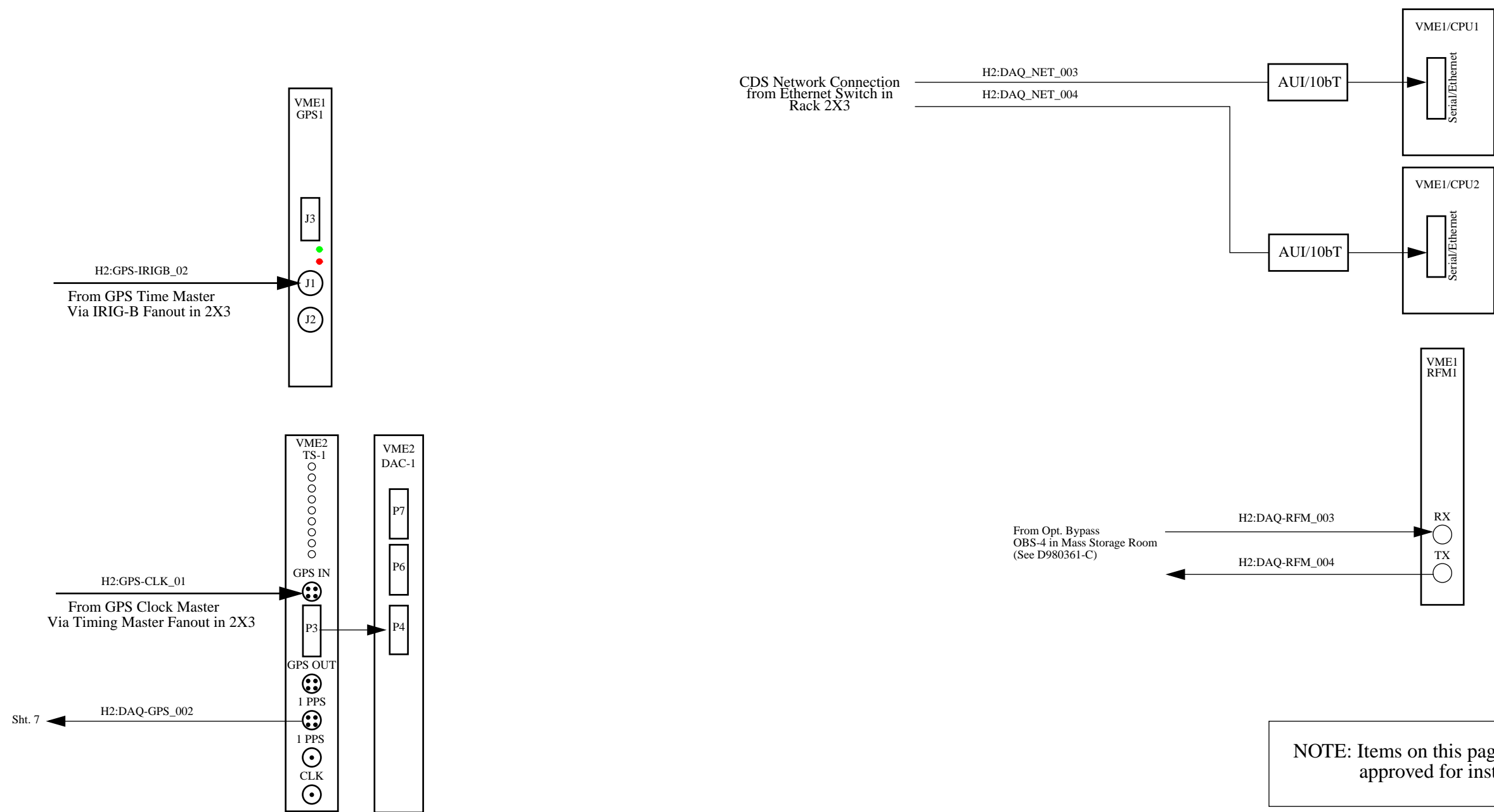


See power connections on Sheet 8

				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR/RACH ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS ON REMOVE ALL BURRS		CURRENT REVISION APPROVAL		LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY	
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								SHEET 6 of 8	
								STD	
								VER. 01	



				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS IN OUT. REMOVE ALL BURRS				CURRENT REVISION APPROVAL				LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
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REFERENCE DRAWINGS				USED ON:				ISSUE DESCRIPTION				SCALE			
NEXT ASS'Y:								DCC				SCALE			
6				5				4				3			
												SHEET 7 of 8			
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												STD			



NOTE: Items on this page are preliminary and not yet approved for installation.

				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR RADIUS ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS IN OUT, REMOVE ALL BURRS				CURRENT REVISION APPROVAL				LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
				DO NOT SCALE THIS DRAWING				DRAWN R. Bork				GROUP			
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DWG. NO.				DESCRIPTION				REV				DESCRIPTION			
REFERENCE DRAWINGS								SHEETS EFFECTED				DATE			
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