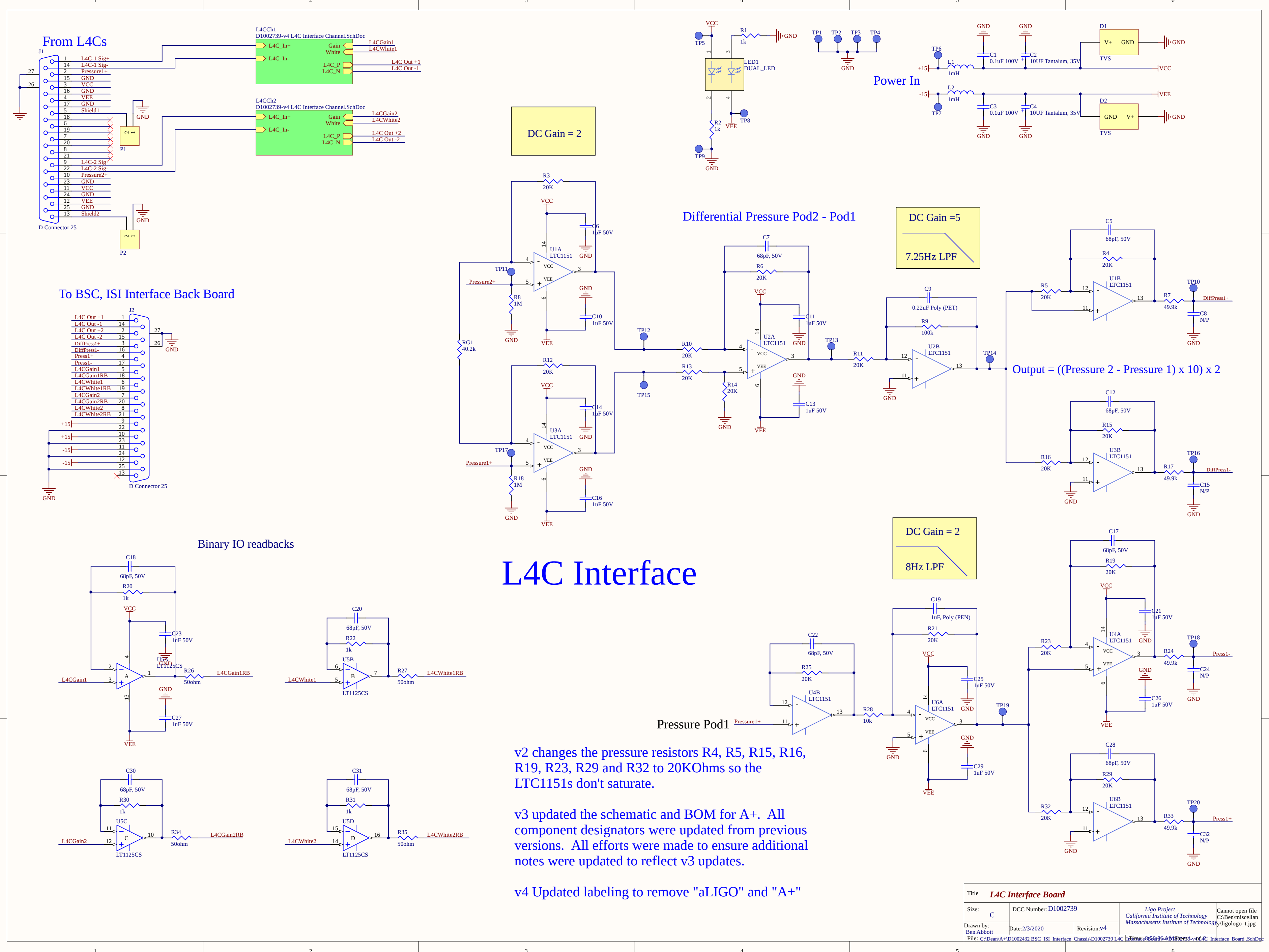




L4C Interface

v2 changes the pressure resistors R4, R5, R15, R16, R19, R23, R29 and R32 to 20KOhms so the LTC1151s don't saturate.

v3 updated the schematic and BOM for A+. All component designators were updated from previous versions. All efforts were made to ensure additional notes were updated to reflect v3 updates.

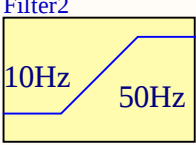
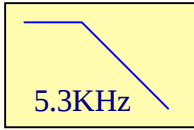
v4 Updated labeling to remove "aLIGO" and "A+"

Title L4C Interface Board			
Size: C	DCC Number: D1002739		Ligo Project California Institute of Technology Massachusetts Institute of Technology Cannot open file C:\Ben\miscellan\ligologo_t.jpg
Drawn by: Ben Abbott	Date: 2/3/2020	Revision: v4	
File: C:\Dean\A+\D1002432 BSC ISI Interface Chassis\D1002739 L4C Interface Board\A+ L4C Interface Board SchDoc			

There is a pole @ 5.3KHz

Whitening is a 10Hz Zero, and a 50Hz Pole. Overall gain=2 @DC.

1V at TP5 = +1V at TP10,
and -1V at TP6



Non-Whitening path selected
by R49 pull-down resistor.
Whitening disabled by R49

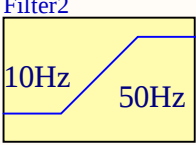
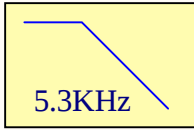
"Nominal" means that the Binary Out module
is not engaged, and that pin 6 of the
Max4660 module has floated high.

Nominal Gain = 7
Switched Gain = 1
Gain = 1 + 2*(5K / Rg)

There is a pole @ 5.3KHz

Whitening is a 10Hz Zero, and a 50Hz Pole. Overall gain=2 @DC.

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"Nominal" means that the Binary Out module
is not engaged, and that pin 6 of the
Max4660 module has floated high.