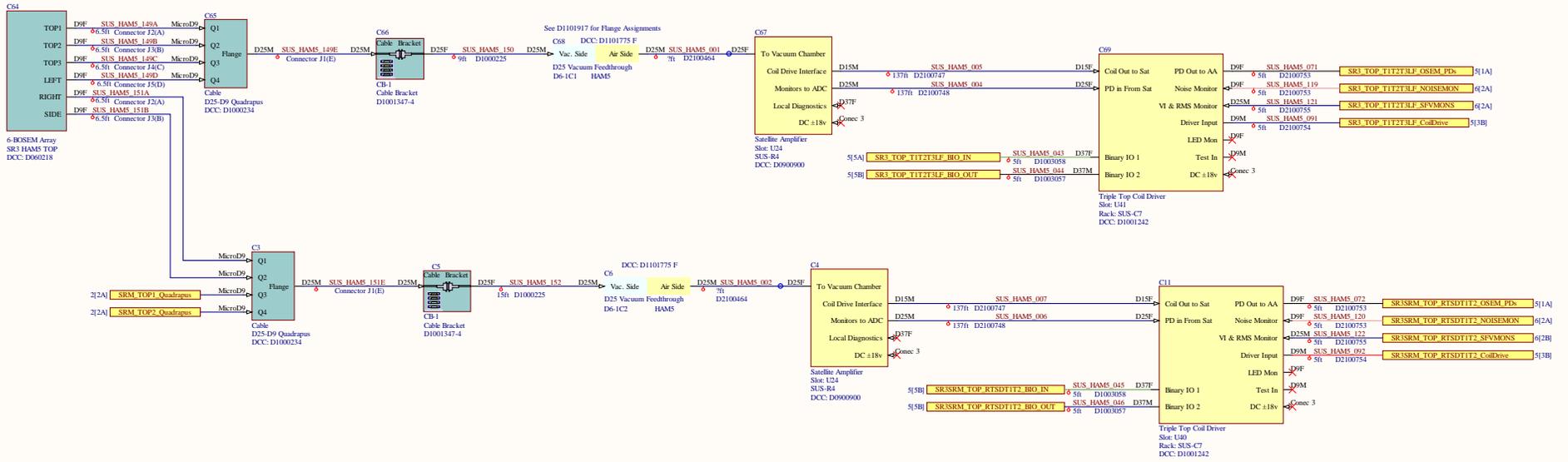
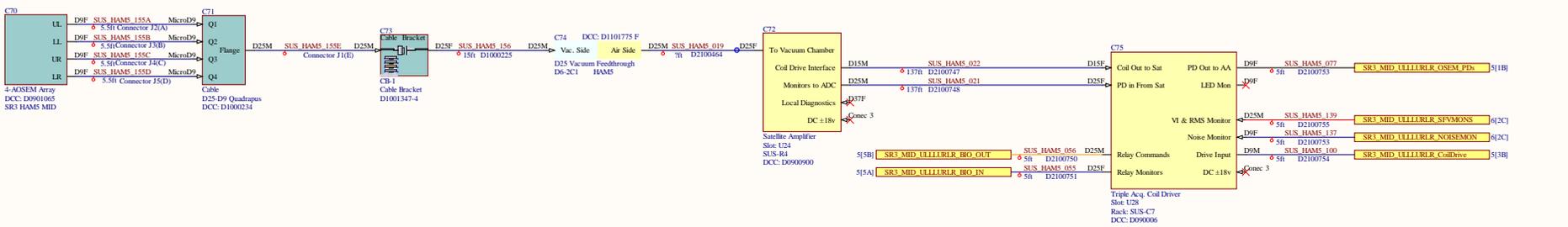


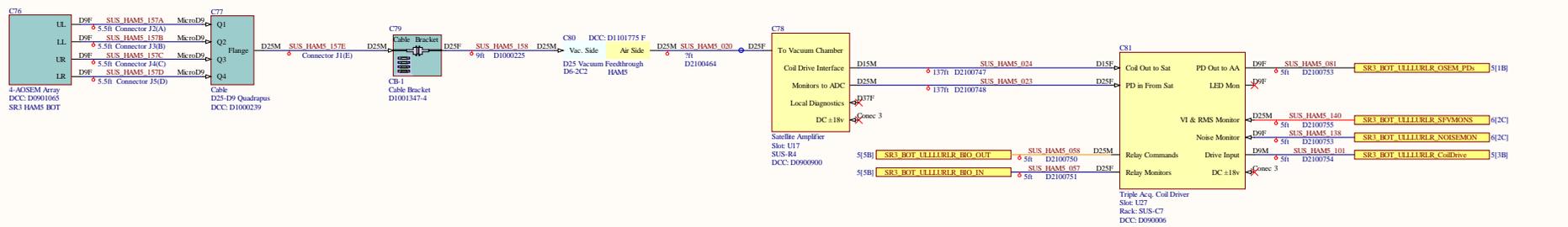
SR3 TOP



SR3 MIDDLE

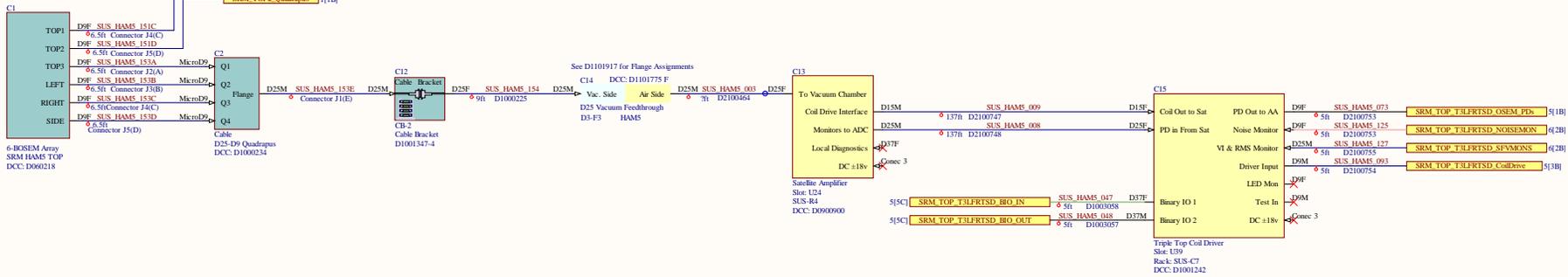


SR3 BOTTOM

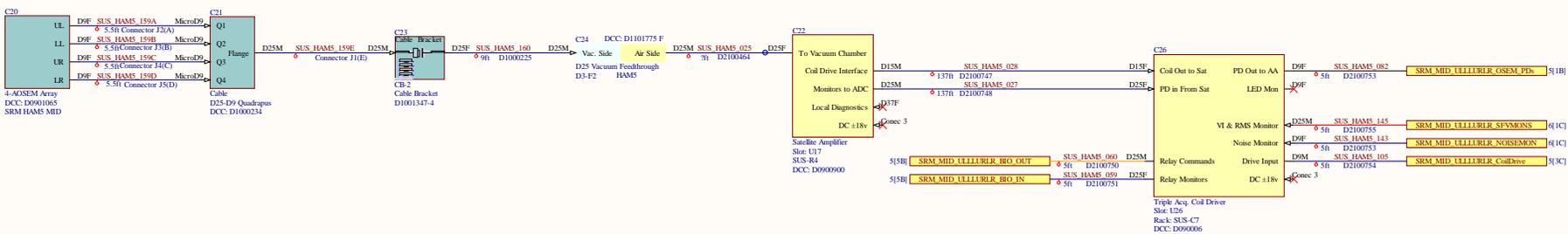


Last Edited: 1/13/2023

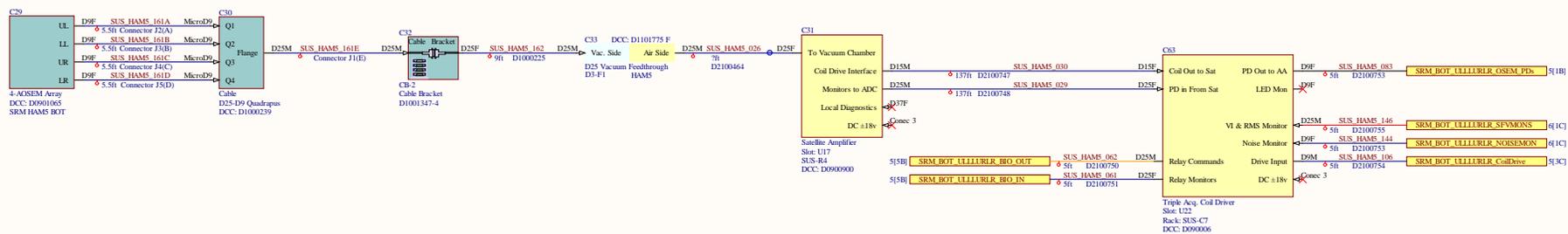
SRM TOP



SRM MIDDLE

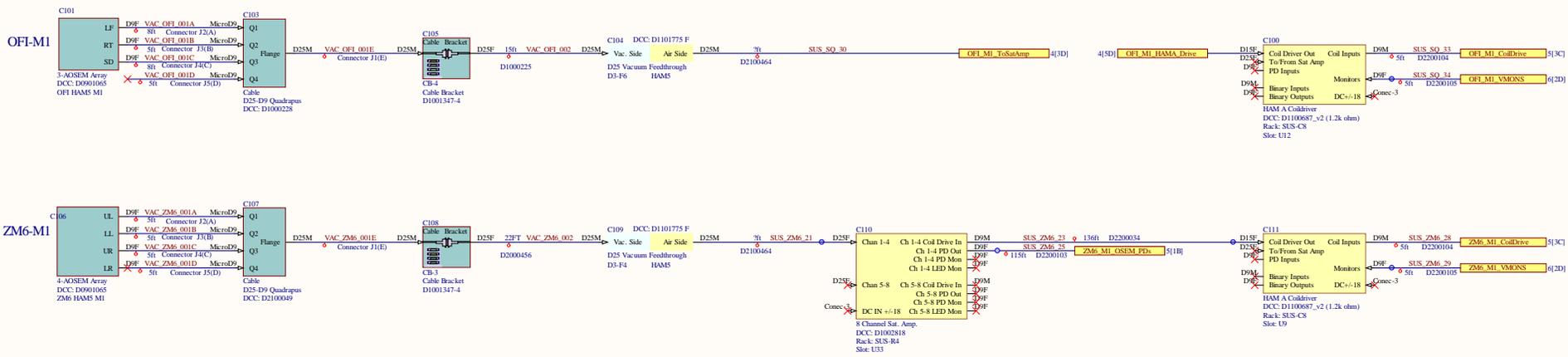


SRM BOTTOM



Last Edited: 1/13/2023

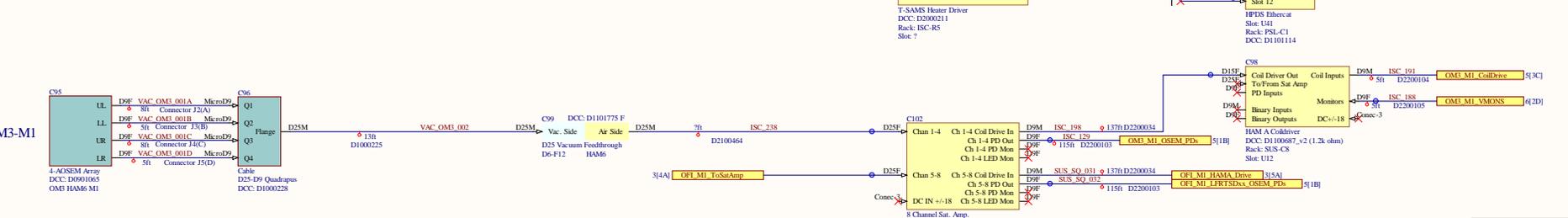
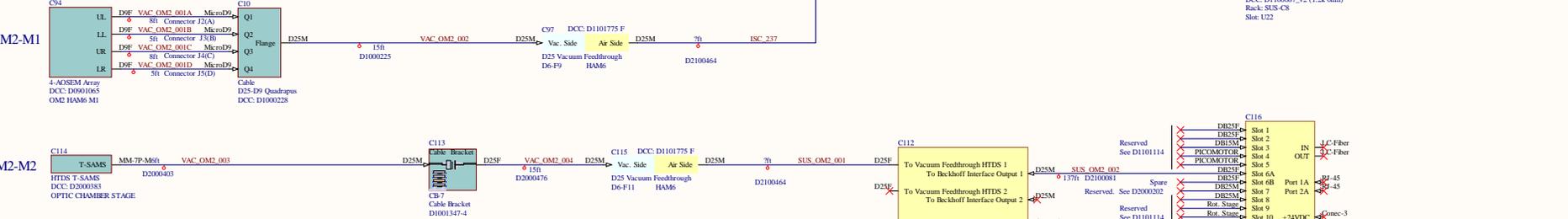
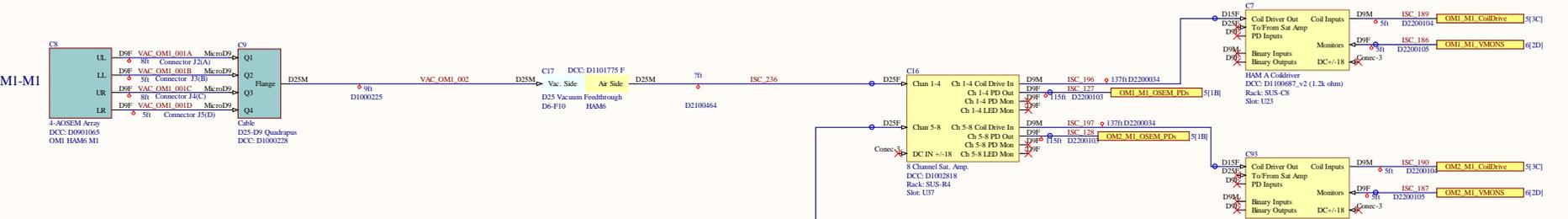
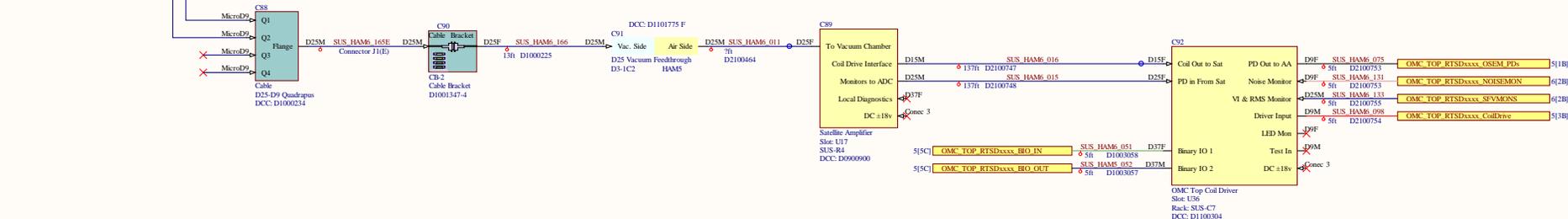
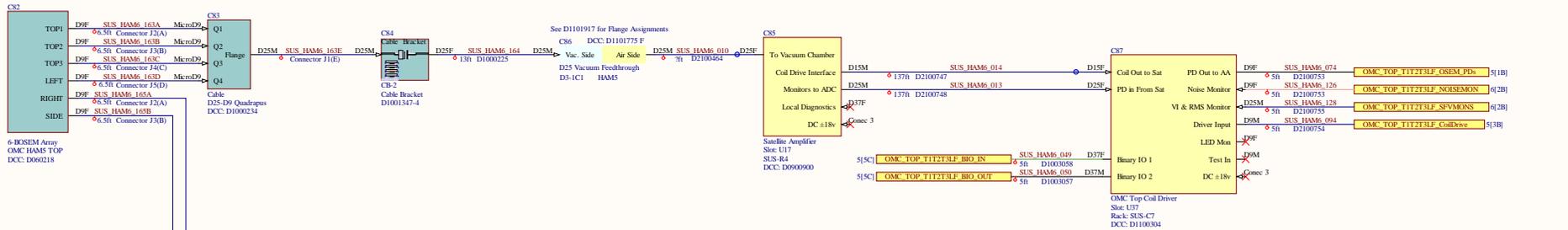
Title HAMS SRM OSEMs to Drivers		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO
Size: C	DCC Number: D1002740	SCH/PCB Revision: v10	Engineer: L. Sanchez	Date: 1/13/2023
File: C:\Users\Public\Documents\Alium\Projects\Wiring\SUS_HAMS56_v9 (8-24-2021) 2-56-17 PM\HAMS_SRMs_v10_SchDoc				Time: 2:56:34 PM
				Sheet 2 of 8

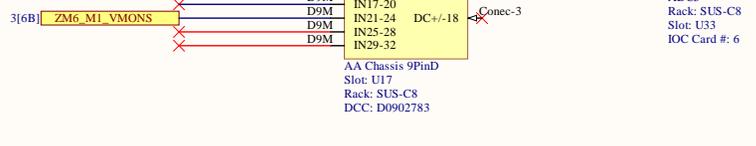
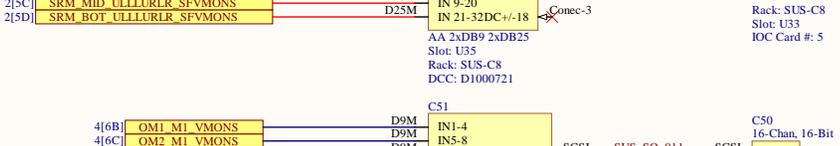
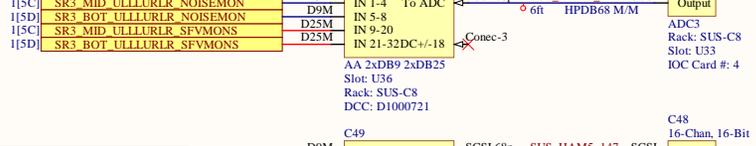
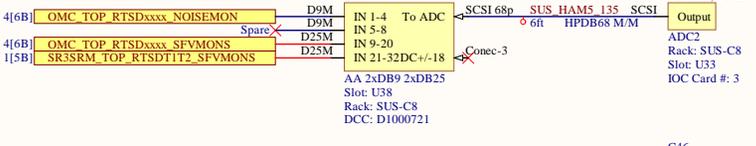
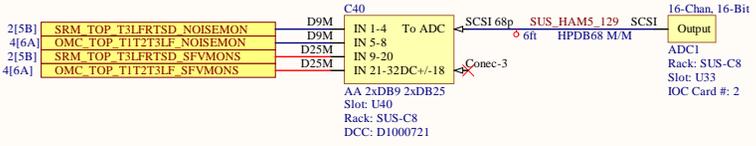
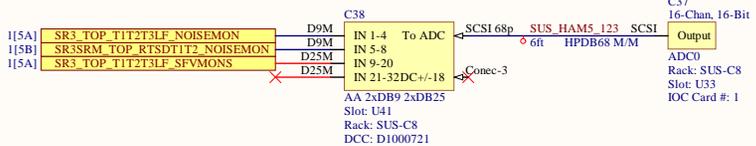


Last Edited: 1/13/2023

Title HAMS OPI & ZM6 OSEMs to Drivers		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: C	DCC Number: D1002740	SCH/PCB Revision: v10	Engineer: L. Sanchez	Date: 1/13/2023	Time: 3:56:34 PM
File: C:\Users\Public\Documents\Alium\Projects\Wiring\SUS_HAMS6_v9 (8-24-2021 2:56:17 PM)\HAMS_OPI_ZM6_v10.SchDoc					
Sheet 3 of 8					

OMC TOP

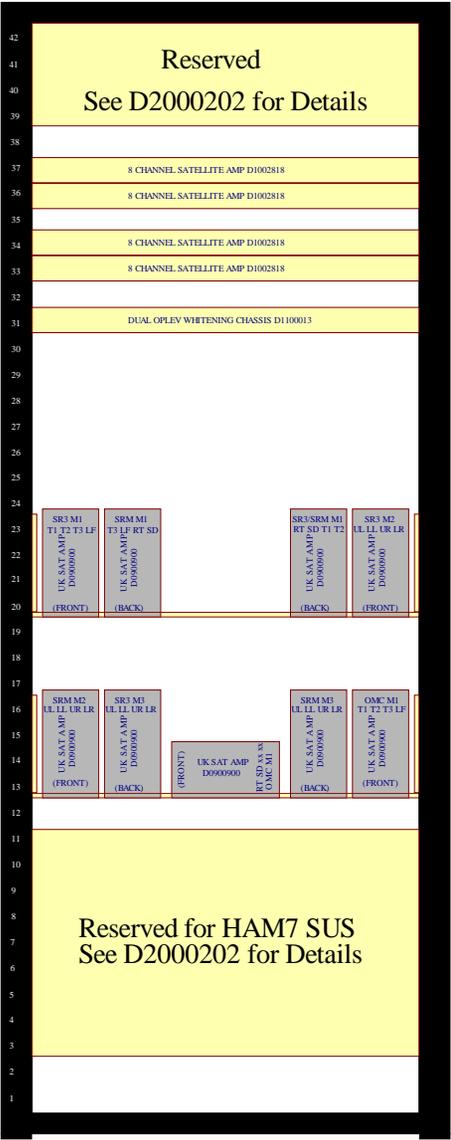




Title		Last Edited: 1/13/2023	
SUS AUX HAM56 Coil Mon. 10 Chains		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology	
Size: B	DCC Number: D1002740	Revision: v10	Engineer: L. Sanchez
File: C:\Users\Public\Documents\Altium\Projects\Wiring_SUS_HAM56_v9 (8-24-2021 2:56-17 PM)\SUS_AUXH56_ADC_DAC_v10.SchDoc		Date: 1/13/2023	Time: 2:56:34 PM
		Sheet 6 of 8	

Front view

R4 RACK



OM1 M1, OM2 M1
OM3 M1, OF1 M1

ZM4 M1, ZM5 M1
ZM6 M1, SPARE

Reserved SR3 M3, SPARE

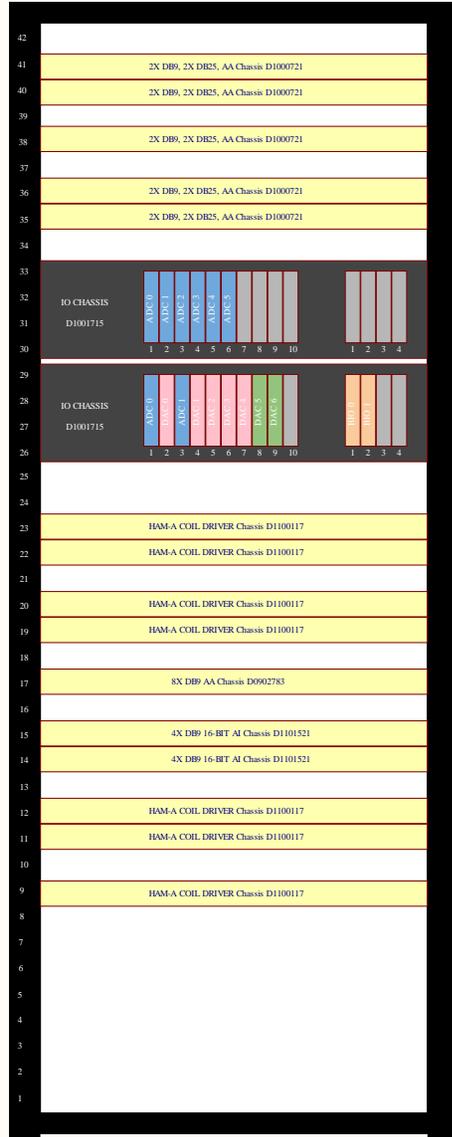
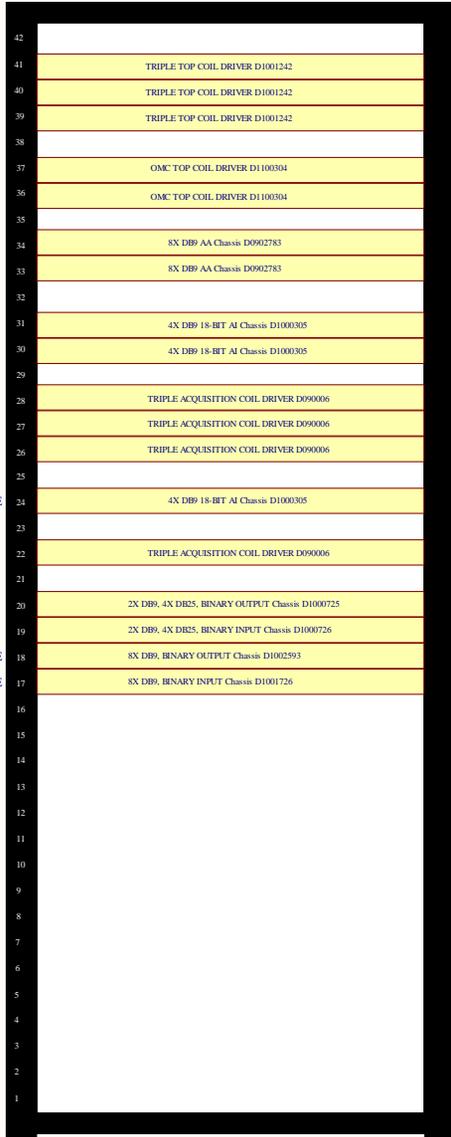
Reserved for HAM7 SUS
See D2000202 for Details

Front view

Front view

C7 RACK

C8 RACK



	16-BIT ADC
	20-BIT DAC
	16-BIT DAC
	64-BIT BFO
	EMPTY

Rack SUS-C7A

Rack SUS-C8A

Last Edited: 1/13/2023

Title		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: C	DCC Number: D1002740	SCH/PCB Revision: v10	Engineer: L. Sanchez	Date: 1/13/2023	Time: 2:56:35 PM
File: C:\Users\Public\Documents\Alium\Projects\Wiring\SUS_HAM56_v9 (8-24-2021 2:56:17 PM)\RACK_SUS_C7_C8_v10.SchDoc					
Sheet 8 of 8					