



DOCUMENT CHANGE NOTICE (DCN)

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|---|------------------------------|-----------------|
| DOCUMENT No. (DOC-REV-GP.ID) | TITLE | NEW REV. |
| D000457-00-C | 21.5 MHz Frequency Reference | B |

CHANGE DESCRIPTION (FROM/TO):

| Resistor | From | To |
|-------------------|------|-------|
| R21, R22 | 16 | 16.2 |
| R15 | 27 | 27.4 |
| R36,R30, R29, R40 | 30K | 30.1K |
| R6 | 36 | 36.5 |
| R20 | 68 | 68.1 |
| R23,R9,R13,R25 | 330 | 332 |

REASON FOR CHANGE:

Schematic update: Resistor values are modified on the schematic to reflect standard 1% values. Most boards already in the field will likely contain these values. See note below *-List S/Ns which comply already*

ACTION:

Incorporate Change

Attach DCN to drawings(s)

Other action (specify):

| DISPOSITION OF HARDWARE (IDENTIFY SERIAL NUMBERS) | | DCN DISTRIBUTION |
|---|---|--|
| <input type="checkbox"/> | No hardware was affected (record change only): | D. Coyne, R. Abbott, J. Heefner, R. Savage, P. King, S. Liu, W. Tyler, R. McCarthy, R. Wooley, P. Russell, G. Sanders, B. Barish, O. Matherny, S. Whitcomb, M. Coles, R. Raab, R. Weiss, G. Stapfer, P. Lindquist, A. Lazzarini, D. Shoemaker, J. Kovalik, B. Abbott |
| X | List S/Ns which comply already: BL2005,2006,2002,2007,2001,2003 | |
| <input type="checkbox"/> | List S/Ns to be reworked/scrapped: | |
| X | List S/Ns to be built with this change: No others | |
| <input type="checkbox"/> | List S/Ns to be retested per this change: | |

| | | | |
|---|--|---|-------------|
| SAFETY, COST, SCHEDULE, REQUIREMENTS IMPACT? | <input checked="" type="checkbox"/> NO | <input type="checkbox"/> YES (If yes, enter CR (CCB) or TCP (TRB) # | |
| APPROVALS | DATE | OTHER APPROVALS (specify) | DATE |
| Originator: R. Karwoski | 01/11/02 | | |
| Task Leader: | | | |
| Group Leader: D. Coyne | | | |
| DCC Release: | | | |