

**LASER INTERFEROMETER GRAVITATIONAL WAVE
OBSERVATORY**

-LIGO-

CALIFORNIA INSTITUTE OF TECHNOLOGY
MASSACHUSETTS INSTITUTE OF TECHNOLOGY

Document Type	DCC Number	Date
Procedures	LIGO-E1000084-v1	4 Feb, 2011
Changing a HAM ISI Test Stand into a BSC ISI Test Stand		
Ben Abbott		

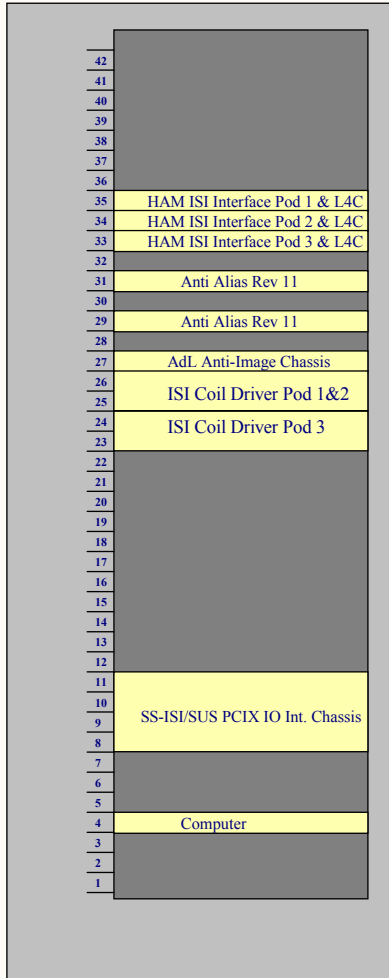
Distribution of this draft: NSF reviewers, LIGO scientists
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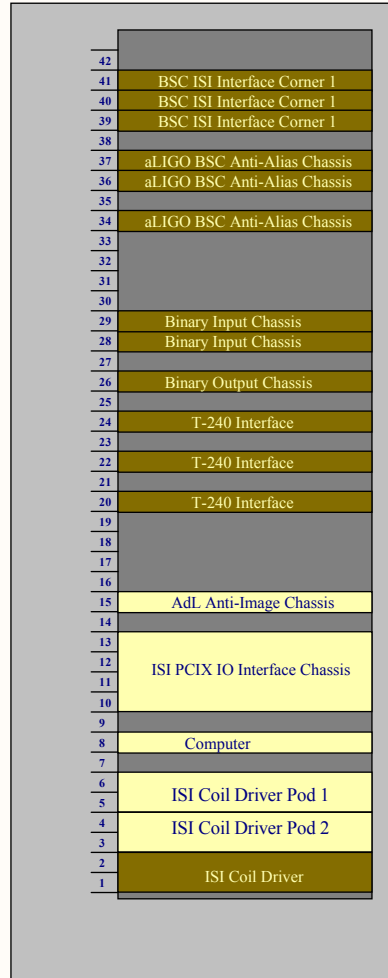
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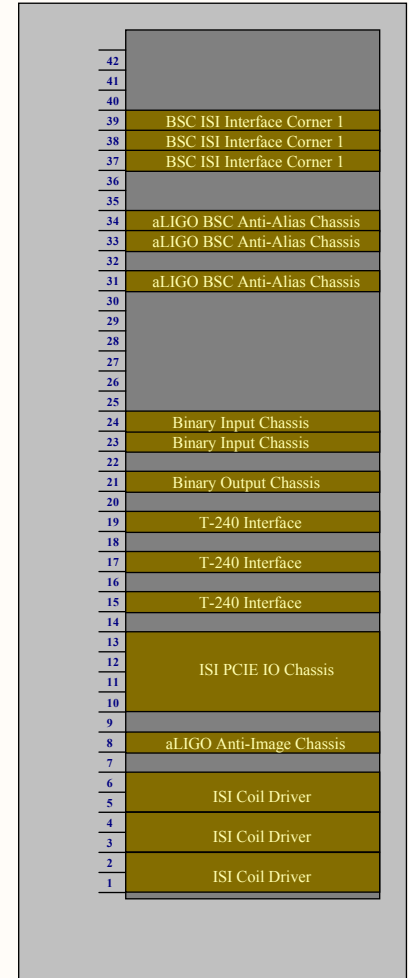
HAM Test Stand



HAM & BSC Test Stand

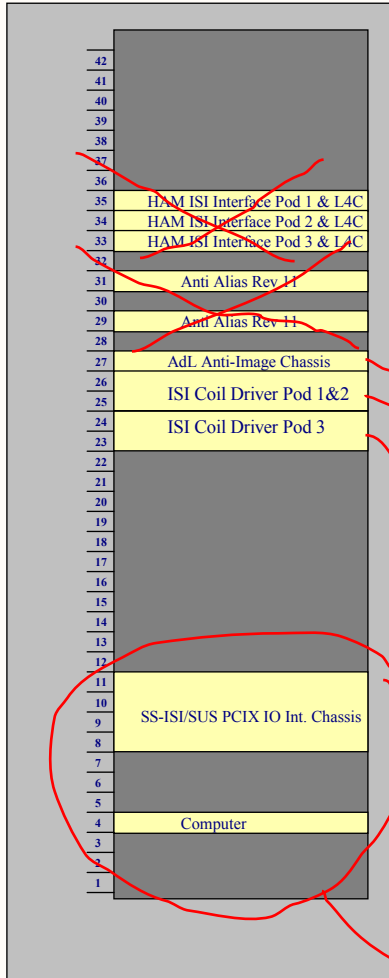


BSC Rack

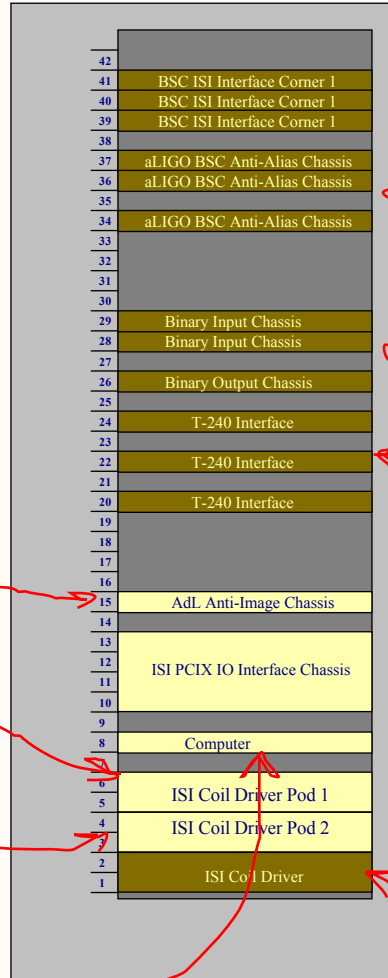


Title HAM to BSC ISI Test Stand System Wiring			
Size: B	DCC Number: D1001412	Ligo Project California Institute of Technology Massachusetts Institute of Technology	
Drawn by: Ben Abbott	Date: 2/3/2011	Revision: v5	Cannot open file C:\Ben\miscellan y\ligologo_1.jpg
File: C:\restored\Ben\Seismic\ISI\AdL\SEI_HAM to BSC_Test_Stand_Sch.Dwg Time: 5:01:43 PM Sheet 6 of 6			

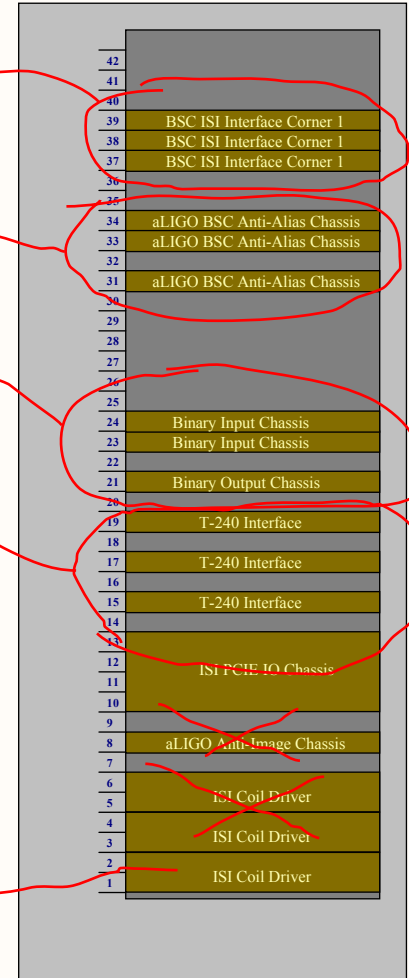
HAM Test Stand



HAM & BSC Test Stand



BSC Rack

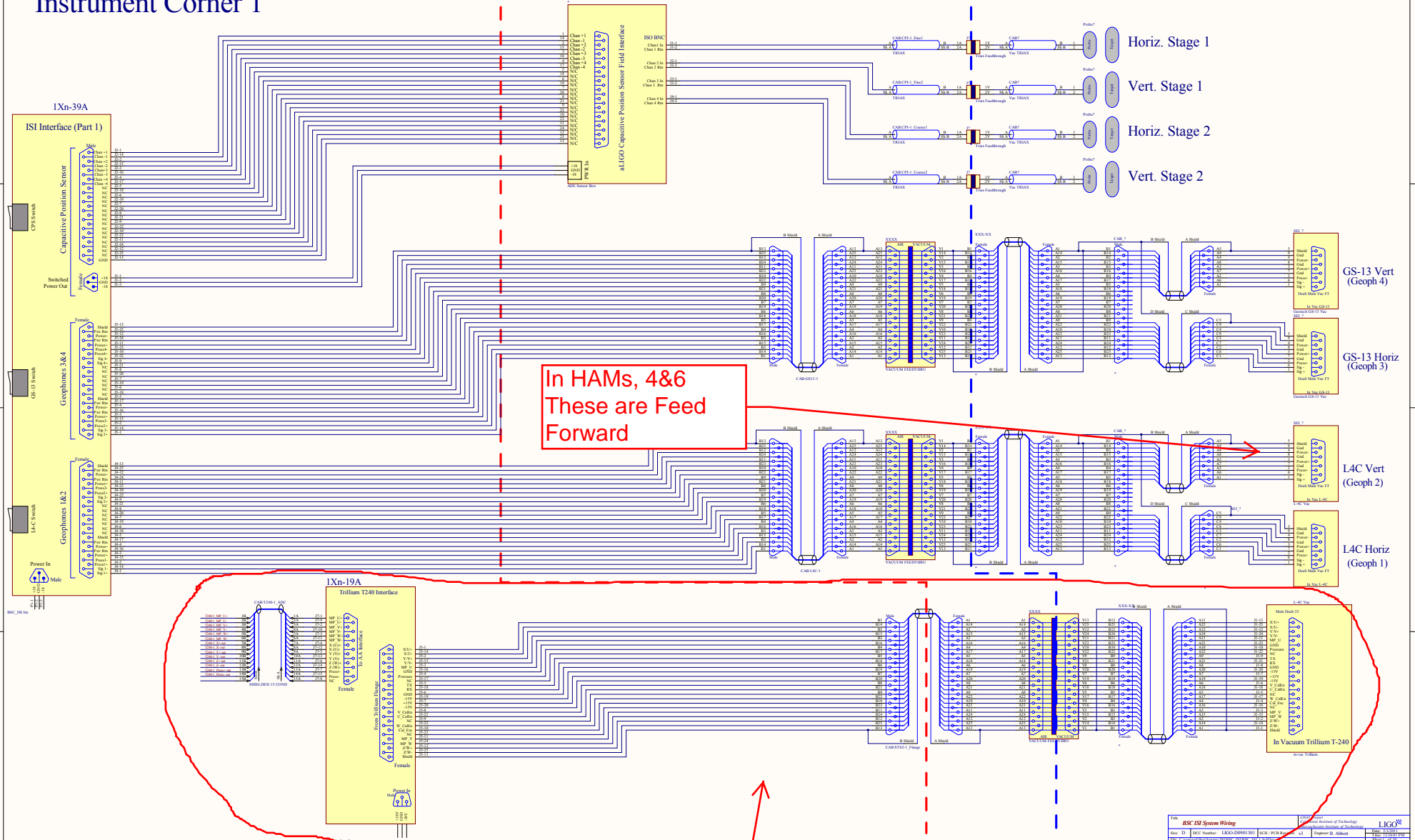


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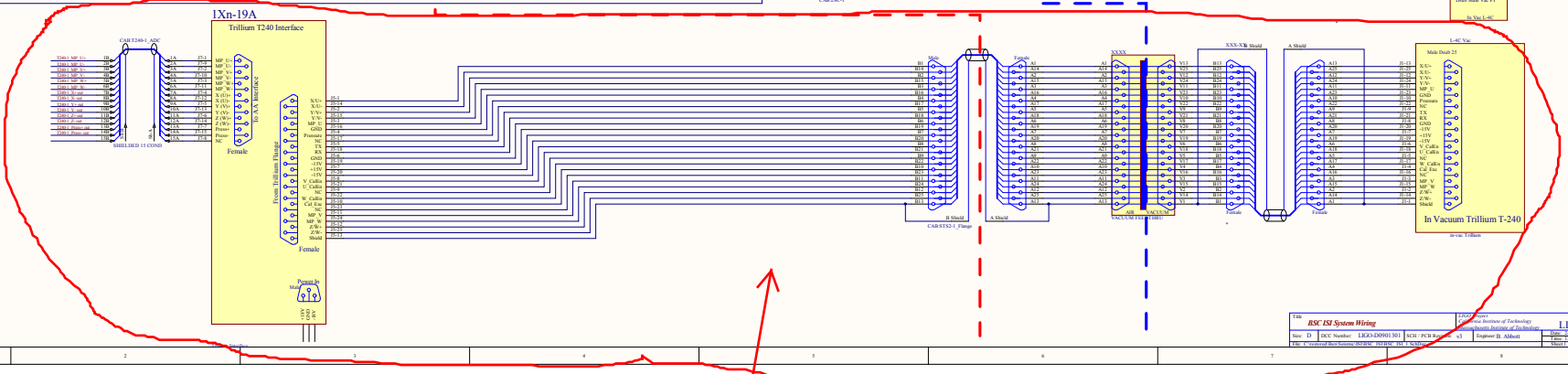
Instrument Corner 1

Rack | Field

Air | Vacuum

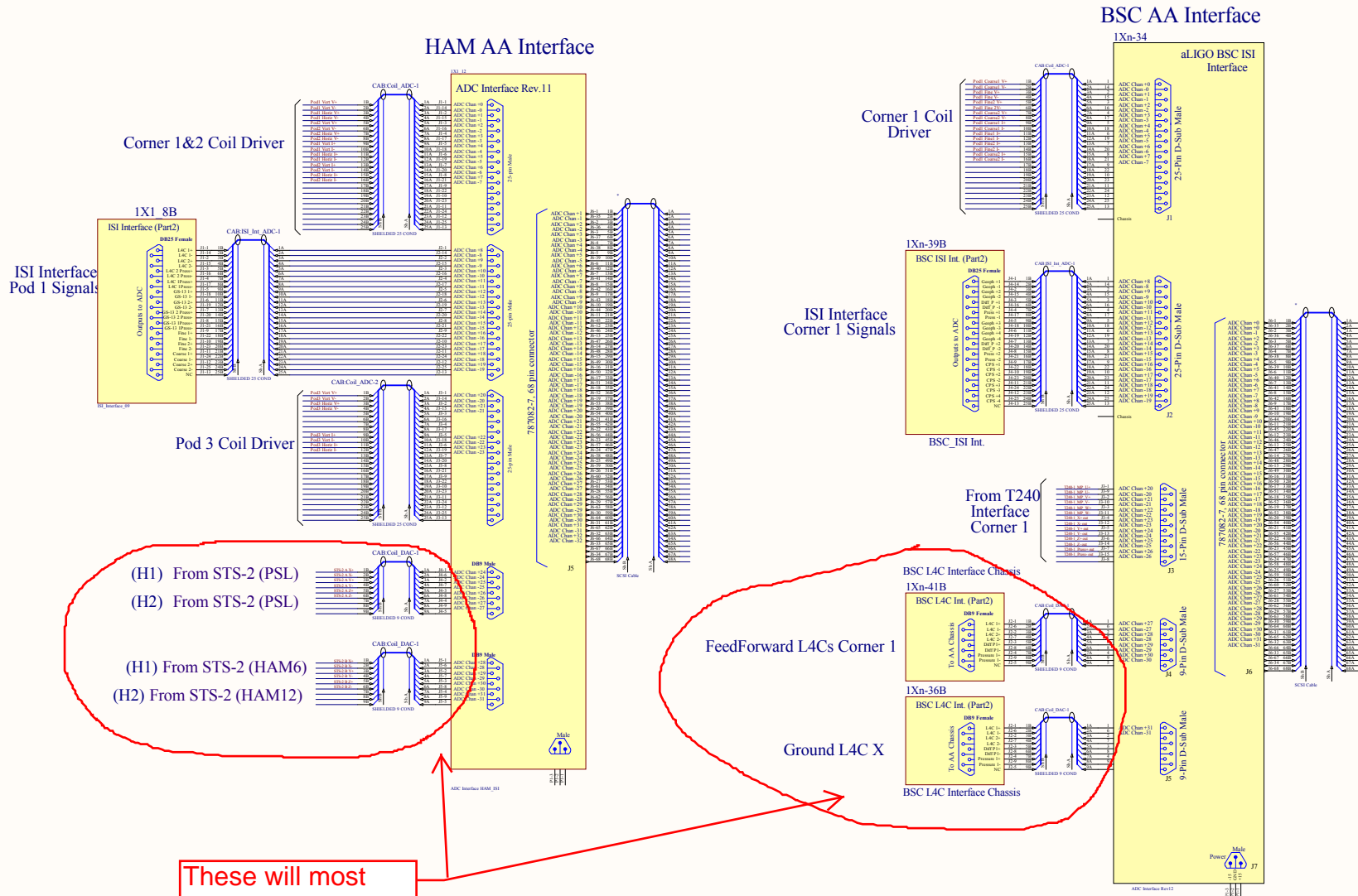


In HAMS, 4&6
These are Feed Forward



New for BSC

HAM AA Interface



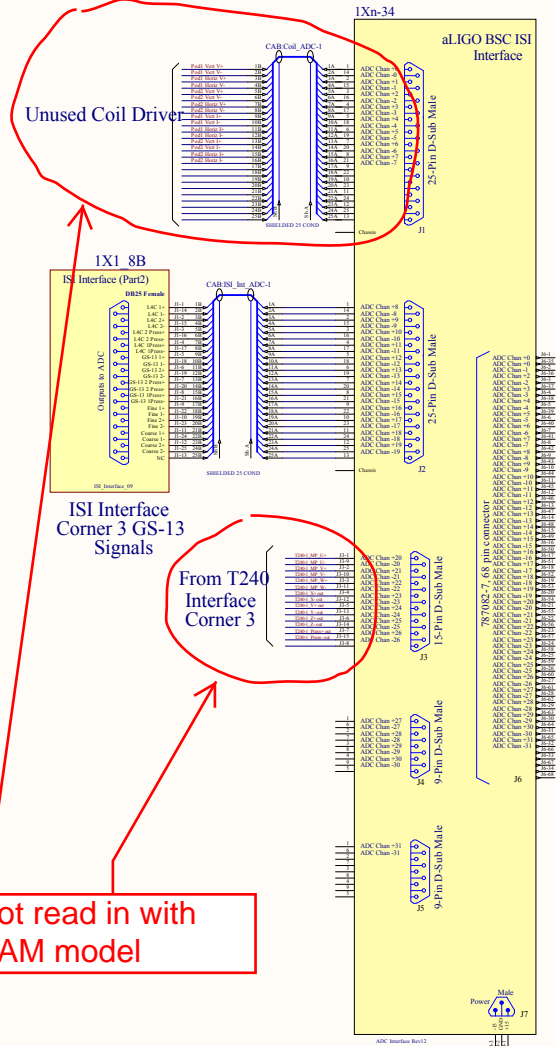
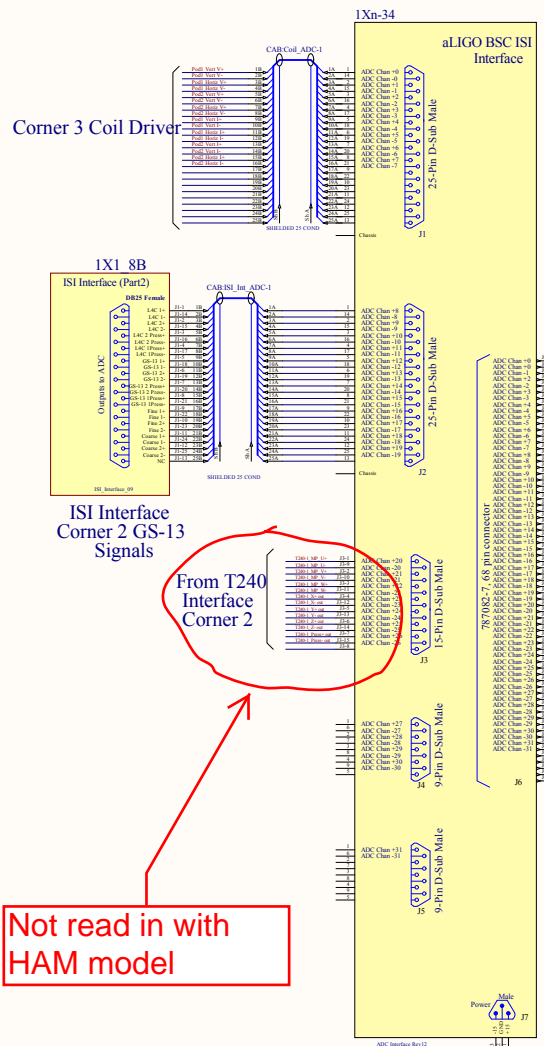
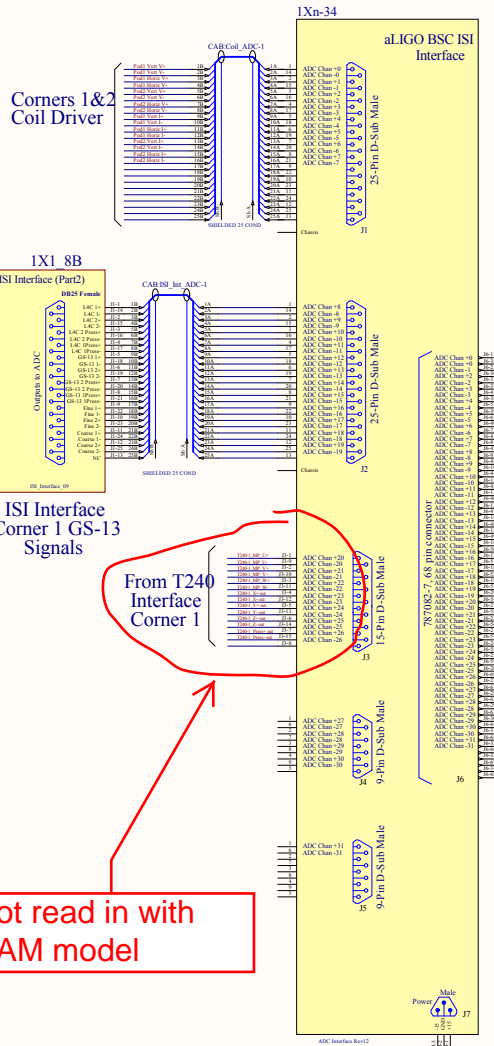
These will most likely be absent.

Rev	Number	Revision
1	1	
2	2	
3	3	
4	4	
5	5	
6	6	
7	7	
8	8	
9	9	
10	10	

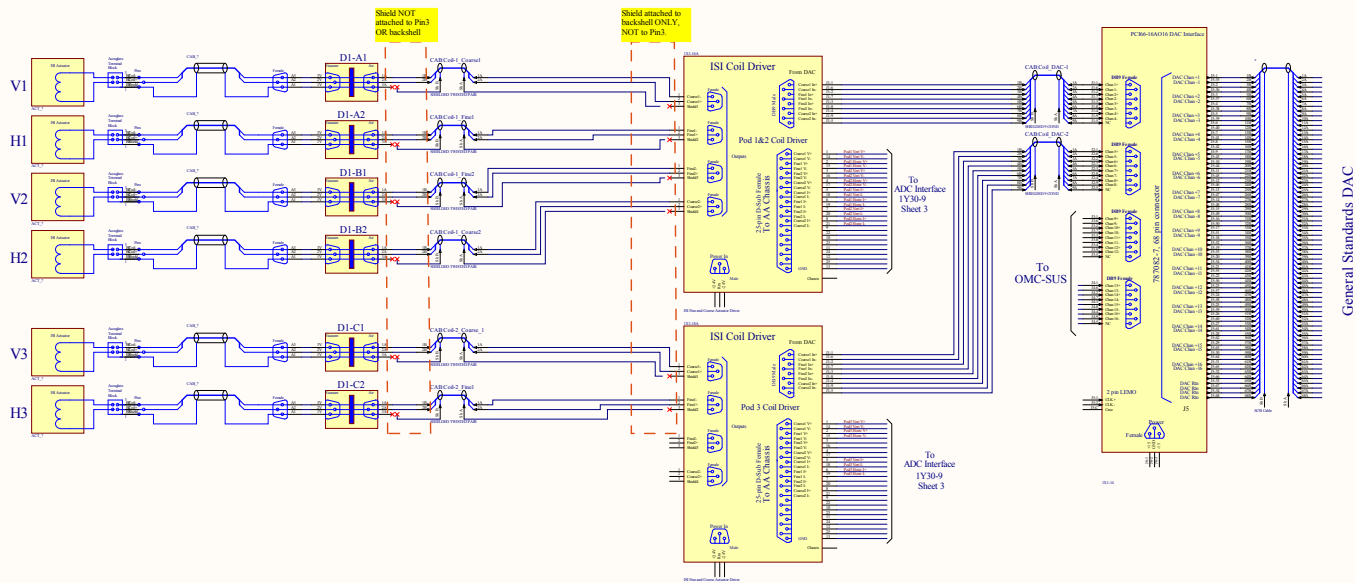
BSC AA Interface
Used for HAMs w/ FF

BSC AA Interface
Used for HAMs w/ FF

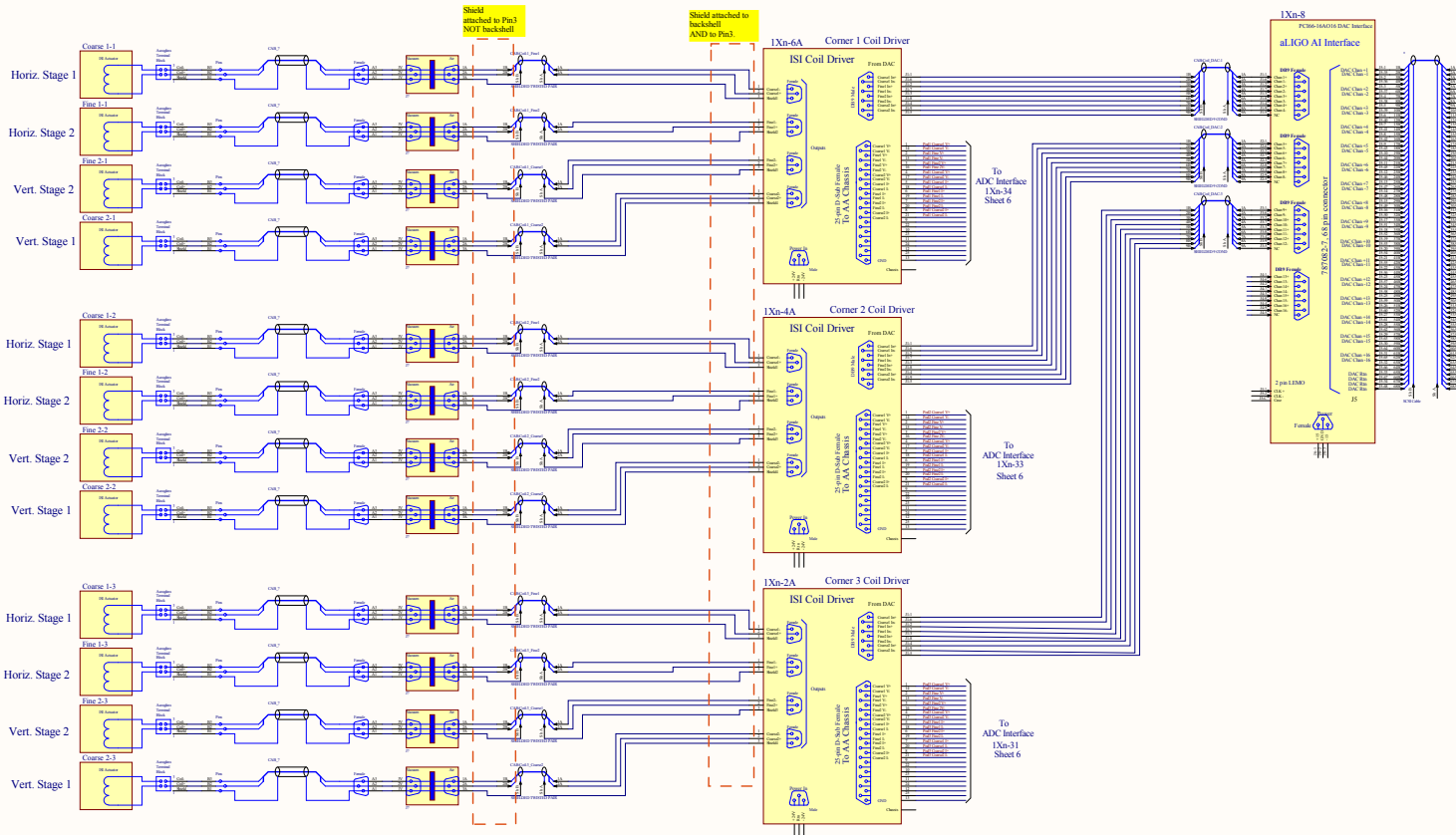
BSC AA Interface
Used for HAMs w/ FF



Rev	Number	Revision
1	1	1

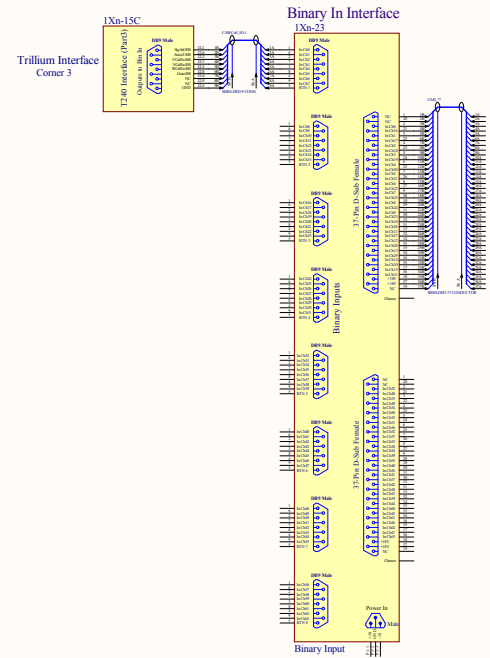
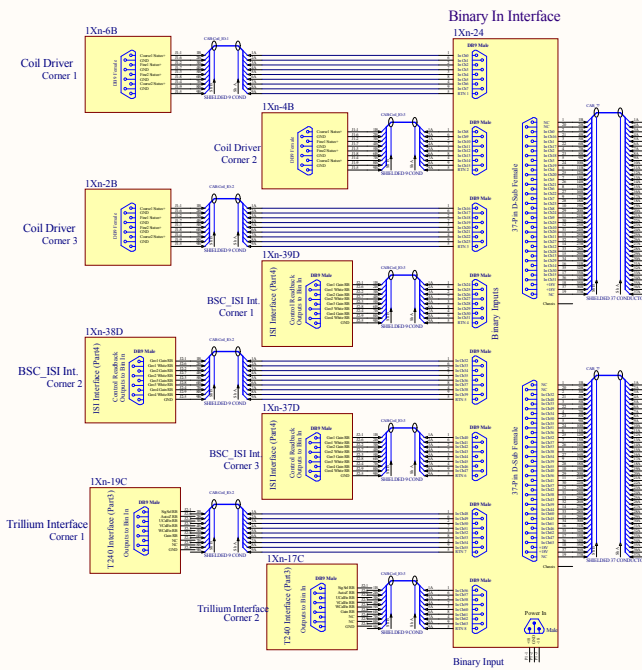
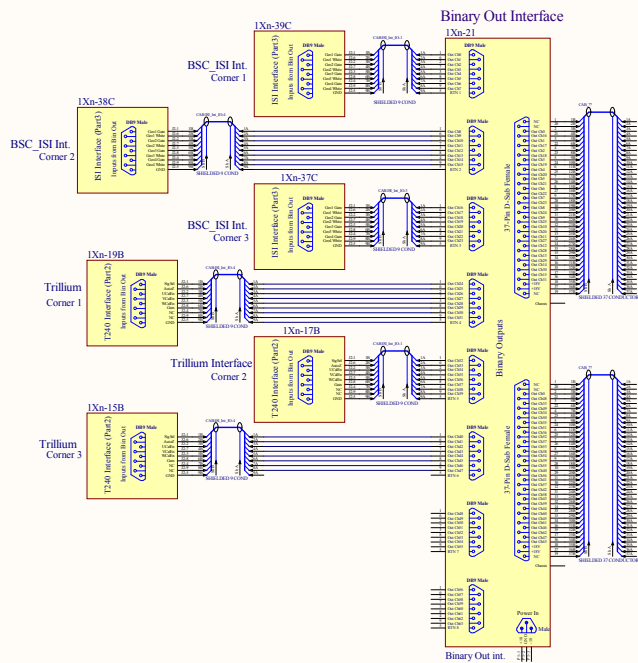


HAM Coil Drivers



BSC Coil Drivers

All of these cables can remain plugged in for both the HAM and the BSC.



For HAM Testing, Use only the connectors marked "S2" which are inside the boxes.

GS-13s S1 Corner1



GS-13s S1 Corner2



GS-13s S1 Corner3



GS-13s S2 Corner3



GS-13s S2 Corner3



GS-13s S2 Corner3



Coil S1 Corner1 H



Coil S1 Corner1 V



Coil S1 Corner2 H



Coil S1 Corner2 V



Coil S1 Corner3 H



Coil S1 Corner3 V



Coil S2 Corner1 H



Coil S2 Corner1 V



Coil S2 Corner2 H



Coil S2 Corner2 V



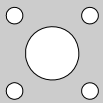
Coil S2 Corner3 H



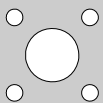
Coil S2 Corner3 V



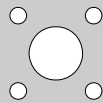
CPSI S1 Corner1 H



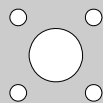
CPSI S1 Corner1 V



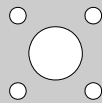
CPSI S1 Corner2 H



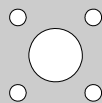
CPSI S1 Corner2 V



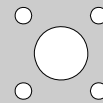
CPSI S1 Corner3 H



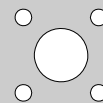
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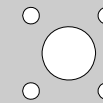
CPSI S2 Corner1 H



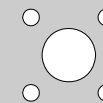
CPSI S2 Corner1 V



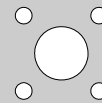
CPSI S2 Corner2 H



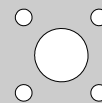
CPSI S2 Corner2 V



CPSI S2 Corner3 H



CPSI S2 Corner3 V



Chassis/Module	Quantity	Present?	Status
ISI Interface Chassis	3	No	Boards in production
BSC AA Interface Chassis	3	No	Chassis in production
Binary Input Chassis	2	No	Chassis in production
Binary Output chassis	1	No	Chassis in production
Trillium Interface Chassis	3	No	Boards in production
Anti-Image Chassis	1	No	Chassis in production
Coil Driver Chassis	3	2	Chassis in production
PCIE Expansion (Blue Box)	1	Yes	
Computer	1	Yes	
ADC Cards	3	2	
DAC Cards	1	1	
Binary IO Cards	3	No	