

PSL Controls Chassis		Left Rail						
Name	Color	Rail	Module	Number	Channel	Type	Adapter	Pin
Power								
24VInput	white	L	EK1501	0	24V	P	TBLOCK	
0VInput	gray	L	EK1501	0	0V	P	TBLOCK	
24VInput	white	L	EK1501	0	+	P	TBLOCK	
0VInput	gray	L	EK1501	0	-	P	TBLOCK	
Environmental								
H1:PSL-ENV_LASERRM_ACSTEMP(+V)out	white	L	EL3154	1	24V	AI	8	1
H1:PSL-ENV_LASERRM_ACSTEMP(current)in	violet	L	EL3154	1	I1	AI	8	14
H1:PSL-ENV_LASERRM_ACNTEMP(+V)out	white	L	EL3154	1	24V	AI	8	2
H1:PSL-ENV_LASERRM_ACNTEMP(current)in	violet	L	EL3154	1	I2	AI	8	15
H1:PSL-ENV_LASERRM_TBLNTEMP(+V)out	white	L	EL3154	1	24V	AI	8	3
H1:PSL-ENV_LASERRM_TBLNTEMP(current)in	violet	L	EL3154	1	I3	AI	8	16
H1:PSL-ENV_LASERRM_RH(current)in	blue	L	EL3154	1	I4	AI	8	4
H1:PSL-ENV_LASERRM_TBLSTEMP(+V)out	white	L	EL3154	2	24V	AI	8	5
H1:PSL-ENV_LASERRM_TBLSTEMP(current)in	violet	L	EL3154	2	I1	AI	8	18
H1:PSL-ENV_ANTERM_TEMP(+V)out	white	L	EL3154	2	24V	AI	8	6
H1:PSL-ENV_ANTERM_TEMP(current)in	violet	L	EL3154	2	I2	AI	8	19
H1:PSL-ENV_ANTERM_RH(current)in	blue	L	EL3154	2	I3	AI	8	7
H1:PSL-ENV_LVEA_TEMP(+V)out	white	L	EL3154	2	24V	AI	8	8
H1:PSL-ENV_LVEA_TEMP(current)in	violet	L	EL3154	2	I4	AI	8	21
H1:PSL-ENV_LASERRMTOANTERM_DPRES(+V)out	white	L	EL3154	3	24v	AI	8	9
H1:PSL-ENV_LASERRMTOANTERM_DPRES(current)in	yellow	L	EL3154	3	I1	AI	8	22
H1:PSL-ENV_ANTERMTOLVEA_DPRES(+V)out	white	L	EL3154	3	24V	AI	8	10
H1:PSL-ENV_ANTERMTOLVEA_DPRES(current)in	yellow	L	EL3154	3	I2	AI	8	23
H1:PSL-ENV_DIODERM_TEMP(+V)out	white	L	EL3154	3	24V	AI	9	1
H1:PSL-ENV_DIODERM_TEMP(current)in	purple	L	EL3154	3	I3	AI	9	14
H1:PSL-ENV_DIODERM_RH(current)in	blue	L	EL3154	3	I4	AI	9	2
H1:PSL-ENV_CHILLERRM_TEMP(+V)out	white	L	EL3154	4	24V	AI	9	3
H1:PSL-ENV_CHILLERRM_TEMP(current)in	purple	L	EL3154	4	I1	AI	9	16
H1:PSL-ENV_CHILLERRM_RH(current)in	blue	L	EL3154	4	I2	AI	9	4
H1:PSL-ENV_DIODERMTOCHILLERRM_DPRES(+V)out	white	L	EL3154	4	24V	AI	9	5
H1:PSL-ENV_DIODERMTOCHILLERRM_DPRES(current)in	yellow	L	EL3154	4	I3	AI	9	18
(Empty)	white	L	EL3154	4	24V	AI	9	6
(Empty)	yellow	L	EL3154	4	I4	AI	9	19