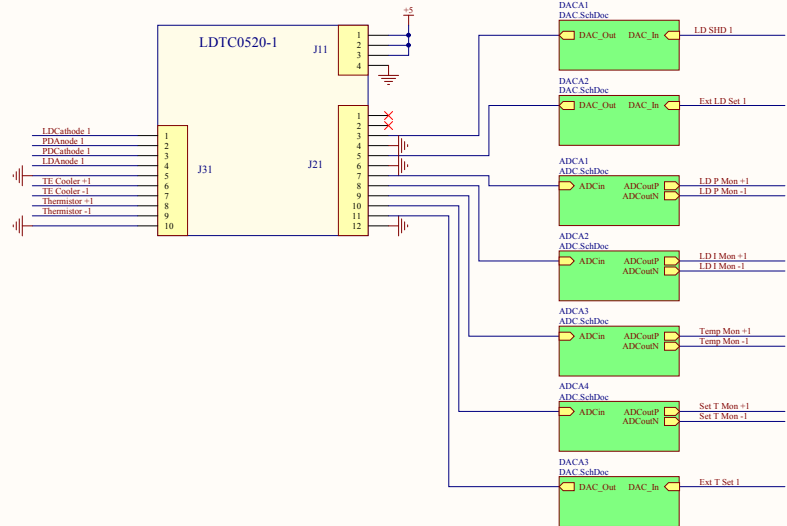
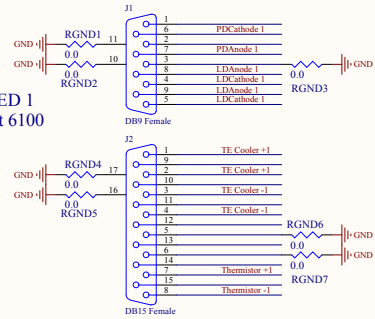
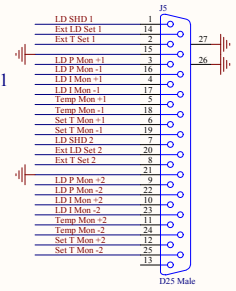


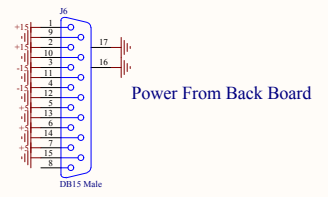
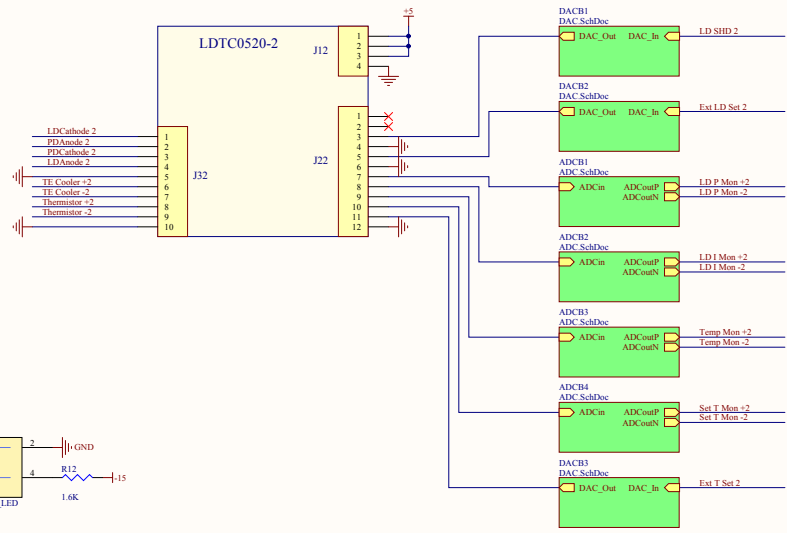
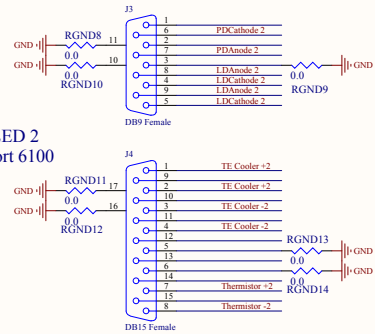
To SLED 1
Newport 6100



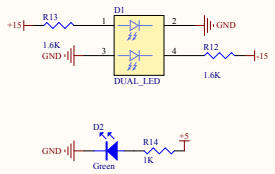
To SLED 1
From Back Board
To SLED 2



To SLED 2
Newport 6100



Power From Back Board



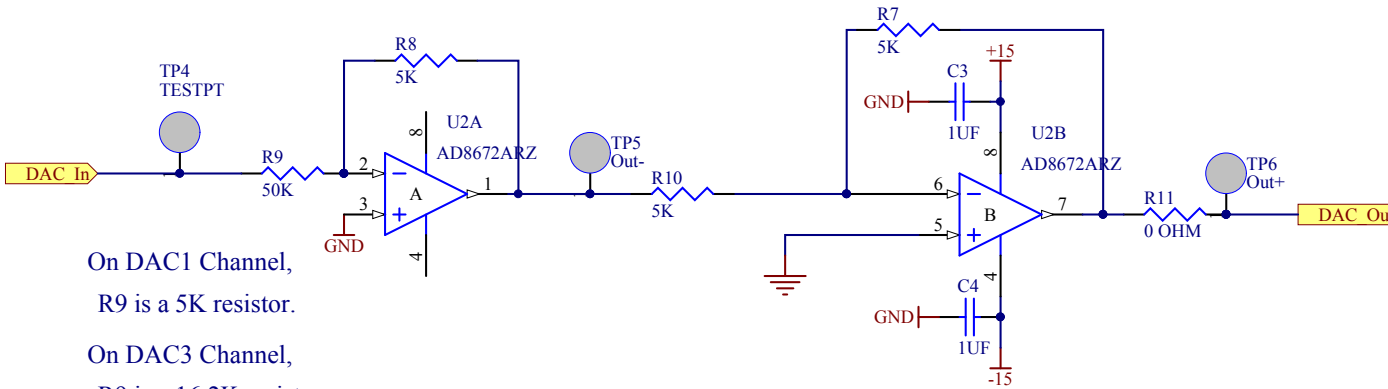
Divide by 10 Inverting stage

Only on DAC Channel 2.

DAC1 is a TTL channel,
so needs inverting unity gain.

DAC3 needs a gain of -0.3.

Re-Inverting stage



On DAC1 Channel,
R9 is a 5K resistor.

On DAC3 Channel,
R9 is a 16.2K resistor.

Title **SLED Receiver**

Size: **A**

DCC Number: **D1200600**

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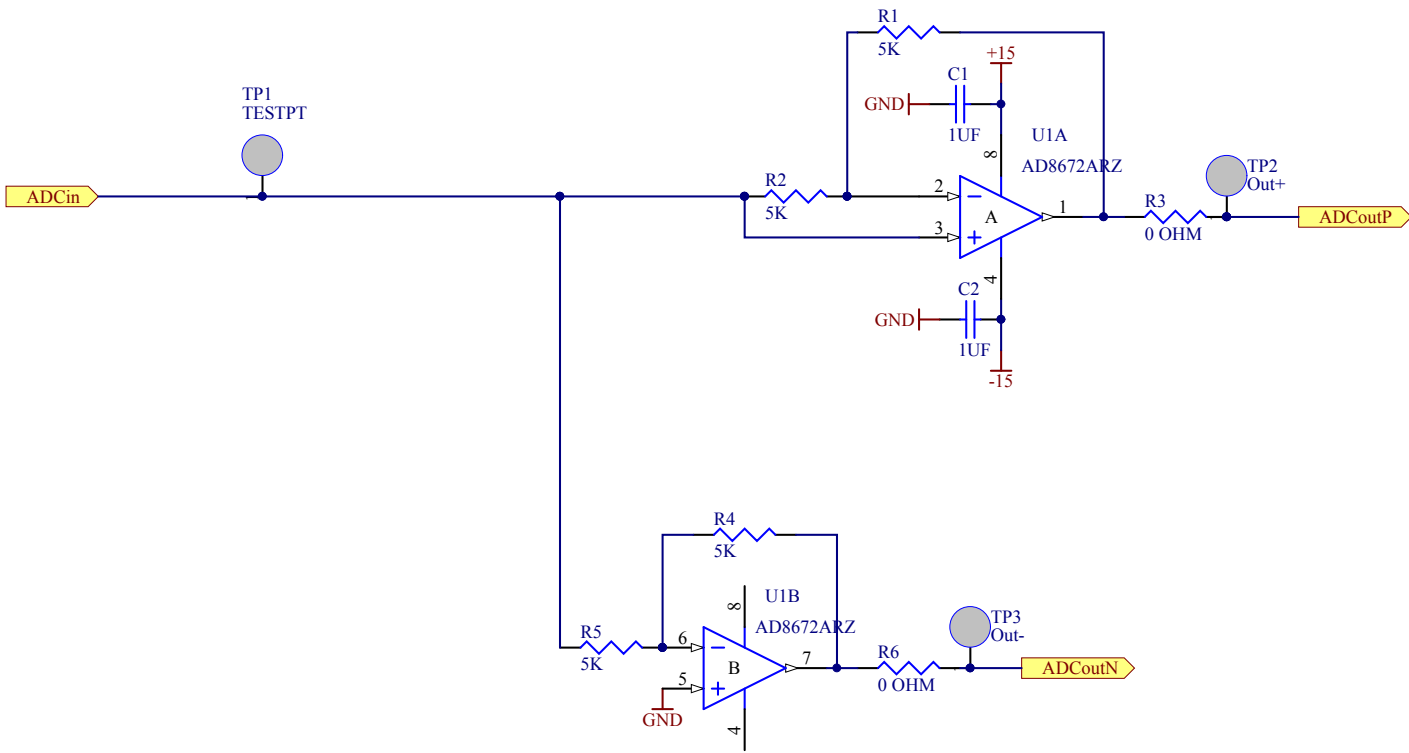


Drawn by:
Ben Abbott

Date: **5/8/2014**

Revision: **v2**

File: C:\restored\Ben\AOS\TCS Stuff\TCS ISS Chassis\SLED Driver\SLD\Driver_349_25RM_Schem2 of 3



Title **SLED Driver**

Size: **A**

DCC Number: **D1200600**

*Ligo Project
California Institute of Technology
Massachusetts Institute of Technology*



Drawn by:
Ben Abbott

Date: **5/8/2014**

Revision: **v2**

File: C:\restored\Ben\AOS\TCS Stuff\TCS ISS Chassis\SLED Driver\SLED Driver 349.25 DM Sheet 3 of 3

