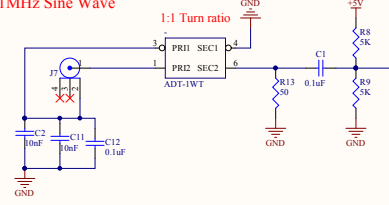
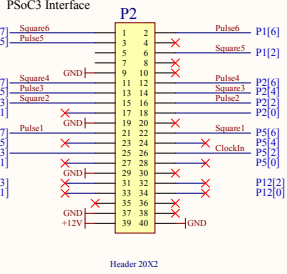
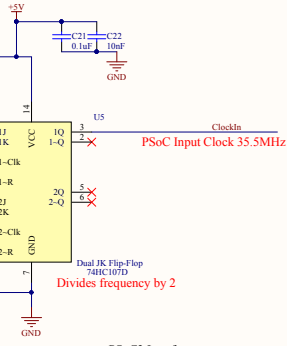


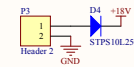
71MHz Sine Wave



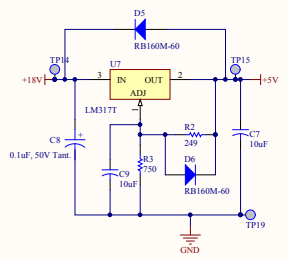
Biases the signal to 2.5V



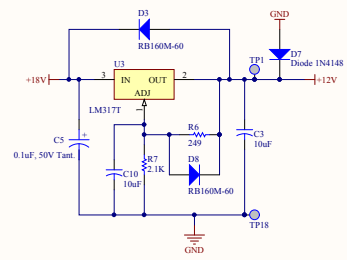
Power In From Breaker



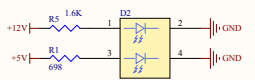
5 V Voltage Regulator



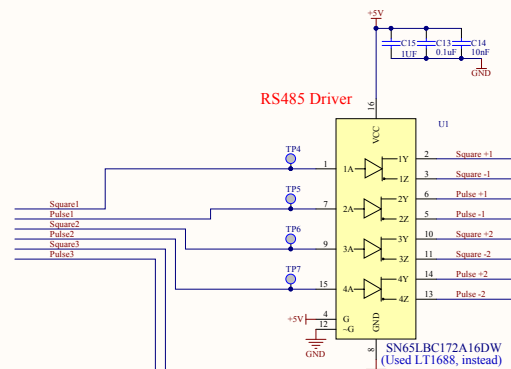
12V Voltage Regulator



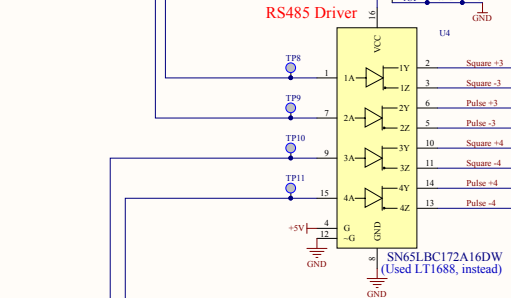
Board Power



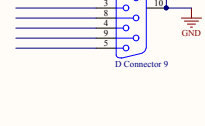
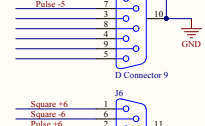
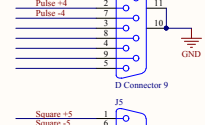
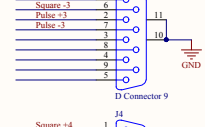
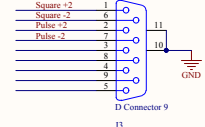
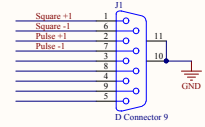
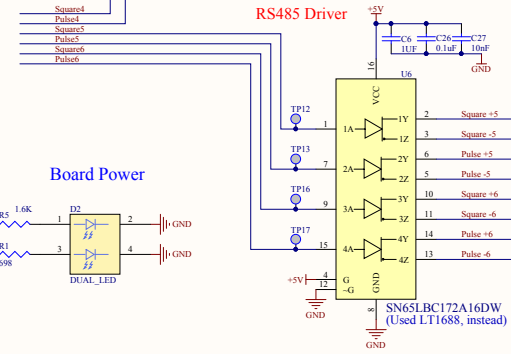
RS485 Driver



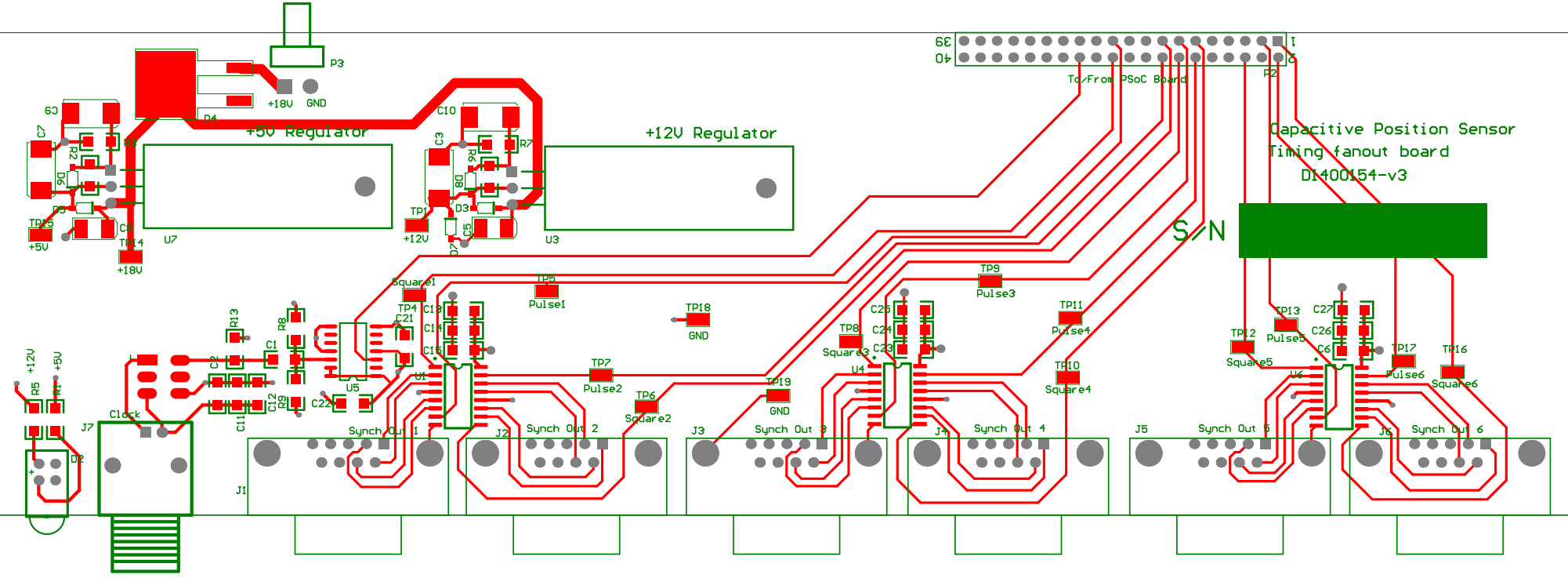
RS485 Driver



RS485 Driver



Title		<b>Capacitive Position Sensor Timing Fanout Board</b>	
Size:	C	DCC Number:	D1400154
Drawn by:	Ben Abbott	Date:	6/23/2014
File:	C:\Created\Ben\Scienc\ISI Cap\PosSensorPower\CPS_Synch_Fanout_Pwr_41631715_Synch_Tank413_Sch.doc	Revision:	v3
Ligo Project		California Institute of Technology Massachusetts Institute of Technology	



Capacitive Position Sensor  
Timing fanout board  
DI400154-v3

S/N

To/From PSoc Board

+5V Regulator

+12V Regulator

Synch Out 1

Synch Out 2

Synch Out 3

Synch Out 4

Synch Out 5

Synch Out 6

Square1

Square2

Square3

Square4

Square5

Square6

Pulse1

Pulse2

Pulse3

Pulse4

Pulse5

Pulse6

TP4

TP7

TP19

TP8

TP11

TP12

TP13

TP17

TP16

TP1

TP5

TP6

TP9

TP3

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