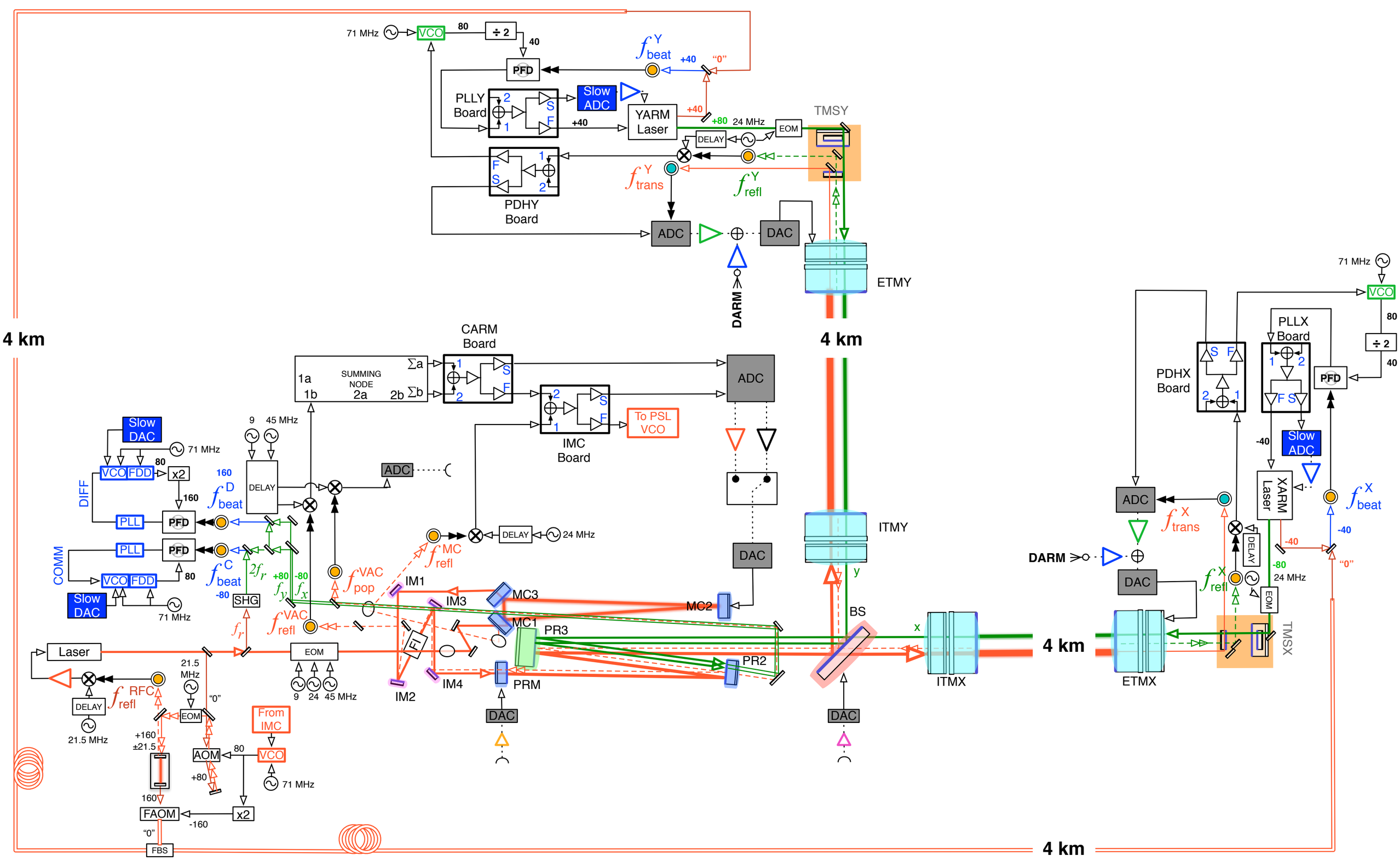
71 MHz
VCO
80
÷ 2
40
PFD
f^{Y}_{beat}
+40
"0"
PLLY Board
Slow ADC
YARM Laser
+40
+80
24 MHz
EOM
TMSY
DELAY
f^{Y}_{trans}
f^{Y}_{refl}
PDHY Board
ADC
DAC
DARM
ETMY
4 km
4 km
CARM Board
1a
1b
SUMMING NODE
2a
2b
Σa
Σb
IMC Board
To PSL VCO
ADC
Slow DAC
71 MHz
VCO
FDD
80
x2
160
DIFF
PLL
PFD
f^{D}_{beat}
DELAY
9
45 MHz
COMM
f^{C}_{beat}
-80
SHG
$2f_r$
+80
-80
f_y
f_x
f_r
f^{VAC}_{pop}
f^{VAC}_{refl}
f^{MC}_{refl}
DELAY
24 MHz
IM1
IM3
IM2
IM4
FI
MC3
MC2
MC1
PR3
PR2
PRM
DAC
ITMY
y
BS
x
ITMX
4 km
Laser
EOM
9
24
45 MHz
21.5 MHz
f^{RFC}_{refl}
DELAY
21.5 MHz
EOM
"0"
+160
±21.5
160
AOM
80
+80
From IMC
VCO
71 MHz
FAOM
-160
x2
"0"
FBS
4 km
71 MHz
VCO
80
÷ 2
40
PDHX Board
PLLX Board
PFD
-40
Slow ADC
XARM Laser
f^{X}_{beat}
-40
"0"
ADC
f^{X}_{trans}
DARM
DELAY
-40
-80
24 MHz
DAC
f^{X}_{refl}
EOM
TMSX
ETMX