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ADC and DAC Channel Usage for SQZ

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1 Purpose

This document lists the specific ADC and DAC channels used within the SQZ I/O expansion chassis. In the following tables, the entries given in the ‘Signal’ column are *not* meant to be the exact DAQ channel name for that signal (though they may be); rather the entries are intended as descriptors to identify the actual hardware channel that is connected to a given ADC/DAC channel.

2 SUSH56 I/O Chassis

Card	AA/AI conn.	ADC/DAC Chs.	Signal	
DAC 5	DB9_1	1-4	SQZ TT1 Coil drives (UL, LL, UR, LR)	
	DB9_2	5-8	SQZ TT2 Coil drives (UL, LL, UR, LR)	
	DB9_3	9-12	VOPO SUS coil drivers (H1, H2, H3, V1, V2, V3)	
		13-14		
		DB9_4	15	Unused
			16	Unused

Card	AA/AI conn.	ADC/DAC Chns.	Signal	
ADC 2	DB9_1	1-4	SQZ TT1	Tip-Tilt BOSEM sensor signals
	DB9_2	5-8	SQZ TT2	
	DB9_3	9-12	SQZ TT1	Tip-Tilt Coil driver readbacks
	DB9_4	13-16	SQZ TT1	
	DB9_5	17-20	VOPO SUS	AOSEM
	DB9_6	21-22		
		23	Unused	
		24	Unused	
	DB9_7	25-28	VOPO SUS	Coil driver readbacks
	DB9_8	29-30		
		31	Unused	
		32	Unused	

3 SQZ-IO I/O Chassis

Card	AA/AI conn.	ADC/DAC Chs.	Signal
DAC 0	DB9_1	1	SQZ-EXTRA_AO_1
		2	SQZ-EXTRA_AO_2
		3	SQZ-EXTRA_AO_3
		4	SQZ-OPO-PZT
	DB9_2	5	SQZ-OPO_SERVO_EXC
		6	SQZ-SHG_SERVO_EXC
		7	SQZ-LO_SERVO_EXC
		8	SQZ-CLF_SERVO_EXC
	DB9_3	9-12	Unused
	DB9_4	13-14	Unused
		15	Unused
		16	Duotone

Card	AA/AI conn.	ADC/DAC Chns.	Signal			
ADC 0	DB9_1	1	SQZ-WFS_A_RF	Seg 1	Q-phase	
		2			I-phase	
		3		Seg 2	Q-phase	
		4			I-phase	
	DB9_2	5		SQZ-WFS_B_RF	Seg 3	Q-phase
		6				I-phase
		7			Seg 4	Q-phase
		8				I-phase
	DB9_3	9	SQZ-WFS_B_RF		Seg 1	Q-phase
		10				I-phase
		11			Seg 2	Q-phase
		12				I-phase
	DB9_4	13		SQZ-WFS_B_RF	Seg 3	Q-phase
		14				I-phase
		15			Seg 4	Q-phase
		16				I-phase
	DB9_5	17	SQZ-LL_MIXER			
		18	SQZ-LL_PZT			
		19	SQZ-LL_EOMRMS			
		20	SQZ-LL_SLOW			
	DB9_6	21	SQZ-HD_DIFF	RF3	Q-phase	
		22			I-phase	
		23	SQZ-OMC_TRANS	RF3	Q-phase	
		24			I-phase	
	DB9_7	25	SQZ-SHG_TRANS	RF35	Q-phase	
		26			I-phase	
		27	SQZ-OPO_REFL	RF80	Q-phase	
		28			I-phase	
	DB9_8	29	SQZ-CLF_REFL	RF6	Q-phase	
		30			I-phase	
		31	Duotone (DAC)			
		32	Duotone			

Card	AA/AI conn.	ADC/DAC Chns.	Signal	
ADC 1	DB9_1	1	SQZ-OPO_SERVO_ERR, CM Servo, I monitor	
		2	SQZ-OPO_SERVO_CTRL, CM Servo, Fast monitor	
		3	SQZ-OPO_SERVO_SLOW, CM Servo, Slow monitor	
		4	Unused	
	DB9_2	5	SQZ-SHG_SERVO_ERR, CM Servo, I monitor	
		6	SQZ-SHG_SERVO_CTRL, CM Servo, Fast monitor	
		7	SQZ-SHG_SERVO_SLOW, CM Servo, Slow monitor	
		8	Unused	
	DB9_3	9	SQZ-LO_SERVO_ERR, CM Servo, I monitor	
		10	SQZ-LO_SERVO_CTRL, CM Servo, Fast monitor	
		11	SQZ-LO_SERVO_SLOW, CM Servo, Slow monitor	
		12	Unused	
	DB9_4	13	SQZ-CLF_SERVO_ERR, CM Servo, I monitor	
		14	SQZ-CLF_SERVO_CTRL, CM Servo, Fast monitor	
		15	SQZ-CLF_SERVO_SLOW, CM Servo, Slow monitor	
		16	Unused	
	DB9_5	17	SQZ-EXTRA_AI_1	
		18	SQZ-EXTRA_AI_2	
		19	SQZ-EXTRA_AI_3	
		20	SQZ-HD_DIFF_DC	
	DB9_6	21	SQZ-CLF_REFL_LF	DC Outputs
		22	SQZ-OPO_REFL_LF	
		23	SQZ-HD_A_DC	
		24	SQZ-HD_B_DC	
	DB9_7	25	SQZ-SHG_FIBR_TRANS_LF	DC Outputs
		26	SQZ-CLF_FIBR_TRANS_LF	
		27	SQZ-OPO_TRANS_LF	
		28	SQZ-LL_IR_LF	
	DB9_8	29	SQZ-LL_PD_LF	4 ch Generic PD interface: ISCT6
		30	SQZ-SHG_TRANS_LF	
		31	SQZ-LASER_IR_LF	
		32	SQZ-SHG_GR_LF	

Card	AA/AI conn.	ADC/DAC Chns.	Signal	
ADC 2	DB9_1	1	Unused	
		2	Unused	
		3	Unused	
		4	Unused	
	DB9_2	5	Unused	4 ch Generic PD interface: SQZT6
		6	Unused	
		7	Unused	
		8	Unused	
	DB9_3	9	SQZ-LL_FIBR_REJECTED_LF	DC Monitors
		10	SQZ-LL_FIBR_TRANS_LF	
		11	SQZ-SHG_LAUNCH_LF	
		12	SQZ-CLF_LAUNCH_LF	
	DB9_4	13	SQZ-SEED_LAUNCH_LF	DC Monitors
		14	SQZ-LO_LAUNCH_LF	
		15	Unused	
		16	OPO_REFL_REJECTED_LF	
	DB9_5	17	SQZ-SHG_FIBR_REJECTED_LF	DC Monitors
		18	SQZ-CLF_FIBR_REJECTED_LF	
		19	SQZ-CLF_REJECTED_LF	
		20	SQZ-SHG_REJECTED_LF	
	DB9_6	21	Unused	
		22	Unused	
		23	Unused	
		24	Unused	
	DB9_7	25	Unused	
		26	Unused	
		27	Unused	
		28	Unused	
	DB9_8	29	Unused	
		30	Unused	
		31	Unused	
		32	Unused	

4 Summary

Below is a summary of the number of I/O cards, unused channels, and available I/O slots for the SQZ I/O Expansion Chassis. This assumes there are a total of 10 slots available in the I/O chassis for ADC and/or DAC cards. For the unused ADC channel column, the number in parentheses is the subset of these channels that are available Anti-Alias (AA) chassis on free DB9 connectors; the other channels are found on AA DB9 connectors which are only partially used.

I/O Chassis	# ADC cards	# DAC cards	Unused ADC chans	Unused DAC chans	Available I/O slots
SQZ/LSC	3	1			
SUSHAM56	1	1			
Totals	4	2			