

- ### Mounting Hardware
- Part1: Mounting Screw/Nut
 - Part2: Washer
 - Part3: Washer
 - Part4: Mounting Screw/Nut
 - Part5: Washer
 - Part6: Washer
 - Part7: Mounting Screw/Nut
 - Part8: Washer
 - Part9: Washer
 - Part10: Mounting Screw/Nut
 - Part11: Washer
 - Part12: Washer
 - Part13: Mating Connector

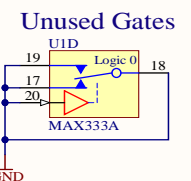
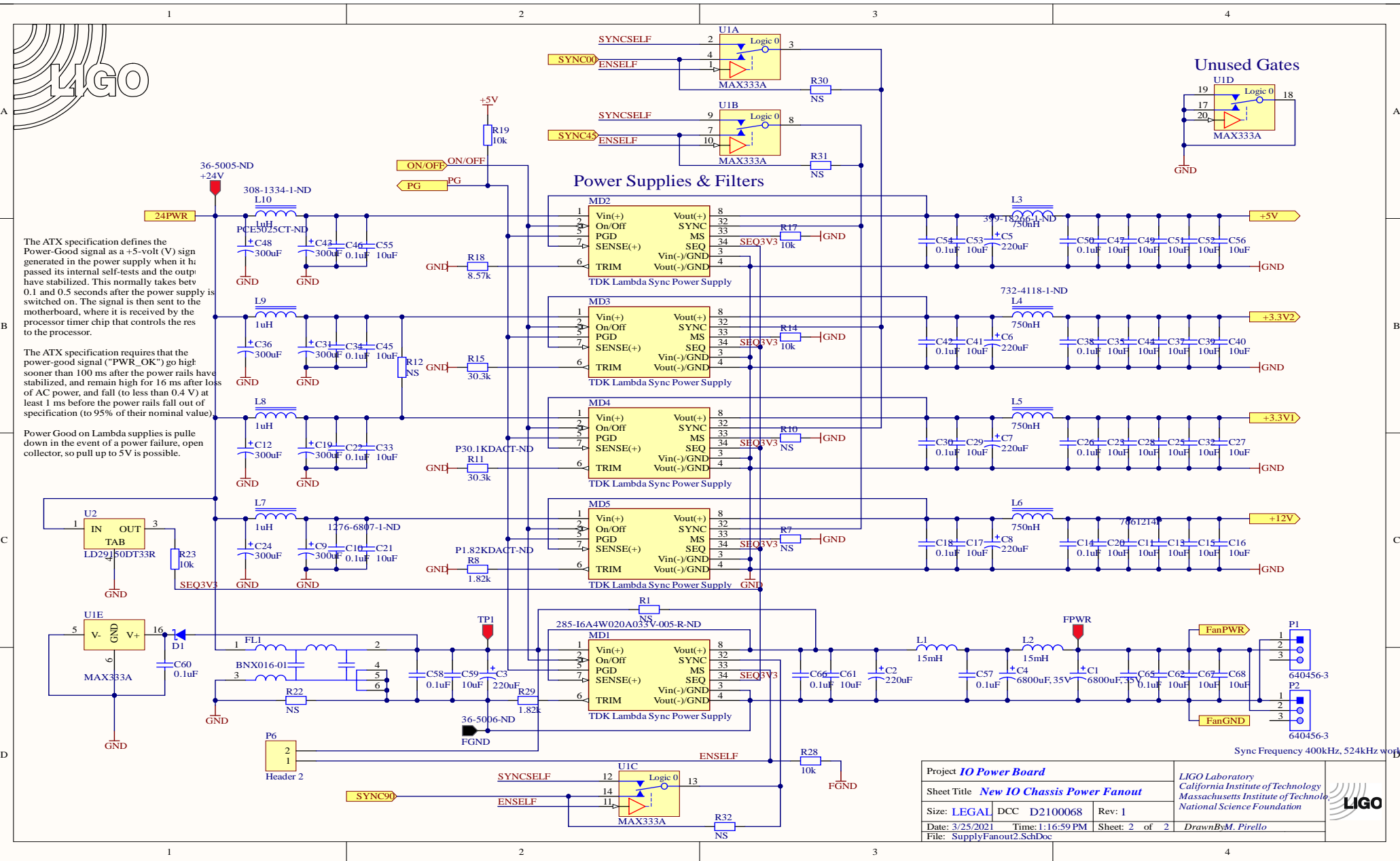
Project IO Power Board			LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology National Science Foundation	
Sheet Title New IO Chassis Power Fanout			LIGO	
Size: LEGAL	DCC D2100068	Rev: 1		
Date: 3/25/2021	Time: 1:16:58 PM	Sheet: 1 of 2	Drawn By: M. Pirello	
File: SupplyFanout1.SchDoc				



The ATX specification defines the Power-Good signal as a +5-volt (V) sign generated in the power supply when it has passed its internal self-tests and the output have stabilized. This normally takes betw 0.1 and 0.5 seconds after the power supply is switched on. The signal is then sent to the motherboard, where it is received by the processor timer chip that controls the res to the processor.

The ATX specification requires that the power-good signal ("PWR_OK") go high sooner than 100 ms after the power rails have stabilized, and remain high for 16 ms after loss of AC power, and fall (to less than 0.4 V) at least 1 ms before the power rails fall out of specification (to 95% of their nominal value).

Power Good on Lambda supplies is pulled down in the event of a power failure, open collector, so pull up to 5V is possible.



Power Supplies & Filters

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Sheet Title New IO Chassis Power Fanout			
Size: LEGAL	DCC D2100068	Rev: 1	
Date: 3/25/2021	Time: 1:16:59 PM	Sheet: 2 of 2	
File: SupplyFanout2.SchDoc			Drawn By: M. Pirello

Sync Frequency 400kHz, 524kHz works

